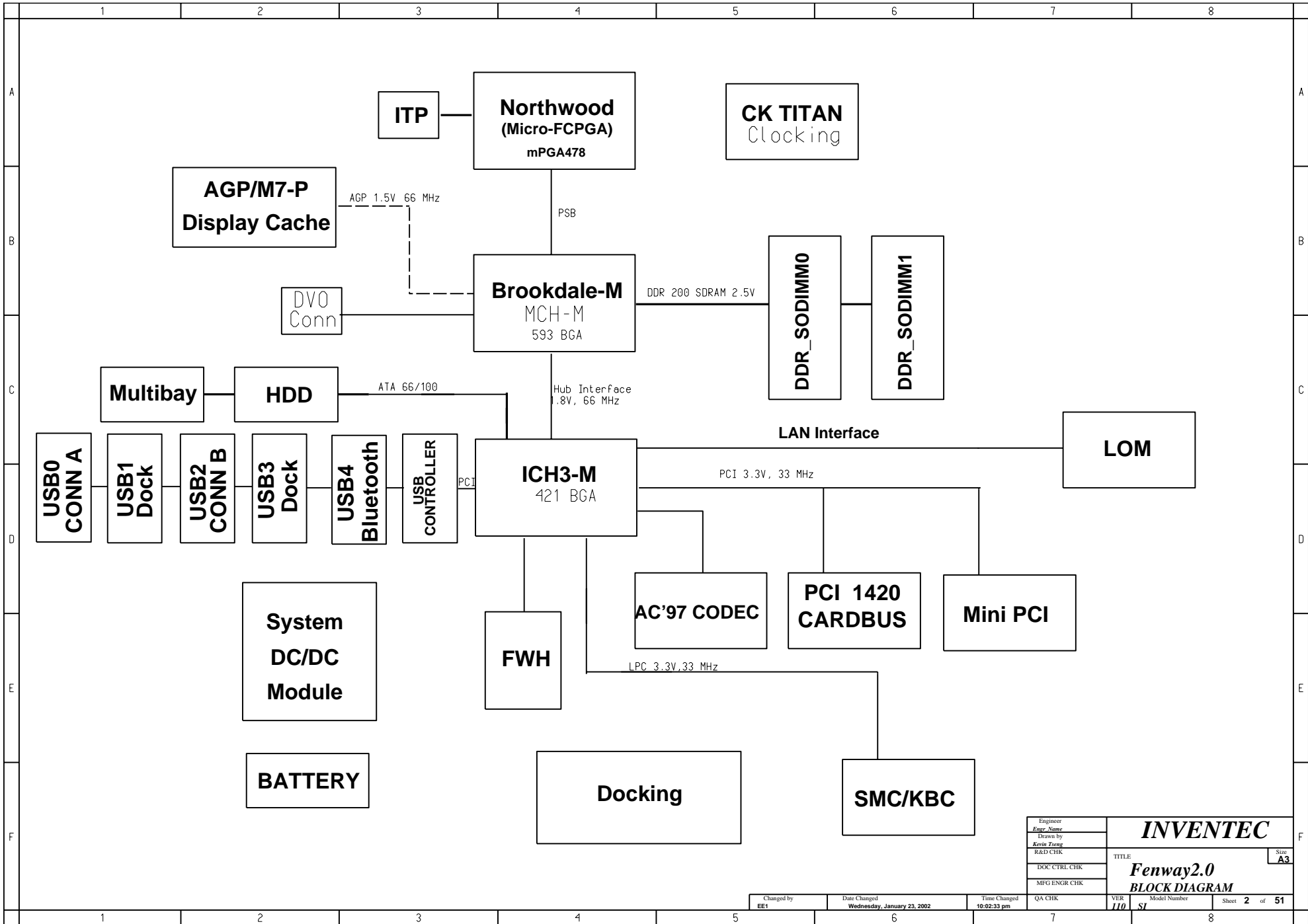


TABLE OF CONTENTS

Revisions			Approvals			
REV	ECN NO.	Description Of Change	DATE	DFTG.	ENGR.	REL.

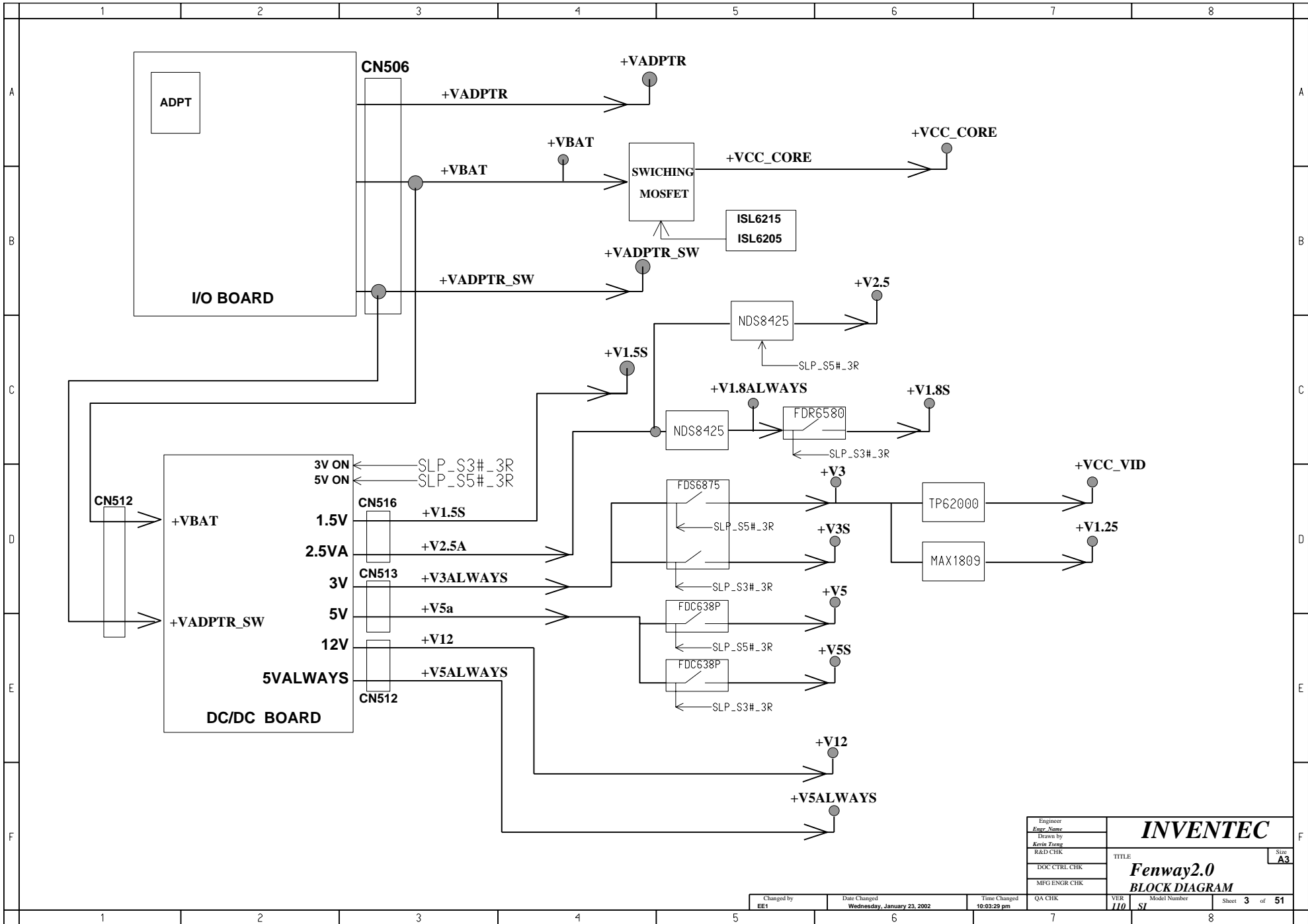
PAGE		PAGE	
4	- CLOCK GENERATOR	32	- DUALBAY
5-7	- NORTHWOOD PROCESSOR	33	- INT KBD./ POINTING DEVICE
8-10	- BROOKDALE	34	- LID SWITCH/LEDS
11-14	- DDR	35	- DOCKING CONNCTOR
15,16	- ATI_M7_P	36	- MINI PCI
17	- VIDEO RAM	37	- USB, IR
18	- CRT,LCD,INVERTER CONNECTOR INTERFACE	38	- BLUETOOTH
19,20	- ICH3-M	39	- THERMAL SENSOR & FAN CONTROLLER
21	- KAHUNA	40	- ADAPTER SENSE CIRCUIT & CLAMP CIRCUIT
22	- FWH & EEPROM	41	- PWR MODULE CONN
23	- SERIAL , PARALLEL AND PS2 PORTS	42	- DECOUPLING CAPS
24	- CARDBUS CONTROLLER	43	- DRILL HOLE
25	- PC CARD SLOT	44	- HIGH SPEED
26	- AC97 CODEC	45	- PULL UPS
27	- EQ & MIC JACK	46	- LAN INTERFACE
28	- AUDIO AMP & HP JACK	47-49	- POWER
29	- HDD AND MB1 PWR AND SWITCH	50	- USB2.0
30	- HDD CONNECTOR	51	- IO BOARD
31	- MULTIBAY		

Engineer <i>Engr Name</i>	INVENTEC
Drawn by <i>Kevin Tseng</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE Fenway2.0	Size A3
TABLE OF CONTENTS	



Engineer		INVENTEC
Engr Name		
Drawn by	Kevin Tseng	
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		TITLE: Fenway2.0 BLOCK DIAGRAM
		Size: A3

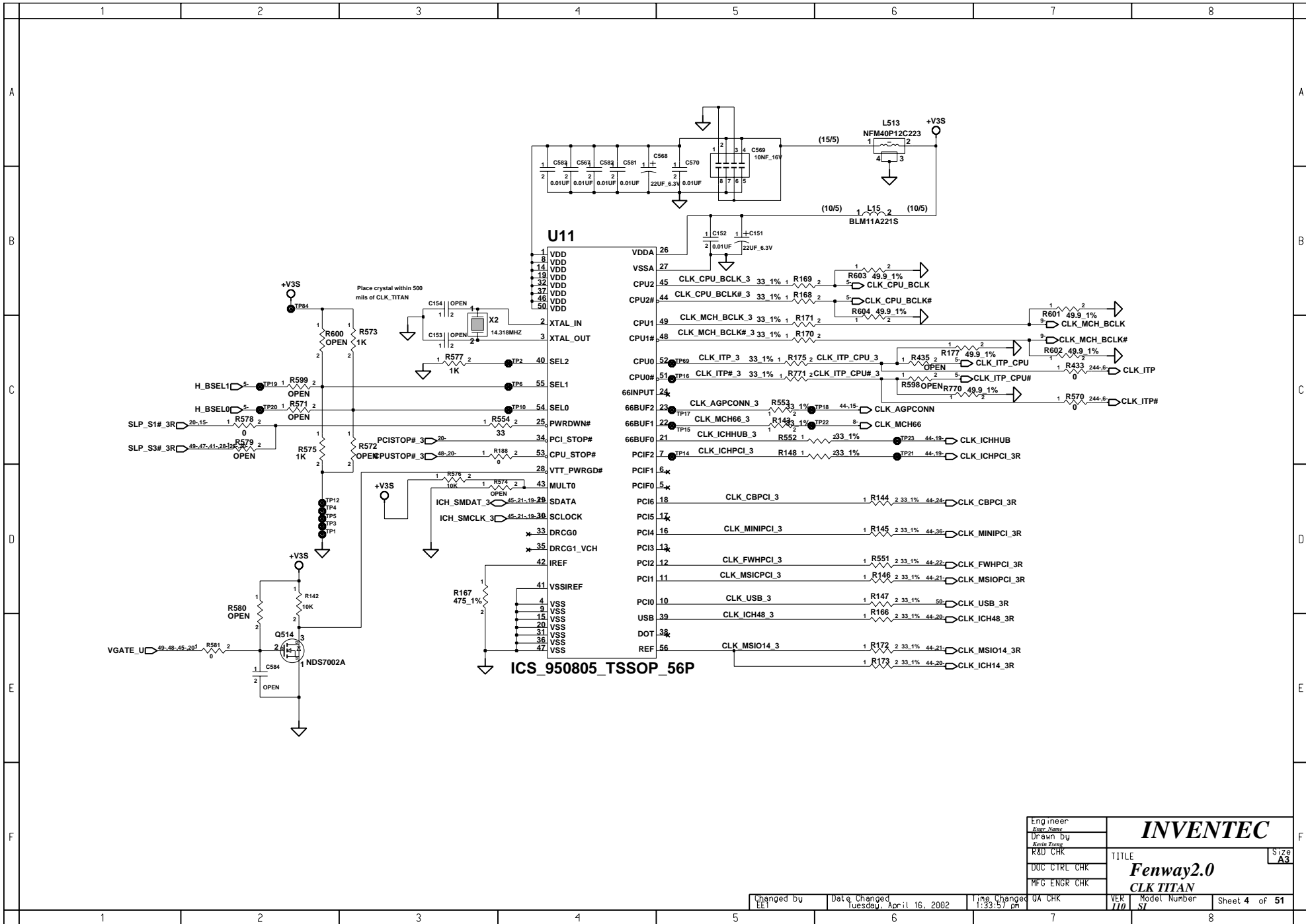
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of	51
EE1	Wednesday, January 23, 2002	10:02:33 pm		110	SI	2		



Engineer	
Engr Name	
Drawn by	Kevin Tseng
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Fenway2.0
	BLOCK DIAGRAM
Size	A3

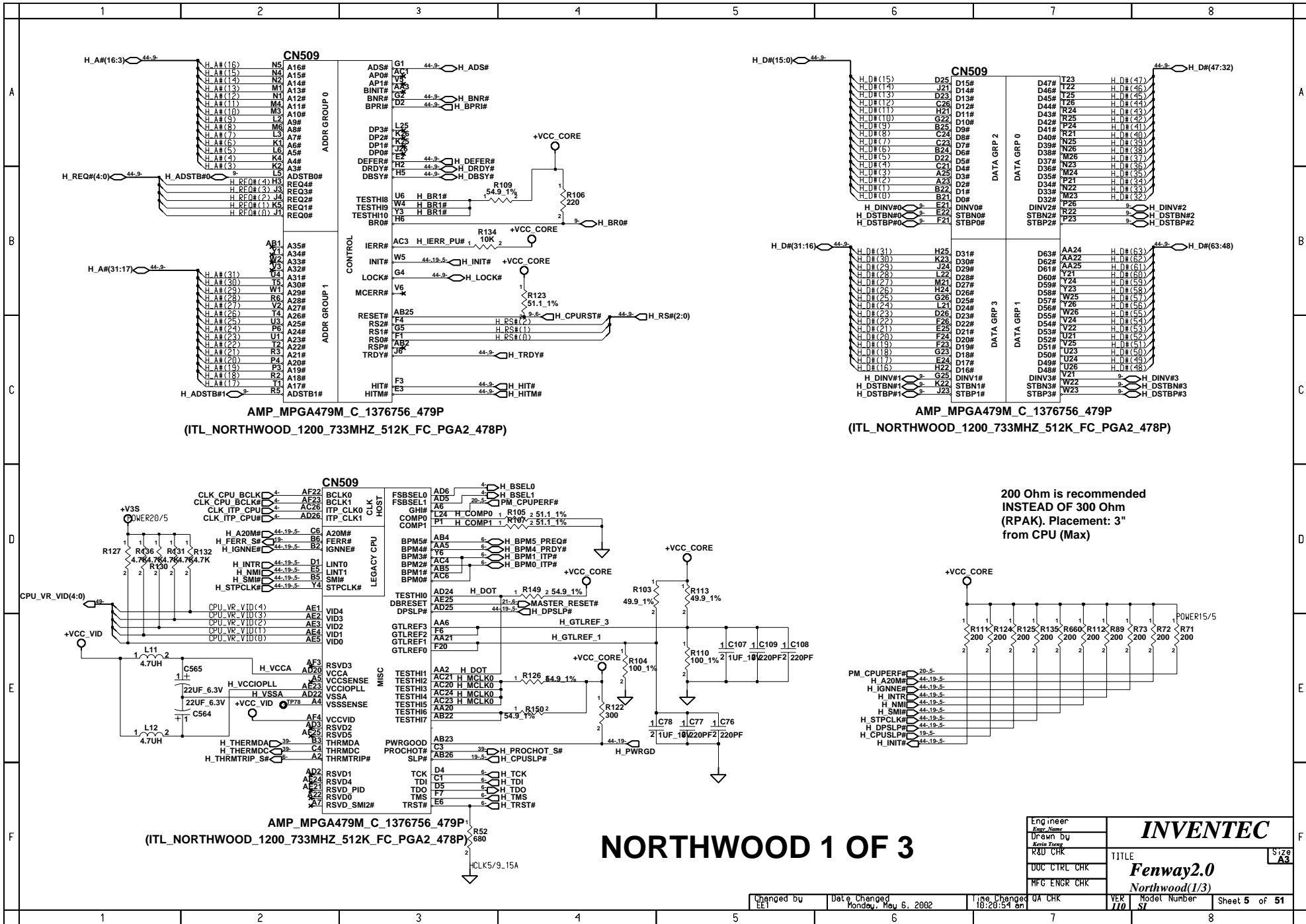
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of	51
EE1	Wednesday, January 23, 2002	10:03:29 pm		110	SI	3		

INVENTEC



Engineer		INVENTEC TITLE Fenway2.0 CLK TITAN S178 A3
Drawn by		
Rev'd		
DOC CTRL		
MFG ENGR		

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet 4 of 51
EE1	Tuesday, Apr 16, 2002	1:33:57 pm		110	S1	



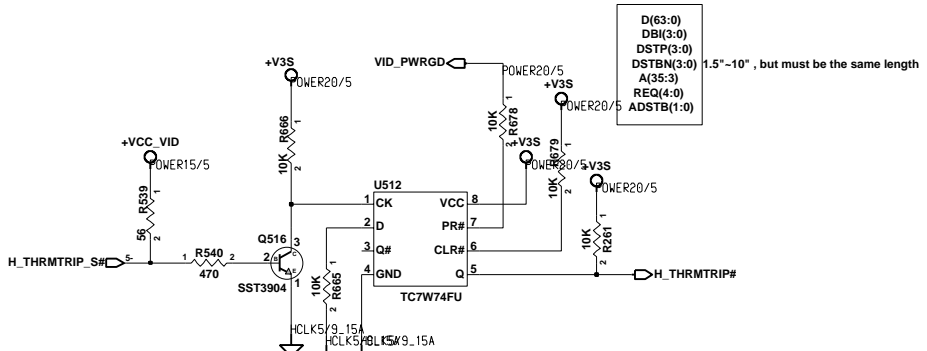
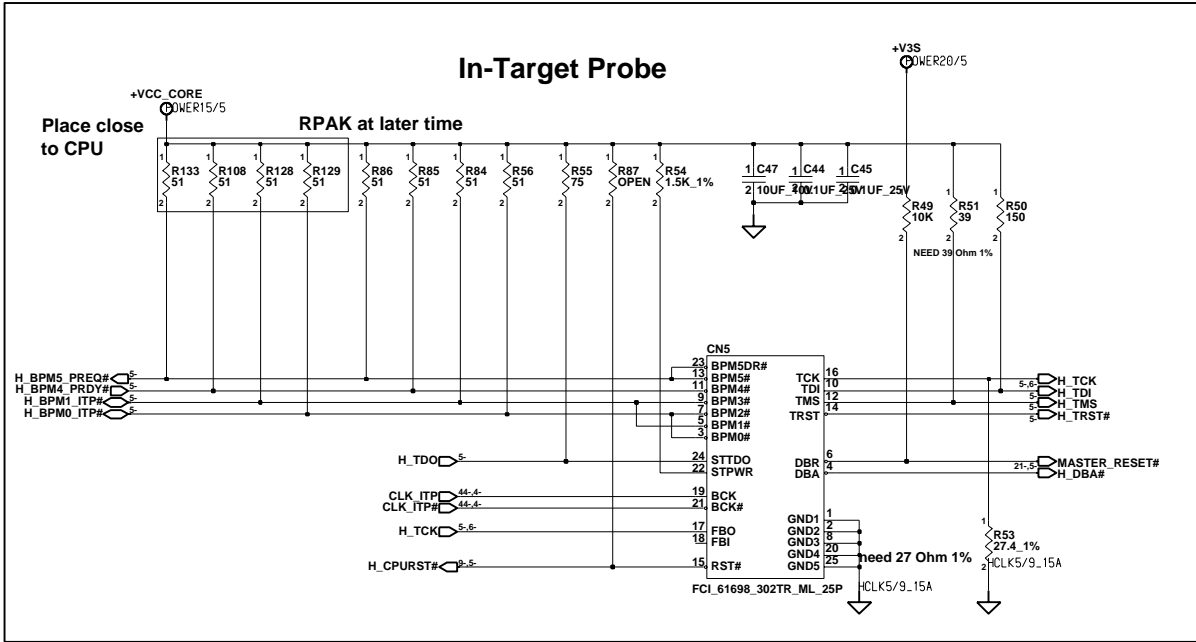
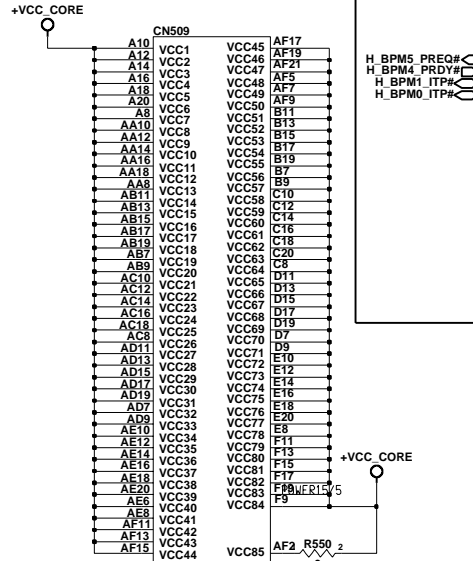
200 Ohm is recommended
INSTEAD OF 300 Ohm
(RPAK). Placement: 3"
from CPU (Max)

NORTHWOOD 1 OF 3

Eng Ineer		INVENTEC
Drawn By		
Revise		
R3D CHK		TITLE
DOC CTRL CHK		Fenway2.0
MFG ENGR CHK		Northwood(1/3)

Changed by	Date Changed	Time Changed	UA CHK	VER	Model Number	Sheet 5 of 51
EE1	Monday, Nov 6, 2002	10:20:54 am		110	SI	

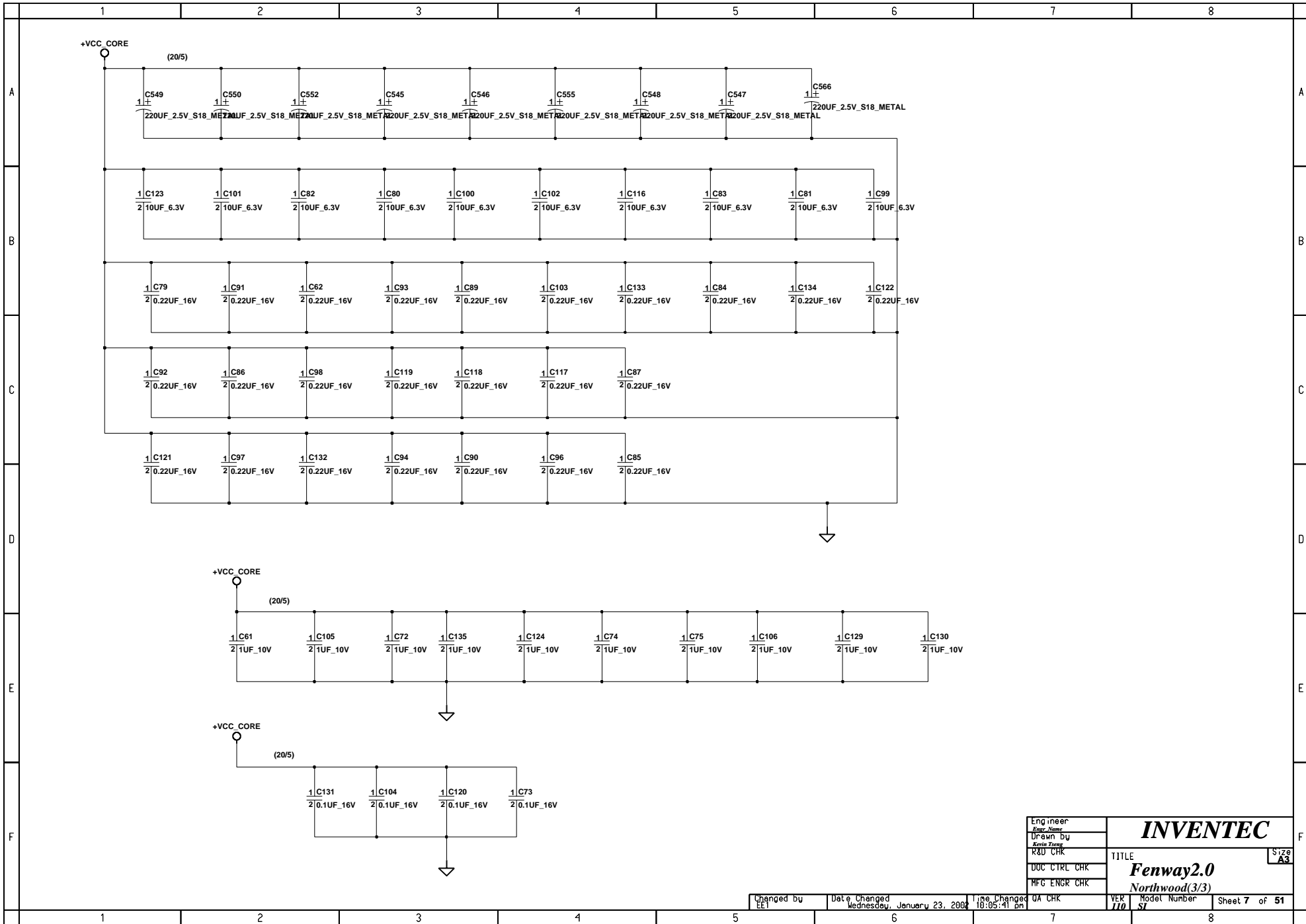
CN509		
H1	VSS1	VSS92
H4	VSS2	VSS93
H26	VSS3	VSS94
A11	VSS4	VSS95
A13	VSS5	VSS96
A15	VSS6	VSS97
A17	VSS7	VSS98
A19	VSS8	VSS99
A21	VSS9	VSS100
A24	VSS10	VSS101
A26	VSS11	VSS102
A3	VSS12	VSS103
A9	VSS13	VSS104
AA1	VSS14	VSS105
AA11	VSS15	VSS106
AA13	VSS16	VSS107
AA15	VSS17	VSS108
AA17	VSS18	VSS109
AA19	VSS19	VSS110
AA23	VSS20	VSS111
AA26	VSS21	VSS112
AA4	VSS22	VSS113
AA7	VSS23	VSS114
AA9	VSS24	VSS115
AA9	VSS25	VSS116
AB10	VSS26	VSS117
AB10	VSS27	VSS118
AB16	VSS28	VSS119
AB16	VSS29	VSS120
AB20	VSS30	VSS121
AB21	VSS31	VSS122
AB24	VSS32	VSS123
AB3	VSS33	VSS124
AB6	VSS34	VSS125
AB6	VSS35	VSS126
AC11	VSS36	VSS127
AC11	VSS37	VSS128
AC15	VSS38	VSS129
AC17	VSS39	VSS130
AC17	VSS40	VSS131
AC19	VSS41	VSS132
AC22	VSS42	VSS133
AC22	VSS43	VSS134
AC25	VSS44	VSS135
AC9	VSS45	VSS136
AC9	VSS46	VSS137
AD1	VSS47	VSS138
AD10	VSS48	VSS139
AD10	VSS49	VSS140
AD12	VSS50	VSS141
AD14	VSS51	VSS142
AD16	VSS52	VSS143
AD18	VSS53	VSS144
AD21	VSS54	VSS145
AD23	VSS55	VSS146
AD4	VSS56	VSS147
AD6	VSS57	VSS148
AE19	VSS58	VSS149
AE13	VSS59	VSS150
AE19	VSS60	VSS151
AE17	VSS61	VSS152
AE22	VSS62	VSS153
AE24	VSS63	VSS154
AE26	VSS64	VSS155
AE7	VSS65	VSS156
AE9	VSS66	VSS157
AF1	VSS67	VSS158
AF1	VSS68	VSS159
AF10	VSS69	VSS160
AF12	VSS70	VSS161
AF14	VSS71	VSS162
AF16	VSS72	VSS163
AF18	VSS73	VSS164
AF20	VSS74	VSS165
AF26	VSS75	VSS166
AF6	VSS76	VSS167
B10	VSS77	VSS168
B12	VSS78	VSS169
B14	VSS79	VSS170
B16	VSS80	VSS171
B18	VSS81	VSS172
B20	VSS82	VSS173
B22	VSS83	VSS174
B24	VSS84	VSS175
B4	VSS85	VSS176
B6	VSS86	VSS177
B8	VSS87	VSS178
C11	VSS88	VSS179
C13	VSS89	VSS180
C15	VSS90	VSS181
C17	VSS91	VSS182



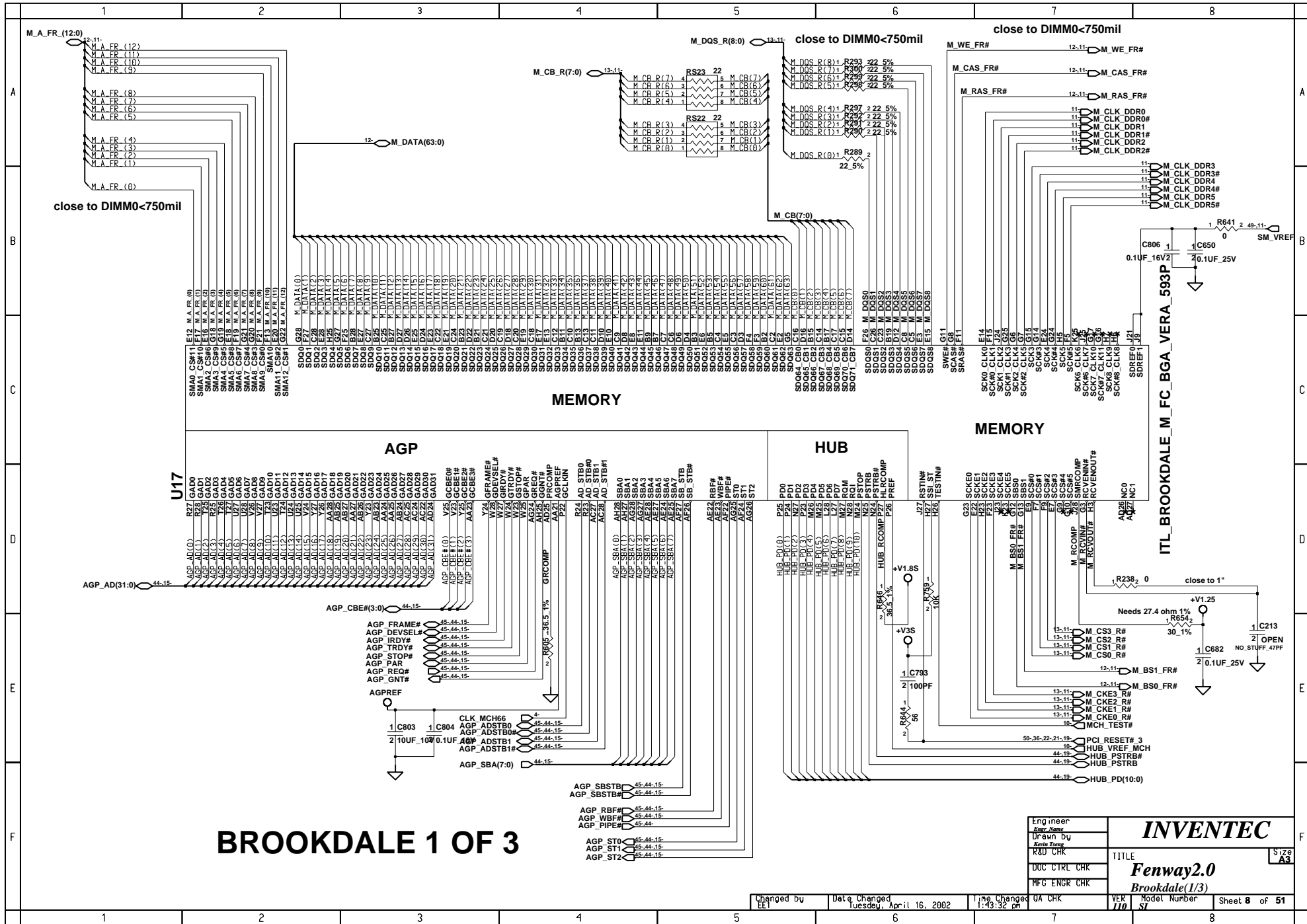
NORTHWOOD 2 OF 3

AMP_MPGA479M_C_1376756_479P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

Eng Name		INVENTEC TITLE Fenway2.0 Northwood(2/3)	S178 A3
Drawn by			
Revise			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			

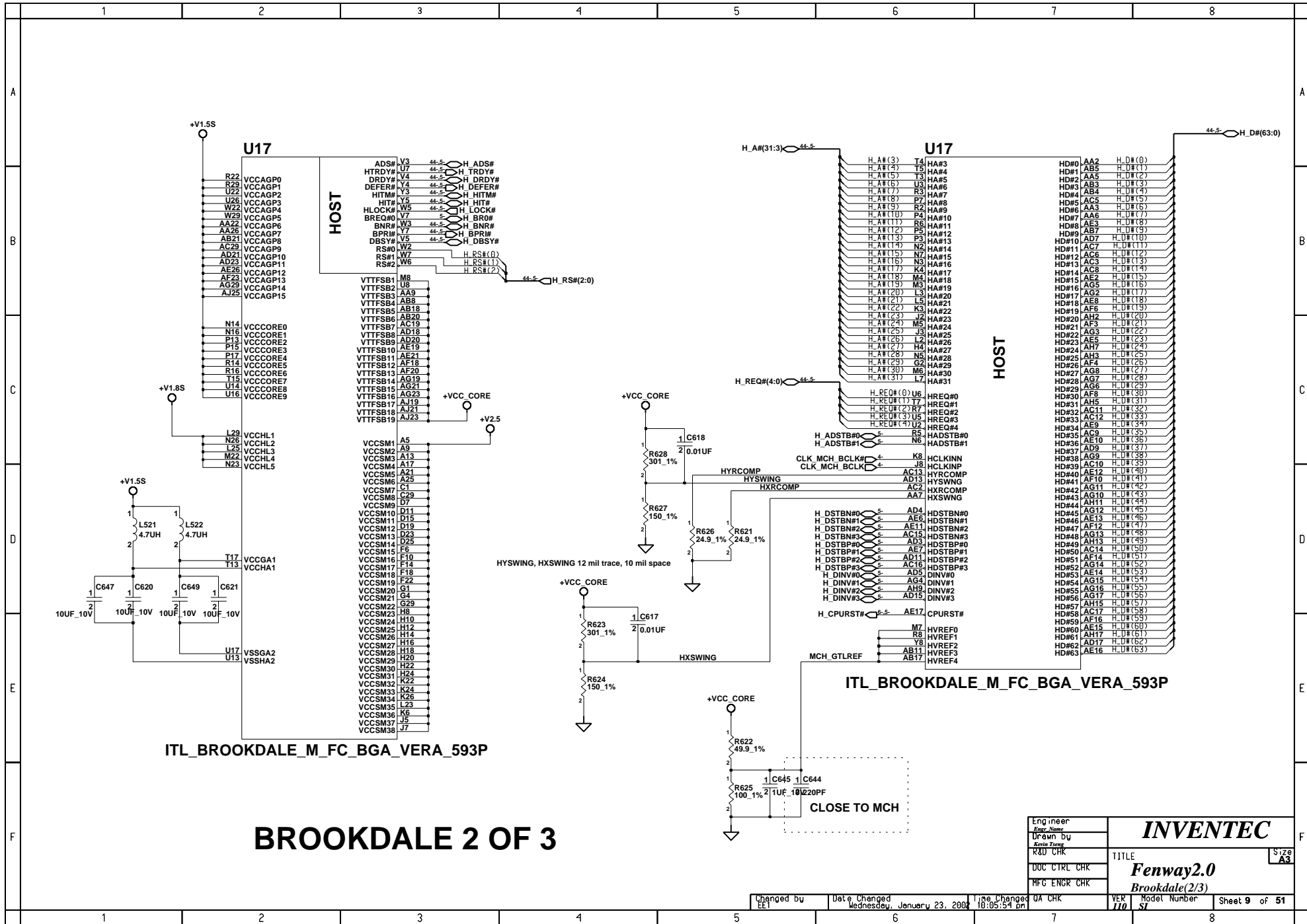


Engineer		INVENTEC	Sheet 7 of 51
Drawn by			
Rev'd			
QA CHK			
DOC CTRL		TITLE	Size A3
MFG ENGR		Fenway2.0	
		Northwood(3/3)	
Changed by	Date Changed	Time Changed	VER
EE1	Wednesday, January 23, 2008	10:05:41 AM	110



BROOKDALE 1 OF 3

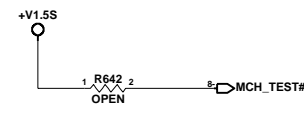
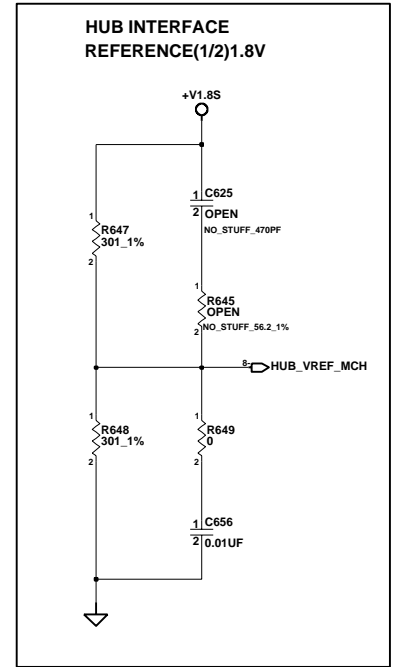
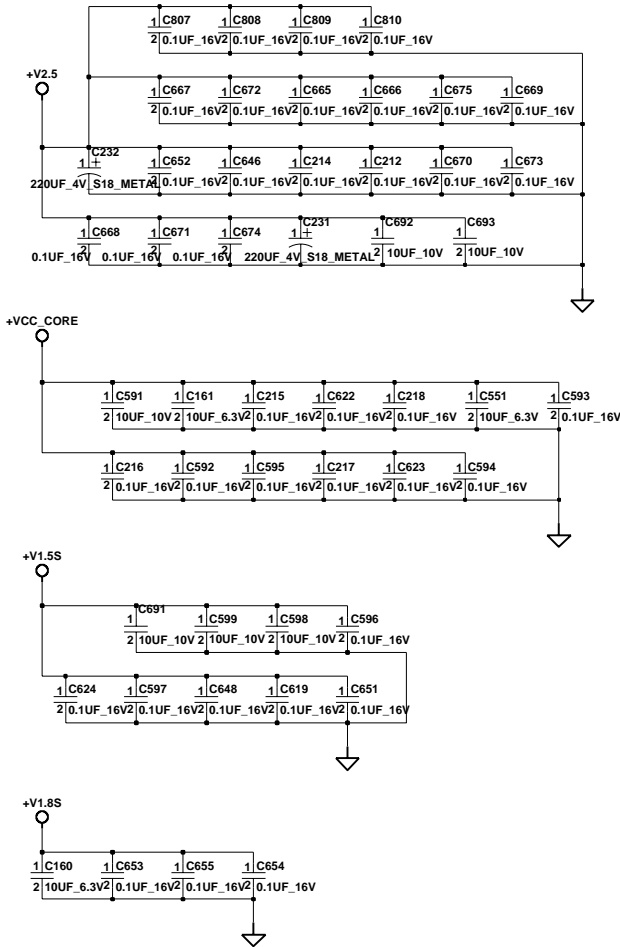
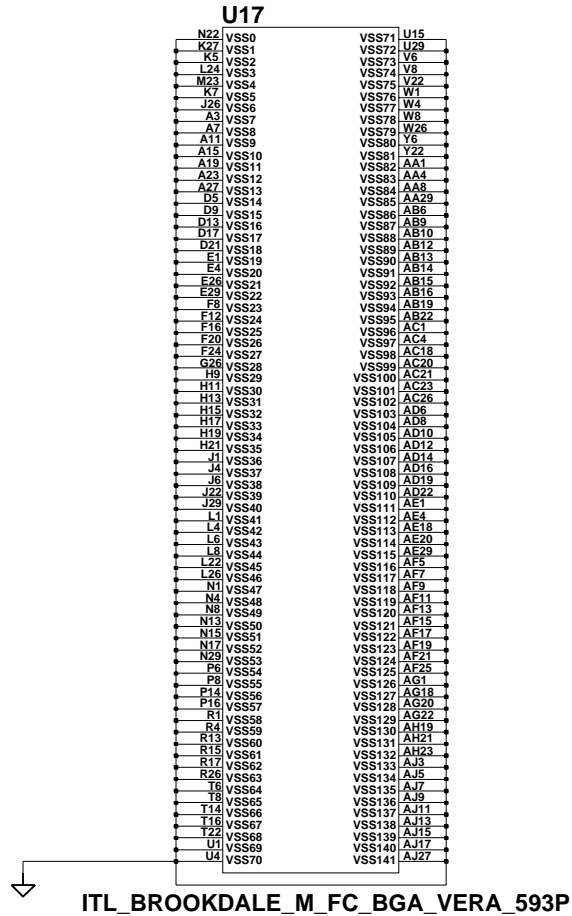
Eng ineer	
Drawn by	
Revise	
R&D CHK	
DOC CTRLR	
MFG ENGR	
Eng ineer	
Drawn by	
Revise	
R&D CHK	
DOC CTRLR	
MFG ENGR	
TITLE	
Fenway2.0	
Brookdale(1/3)	
VER	
110	
Model Number	
Sheet 8 of 51	



BROOKDALE 2 OF 3

Eng Ineer Eng Name	INVENTEC	TITLE Fenway2.0	Sheet 9 of 51
Drawn by Kevin Tarrag			
DOC CTRL CHK		Brookdale(2/3)	
MFG ENGR CHK			

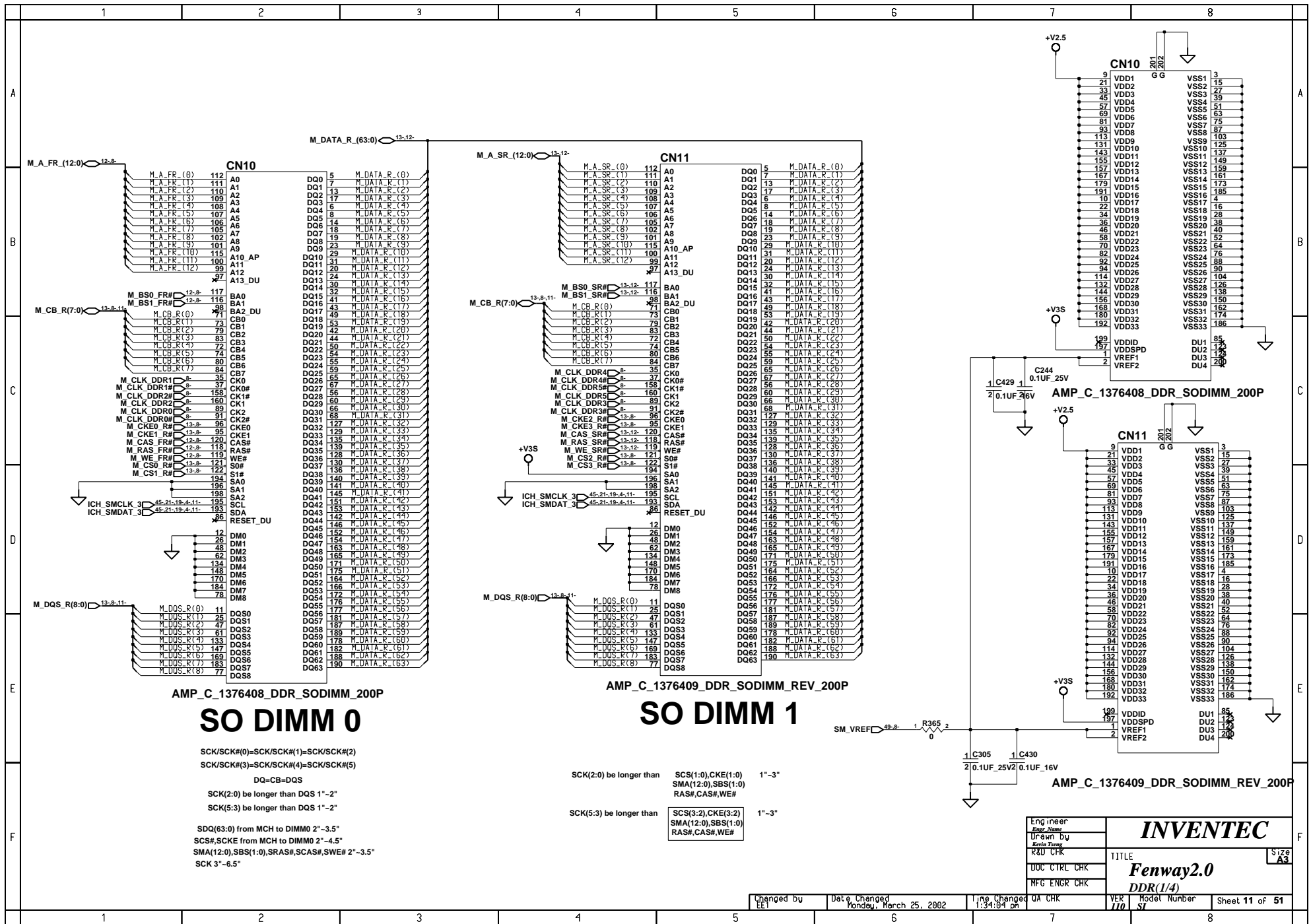
Changed by EE1 Date Changed Wednesday, January 23, 2008 Time Changed 10:05:54 pm



BROOKDALE 3 OF 3

Engineer	
Drawn by	
Revise	
RDU CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	INVENTEC
	Fenway2.0
	Brookdale(3/3)

Changed by	Date Changed	Time Changed	UA CHK	VER	Model Number	Sheet 10 of 51
EE1	Friday, Nov 10, 2002	6:30:10 pm		110	SI	



AMP_C_1376408_DDR_SODIMM_200P

AMP_C_1376409_DDR_SODIMM_REV_200P

SO DIMM 0

SCK/SCK#(0)=SCK/SCK#(1)=SCK/SCK#(2)
 SCK/SCK#(3)=SCK/SCK#(4)=SCK/SCK#(5)
 DQ=CB=DQS
 SCK(2:0) be longer than DQS 1"-2"
 SCK(5:3) be longer than DQS 1"-2"
 SDQ(63:0) from MCH to DIMM0 2"-3.5"
 SCS# from MCH to DIMM0 2"-4.5"
 SMA(12:0),SBS(1:0),SRAS#,SCAS#,SWE# 2"-3.5"
 SCK 3"-6.5"

SO DIMM 1

SCK(2:0) be longer than
 SCS(1:0),CKE(1:0) 1"-3"
 SMA(12:0),SBS(1:0)
 RAS#,CAS#,WE#
 SCK(5:3) be longer than
 SCS(3:2),CKE(3:2) 1"-3"
 SMA(12:0),SBS(1:0)
 RAS#,CAS#,WE#

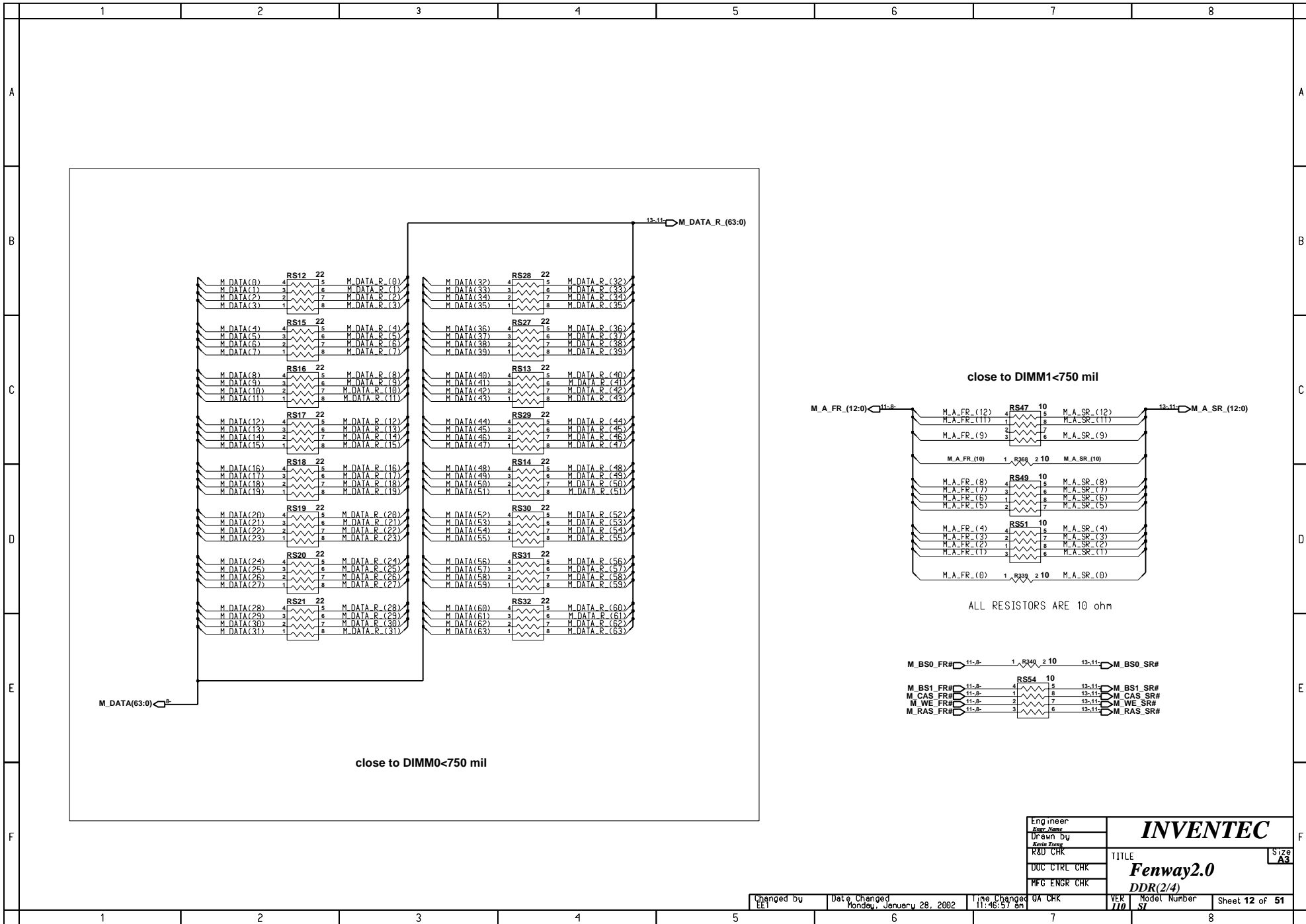
Engineer
 Drawn By
 R&D CHK
 DUC CTRLR CHK
 HFG ENGR CHK

INVENTEC

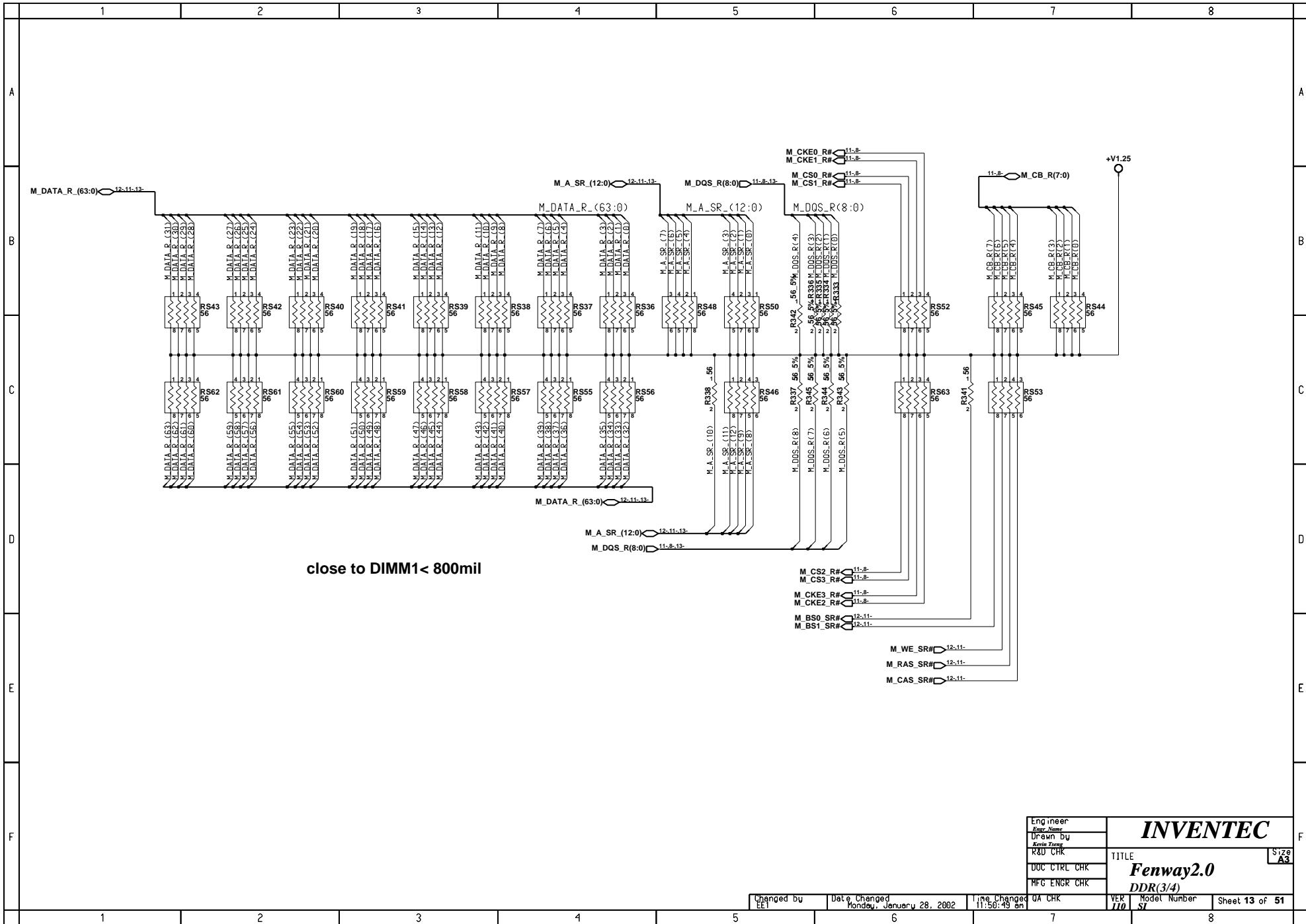
TITLE
Fenway2.0
DDR(1/4)

S178
A3

Changed by EET Date Changed 1:34:04 pm, March 25, 2002 Time Changed UA CHK VBR 110 Model Number 51 Sheet 11 of 51



Eng Ineer Engr Name	INVENTEC	S/29 A3
Drawn by		
Revise R&D CHK		
DOC CTRL CHK		
HFG ENGR CHK		
TITLE	Fenway2.0	
Model Number	DDR(2/4)	



close to DIMM1 < 800mil

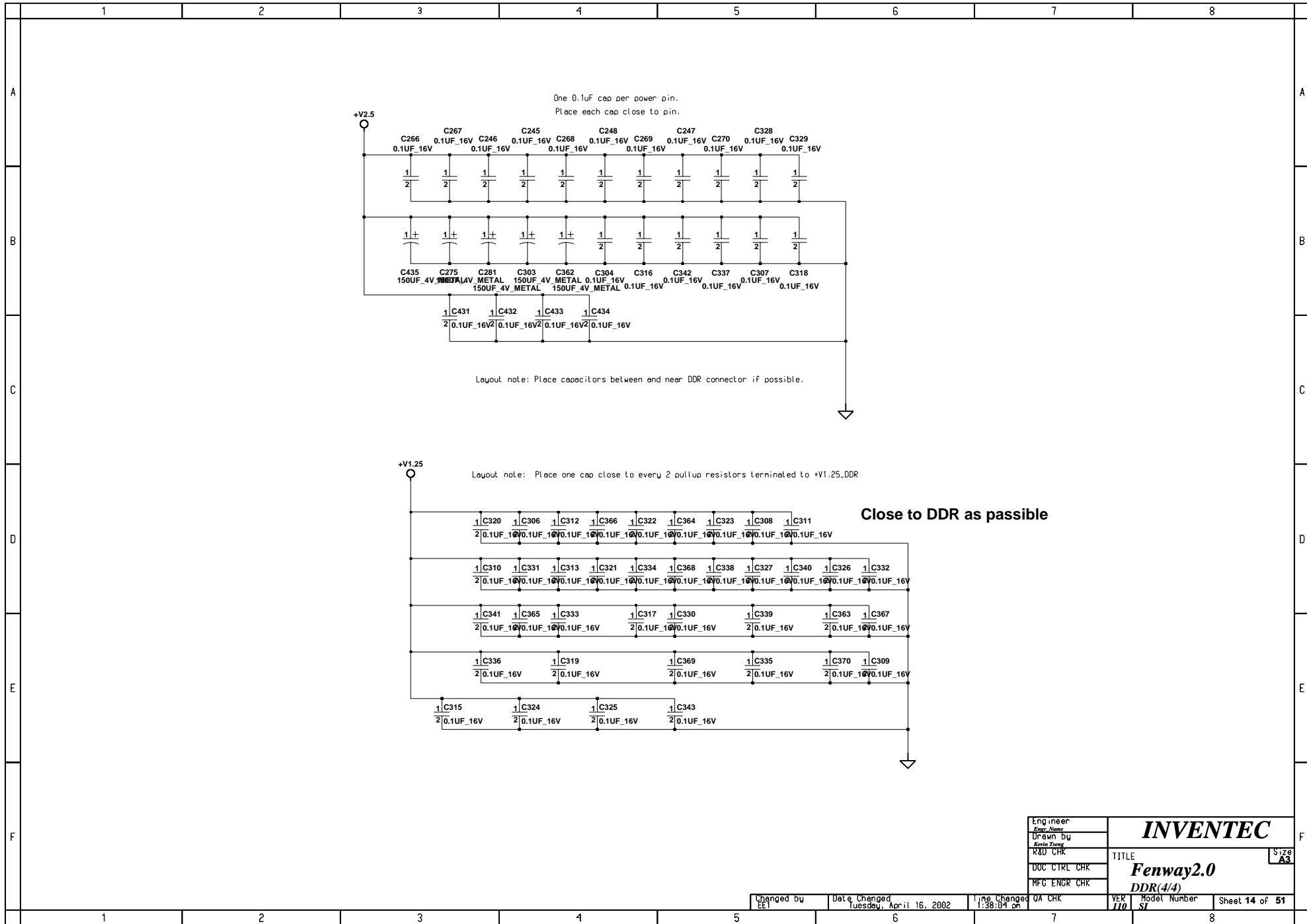
Engineer	
Drawn by	
Revise	
RDW CHK	
DOC CTRL CHK	
MFG ENGR CHK	
Model Number	VER 110
Sheet 13 of 51	

INVENTEC

TITLE
Fenway2.0
 DDR(3/4)

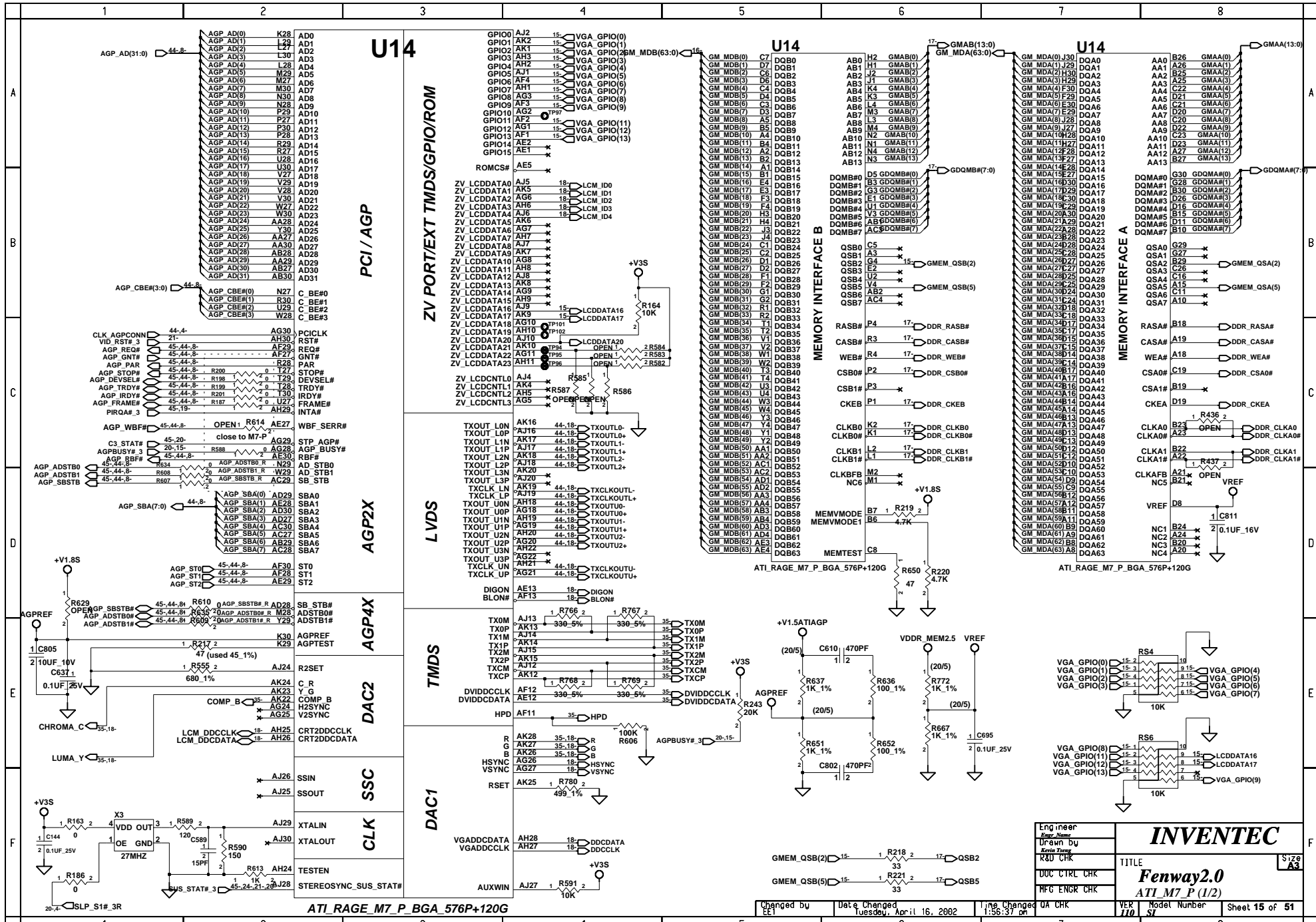
S178
 A3

Changed by EE1 Date Changed Monday, January 28, 2002 Time Changed 11:50:49 am



Eng. Iner	INVENTEC TITLE Fenway2.0 DDR(4/4)	Size A3
Eng. Name		
Drawn by		
Revise		
R3D CHK		
DOC CTRL CHK	Model Number	Sheet 14 of 51
MFG ENGR CHK	VER 110	SI

Changed by	Date Changed	Time Changed	UA CHK
EE1	Tuesday, Apr 16, 2002	1:38:04 pm	



Changed by: EET	Date Changed: Tuesday, Apr 16, 2002	Time Changed: 1:56:37 pm	UA CHK:	VER 110	Model Number: 51	Sheet 15 of 51
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Eng Ineer _____
 Drawn by _____
 Revise by _____
 R&D CHK _____
 DUC CTRL CHK _____
 MFG ENGR CHK _____

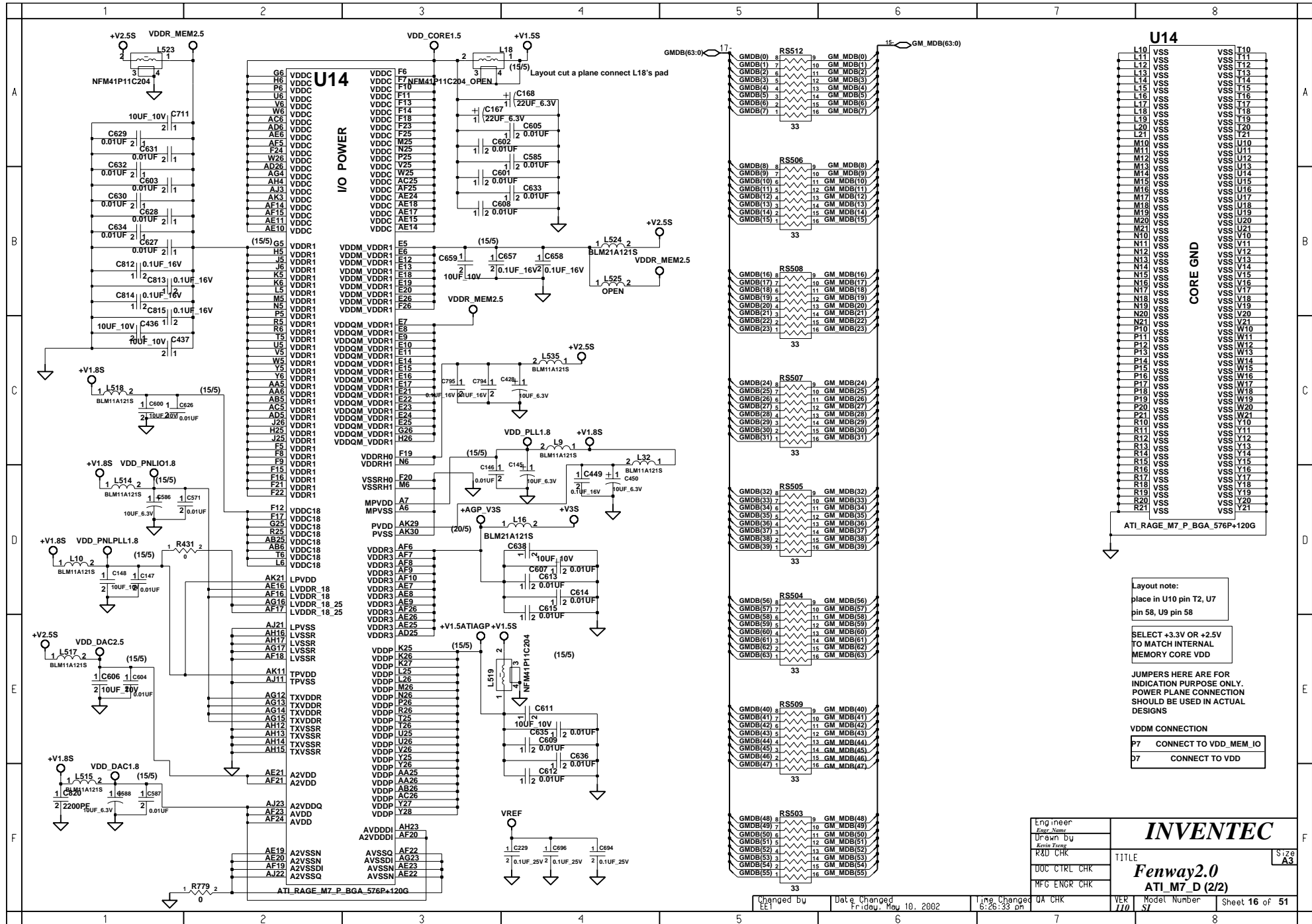
INVENTEC

Fenway 2.0

ATI M7 P (1/2)

S178 A3

ATI_RAGE_M7_P_BGA_576P+120G



U14		CORE GND	
L10	VSS	T10	VSS
L11	VSS	T11	VSS
L12	VSS	T12	VSS
L13	VSS	T13	VSS
L14	VSS	T14	VSS
L15	VSS	T15	VSS
L16	VSS	T16	VSS
L17	VSS	T17	VSS
L18	VSS	T18	VSS
L19	VSS	T19	VSS
L20	VSS	T20	VSS
L21	VSS	T21	VSS
M10	VSS	U10	VSS
M11	VSS	U11	VSS
M12	VSS	U12	VSS
M13	VSS	U13	VSS
M14	VSS	U14	VSS
M15	VSS	U15	VSS
M16	VSS	U16	VSS
M17	VSS	U17	VSS
M18	VSS	U18	VSS
M19	VSS	U19	VSS
M20	VSS	U20	VSS
M21	VSS	U21	VSS
N10	VSS	V10	VSS
N11	VSS	V11	VSS
N12	VSS	V12	VSS
N13	VSS	V13	VSS
N14	VSS	V14	VSS
N15	VSS	V15	VSS
N16	VSS	V16	VSS
N17	VSS	V17	VSS
N18	VSS	V18	VSS
N19	VSS	V19	VSS
N20	VSS	V20	VSS
N21	VSS	V21	VSS
P10	VSS	W10	VSS
P11	VSS	W11	VSS
P12	VSS	W12	VSS
P13	VSS	W13	VSS
P14	VSS	W14	VSS
P15	VSS	W15	VSS
P16	VSS	W16	VSS
P17	VSS	W17	VSS
P18	VSS	W18	VSS
P19	VSS	W19	VSS
P20	VSS	W20	VSS
P21	VSS	W21	VSS
P22	VSS	W22	VSS
R10	VSS	Y10	VSS
R11	VSS	Y11	VSS
R12	VSS	Y12	VSS
R13	VSS	Y13	VSS
R14	VSS	Y14	VSS
R15	VSS	Y15	VSS
R16	VSS	Y16	VSS
R17	VSS	Y17	VSS
R18	VSS	Y18	VSS
R19	VSS	Y19	VSS
R20	VSS	Y20	VSS
R21	VSS	Y21	VSS

Layout note:
 place in U10 pin T2, U7
 pin 58, U9 pin 58

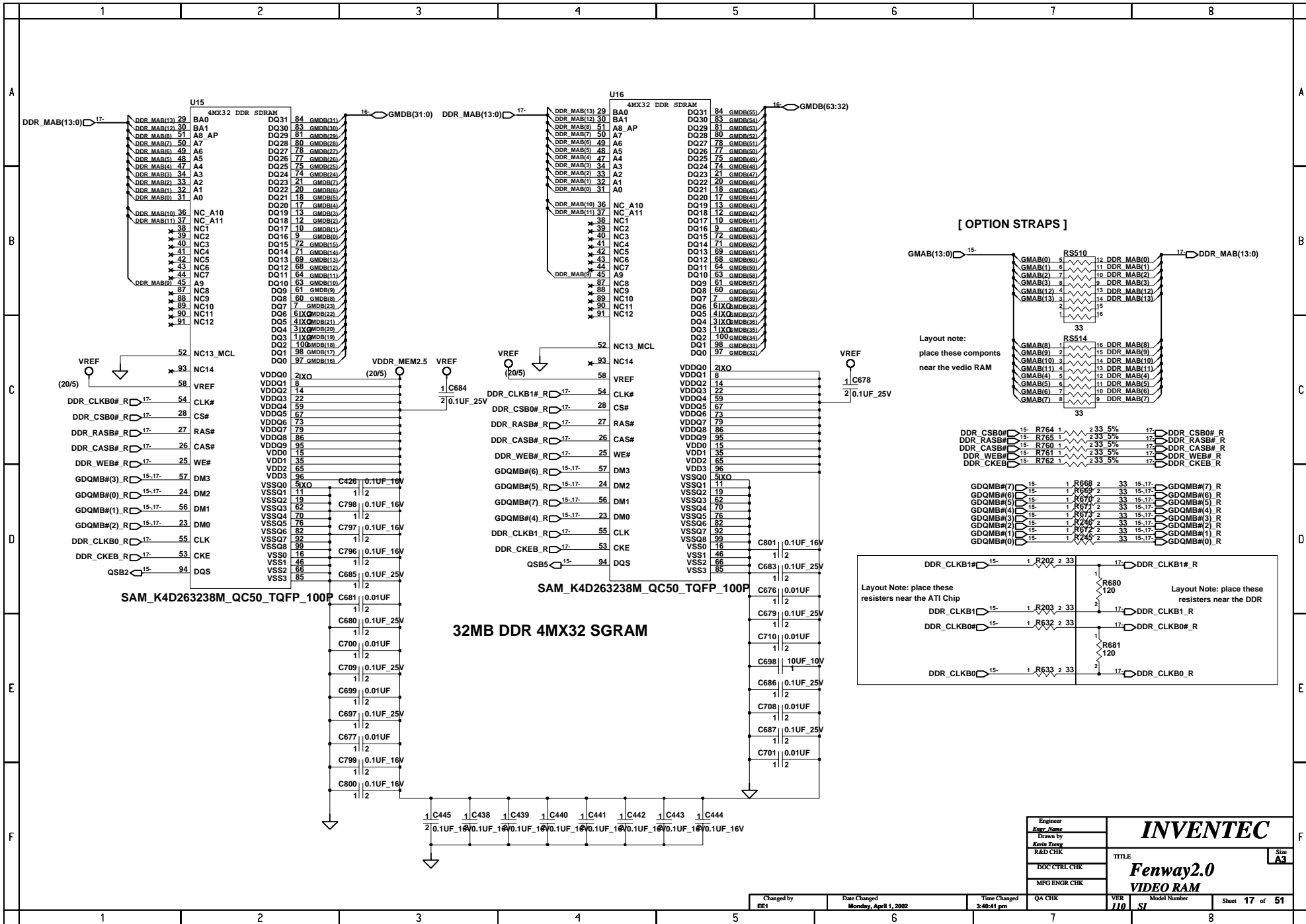
SELECT +3.3V OR +2.5V
 TO MATCH INTERNAL
 MEMORY CORE VDD

JUMPERS HERE ARE FOR
 INDICATION PURPOSE ONLY.
 POWER PLANE CONNECTION
 SHOULD BE USED IN ACTUAL
 DESIGNS

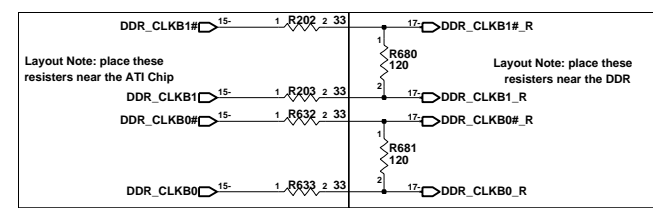
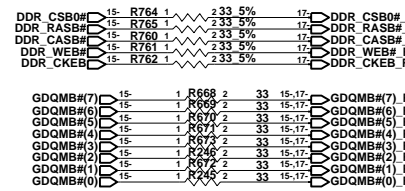
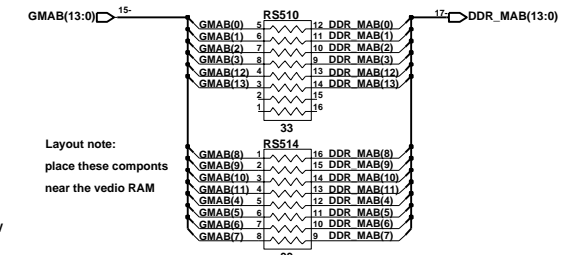
VDDM CONNECTION

P7	CONNECT TO VDD_MEM_IO
D7	CONNECT TO VDD

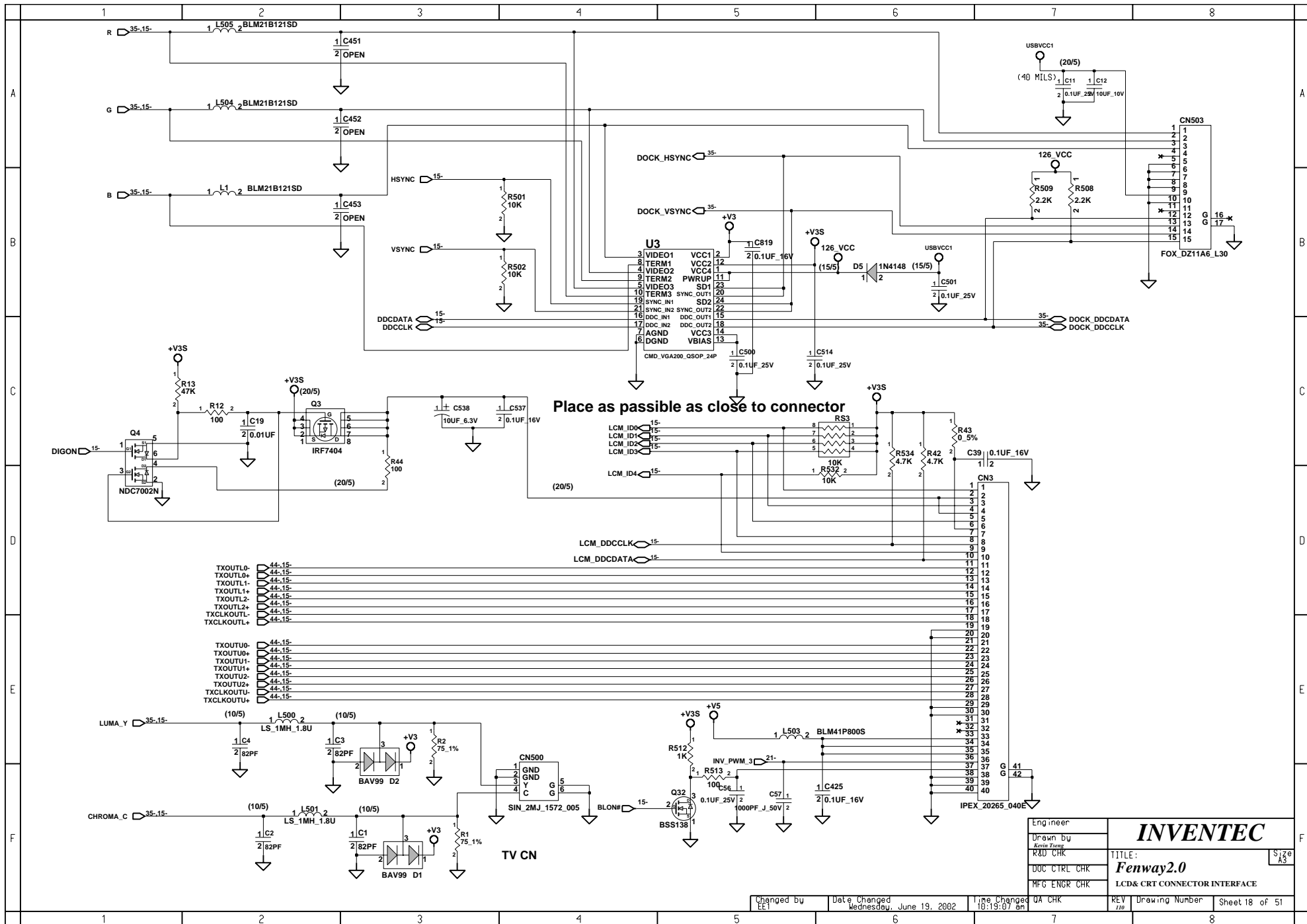
Eng Name		INVENTEC	
Drawn By		Fenway2.0	
Rev'd By			
Rel'd By		TITLE	
UOC CTRL CHK		ATI_M7_D (2)	
MFG ENGR CHK		S79	
VER	110	Model Number	
Chg'd By	EET	Date Changed	Friday, May 10, 2002
Eng. Changed	6:26:33 on	UA CHK	
Sheet	16	of	51



[OPTION STRAPS]

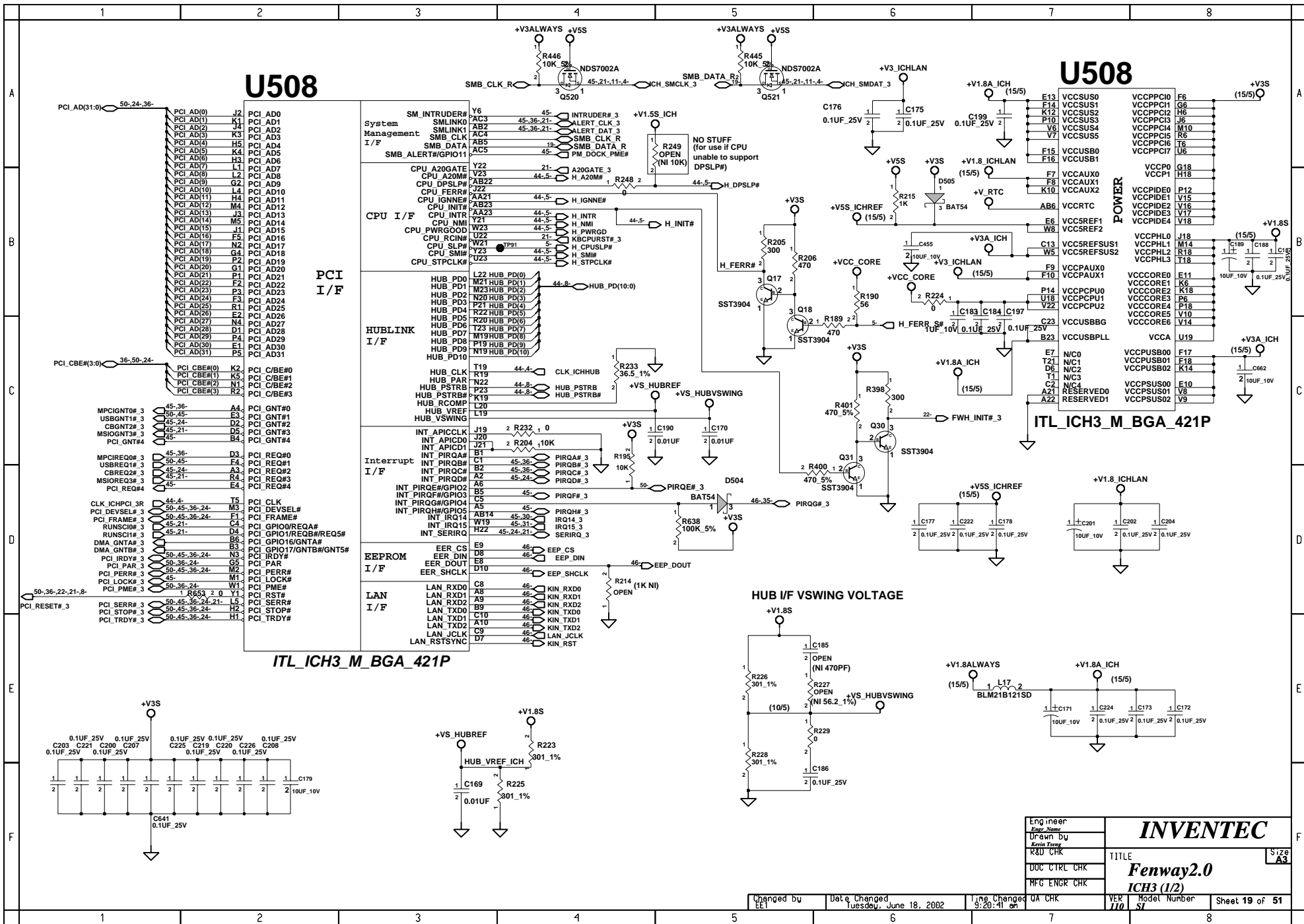


Engineer		INVENTEC	Size A3
Engr Name			
Drawn by			
Revise			
R&D CHK		TITLE	
DOC CTRL CHK		Fenway2.0	
MFG ENGR CHK		VIDEO RAM	
QA CHK		VER	Model Number
Changed by	Date Changed	Time Changed	Sheet 17 of 51
EE1	Monday, April 1, 2002	3:40:41 pm	



Place as possible as close to connector

Engineer		INVENTEC TITLE: Fenway2.0 LCD & CRT CONNECTOR INTERFACE	Size A3
Drawn by			
Rev'd			
DOC CTRL			
MFG ENGR			
Changed by	Date Changed	Time Changed	UA CHK
EE1	Wednesday, June 19, 2002	10:19:07 am	REV 110
			Drawing Number
			Sheet 18 of 51



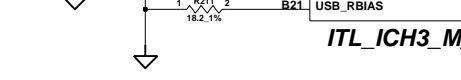
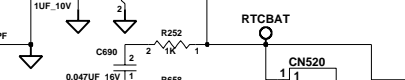
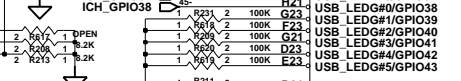
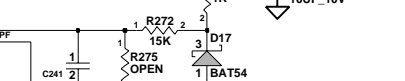
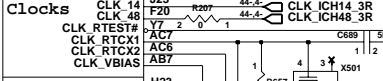
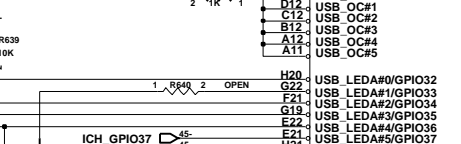
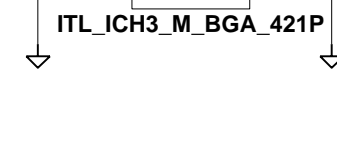
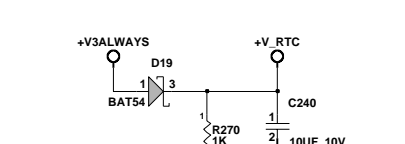
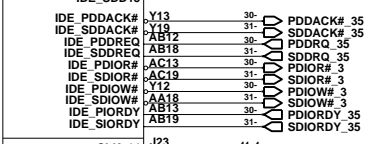
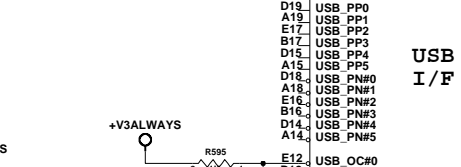
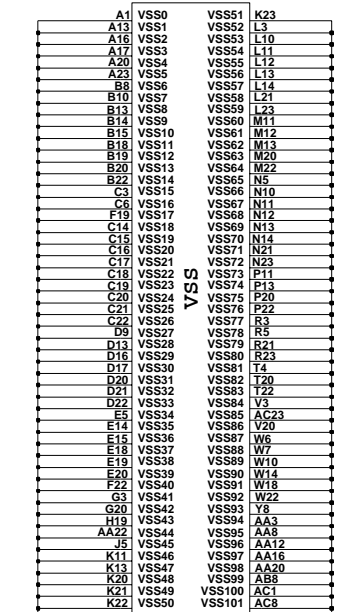
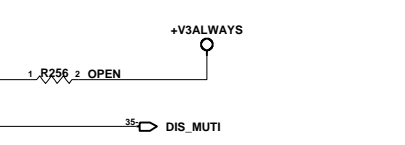
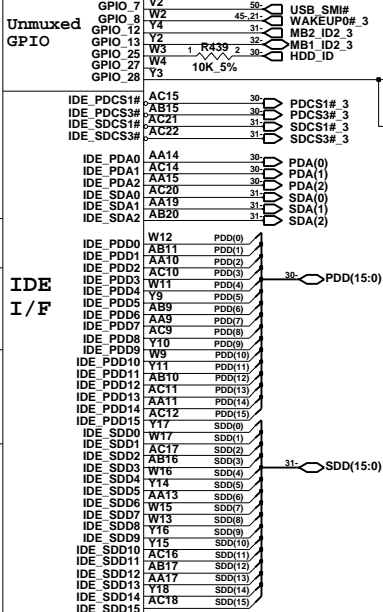
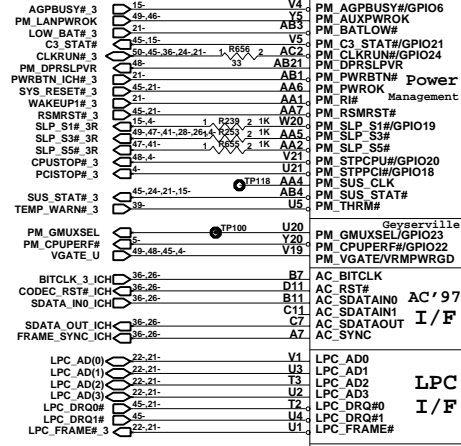
Eng. in-ner		INVENTEC
Drawn by		
Rev. 1.0		
DOC. CTRL. CHK		TITLE
MFG ENGR. CHK		Fenway2.0
		ICH3 (I/2)

Changed by	Date Changed	Eng. Changed	QA CHK	VER	Model Number	Sheet 19 of 51
EE1	Tuesday, June 18, 2002	5:20:41 am		110	SI	

S178
A3

U508

U508

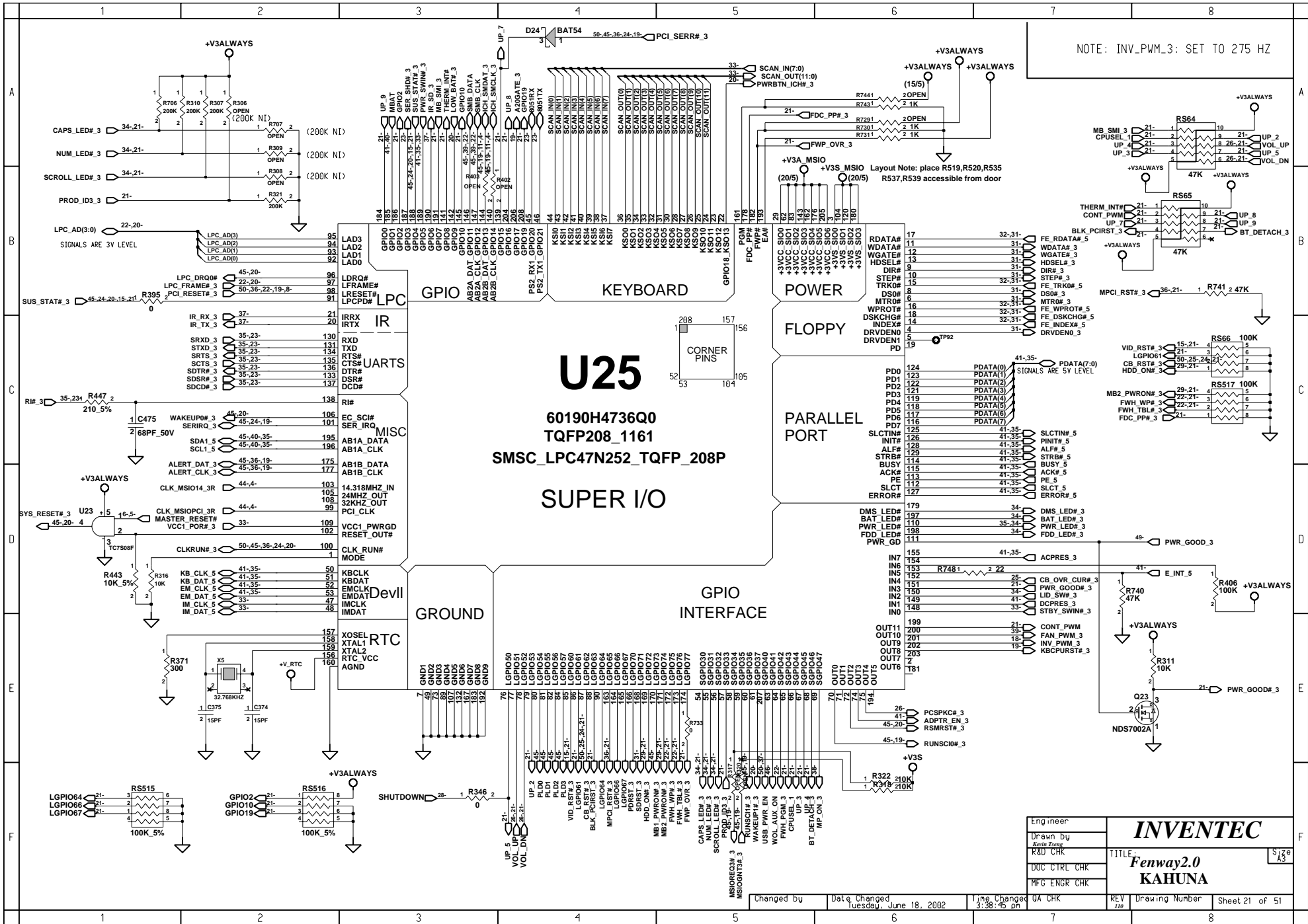


VMEM_CFG(2:0)-Memory type	
001	Samsung 32MB
011	Hynix 32MB
101	Micron 32MB
111	Winbond 32MB

Eng Ineer	
Drawn by	
Revise	
R3D CHK	
DUO CTRL CHK	
MFG ENGR CHK	
Model Number	VER 110
Sheet 20 of 51	

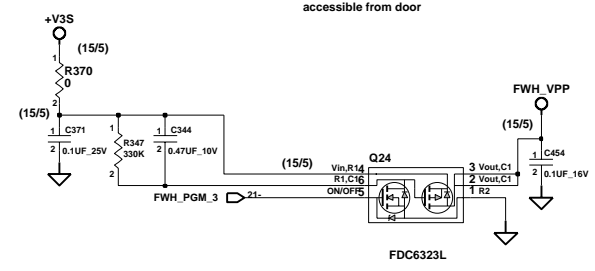
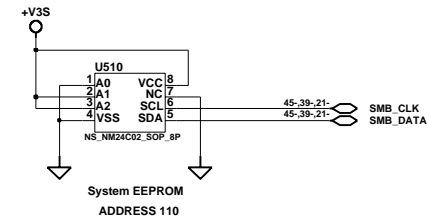
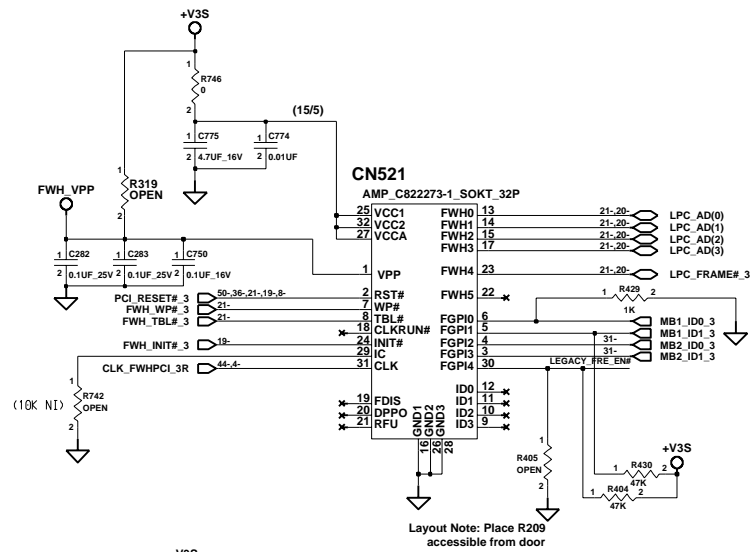
INVENTEC
Fenway2.0
ICH3 (2/2)

Changed by: EE1
 Date Changed: Saturday, March 30, 2002
 Time Changed: 5:39:51 pm



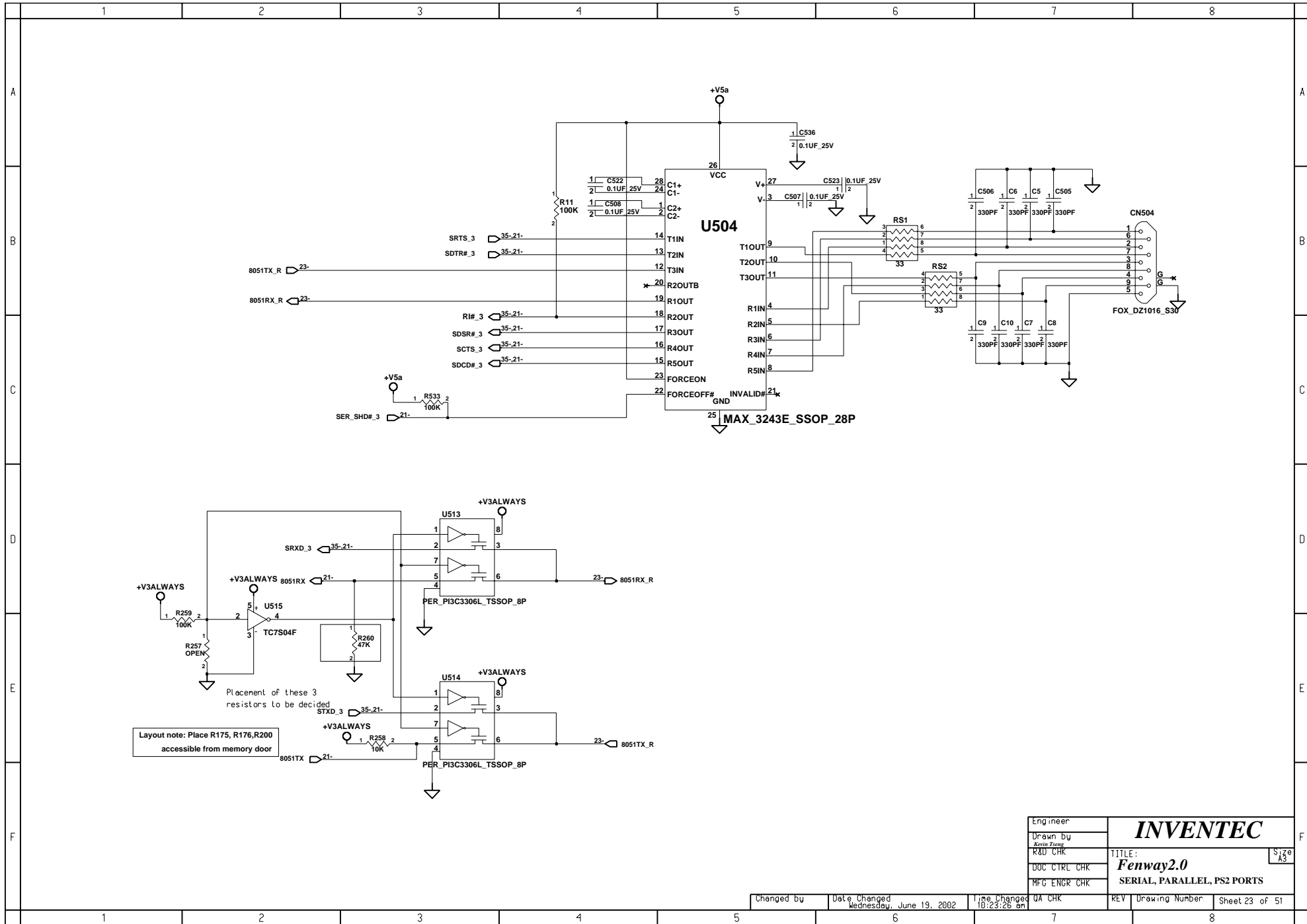
Eng'neer	
Drawn by	
Rev'd by	
DOC CTRL	CHK
MFG ENGR	CHK
REV 110	
Drawing Number	
Sheet 21 of 51	

Changed by: _____ Date Changed: Tuesday, June 18, 2002 Time Changed: 3:38:45 pm



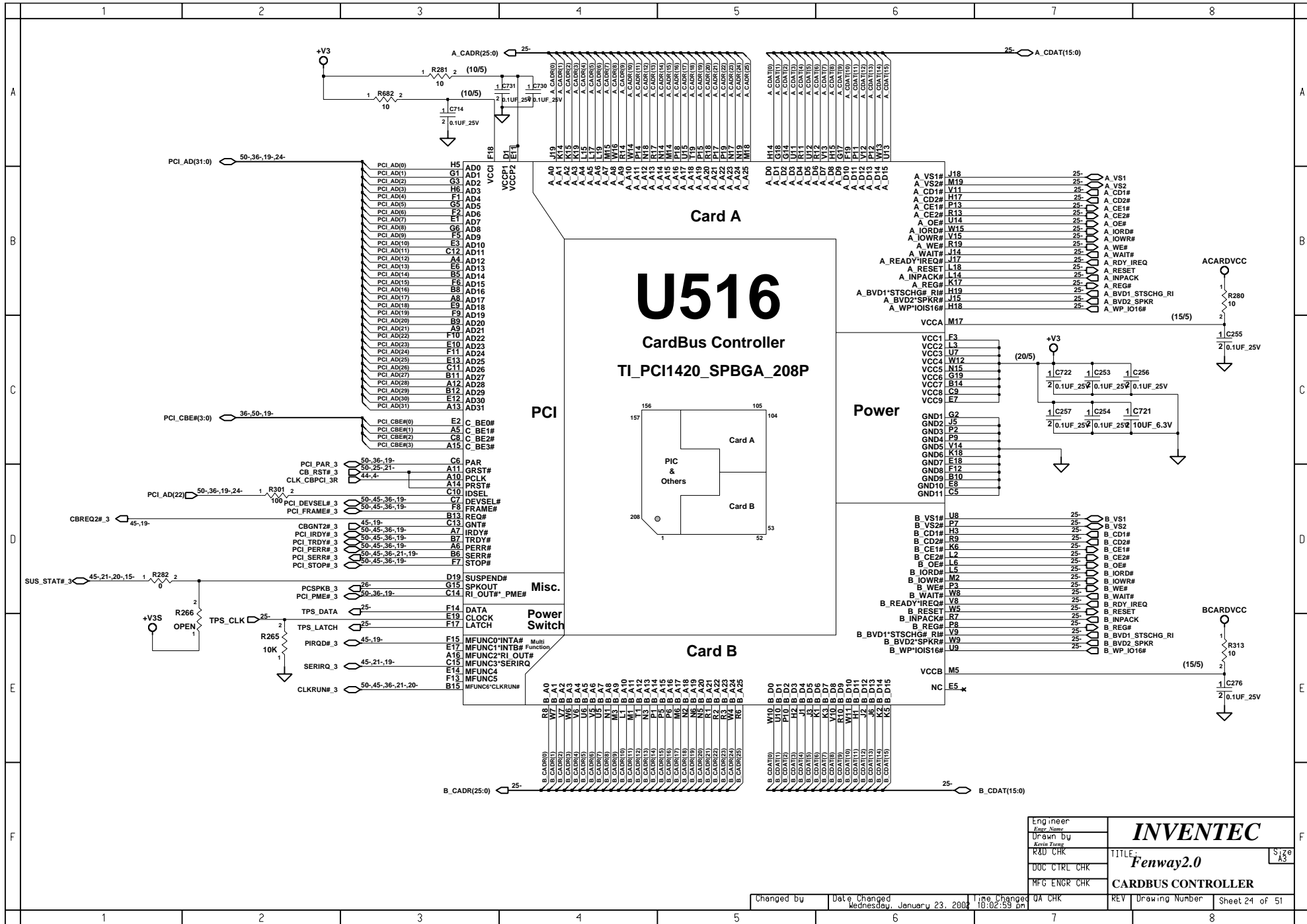
Engineer	INVENTEC		
Drawn by	Kevin Tseng		
Rev'd	R&D CHK		
DOC CTRL	MFG ENGR		
DATE	TITLE: Penway2.0		
REV	Drawing Number		
Sheet 22 of 51	SIZE A3		

Changed by	Date Changed	Time Changed	QA CHK	REV	Drawing Number	Sheet 22 of 51
	Tuesday, April 2, 2002	2:51:41 PM				



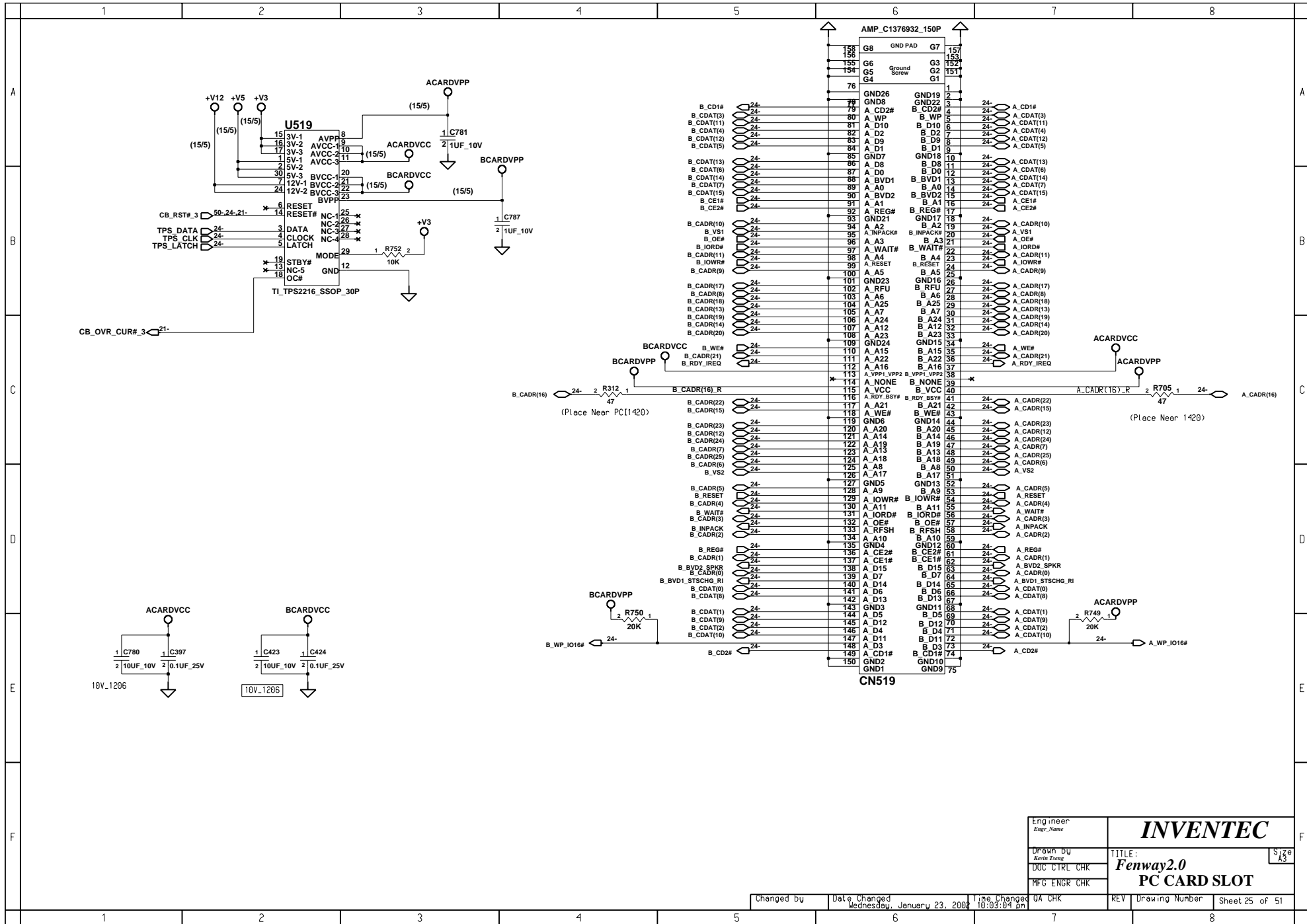
Engineer	INVENTEC
Drawn by	
Rev'd by	
R3D CHK	
DOC CTRL CHK	
MFG ENGR CHK	TITLE: Fenway2.0
	SERIAL, PARALLEL, PS2 PORTS

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 23 of 51
	Wednesday, June 19, 2002	10:23:26 am				

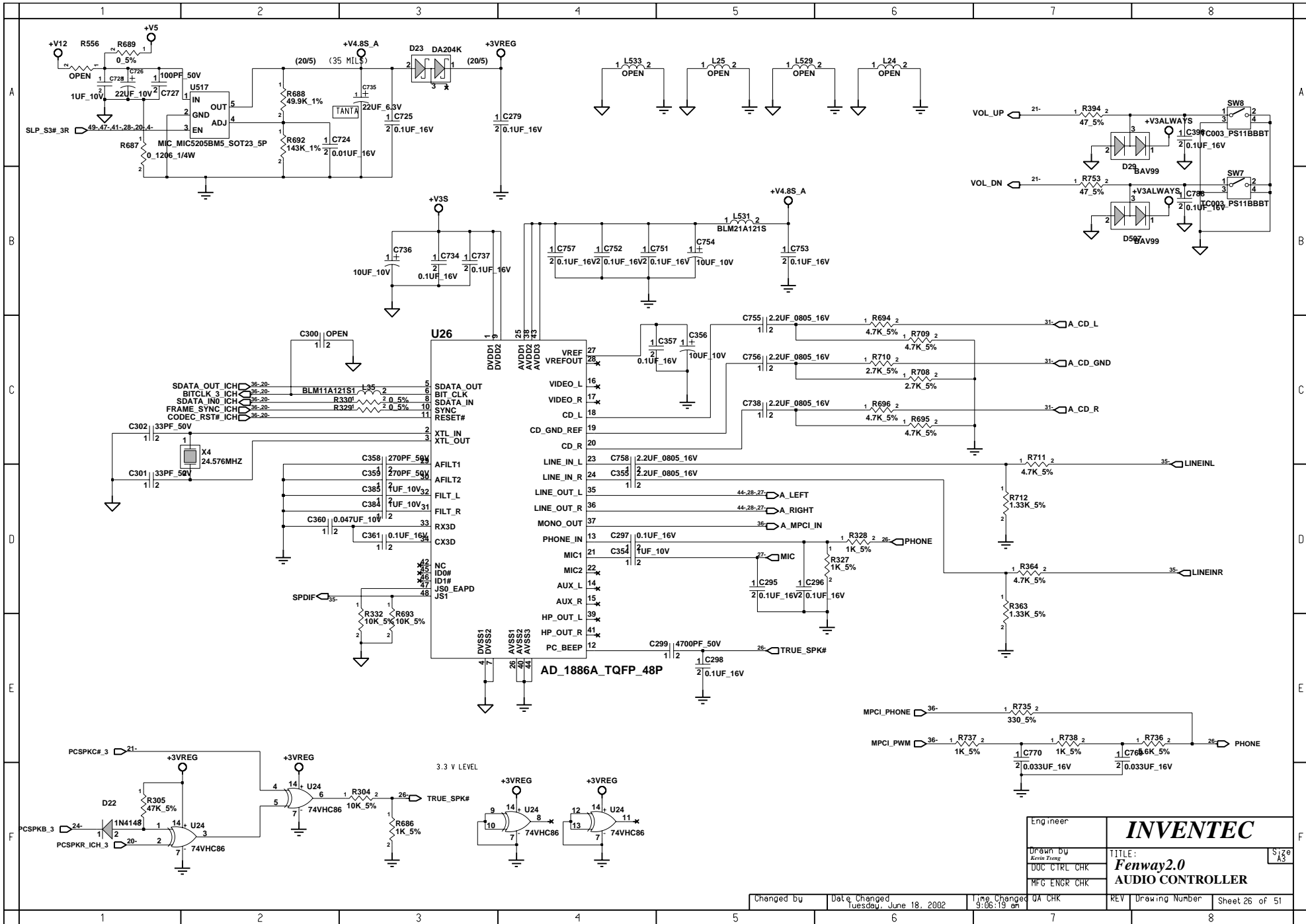


Eng. Name		INVENTEC	TITLE: Fenway2.0	SIZE: A3
Drawn By				
Rev. No.		CARDBUS CONTROLLER		
DOC. CTRL. CHK		REV Drawing Number		
MFG ENGR. CHK		Sheet 24 of 51		

Changed by _____ Date Changed Wednesday, January 23, 2002 Time Changed 10:02:59 pm

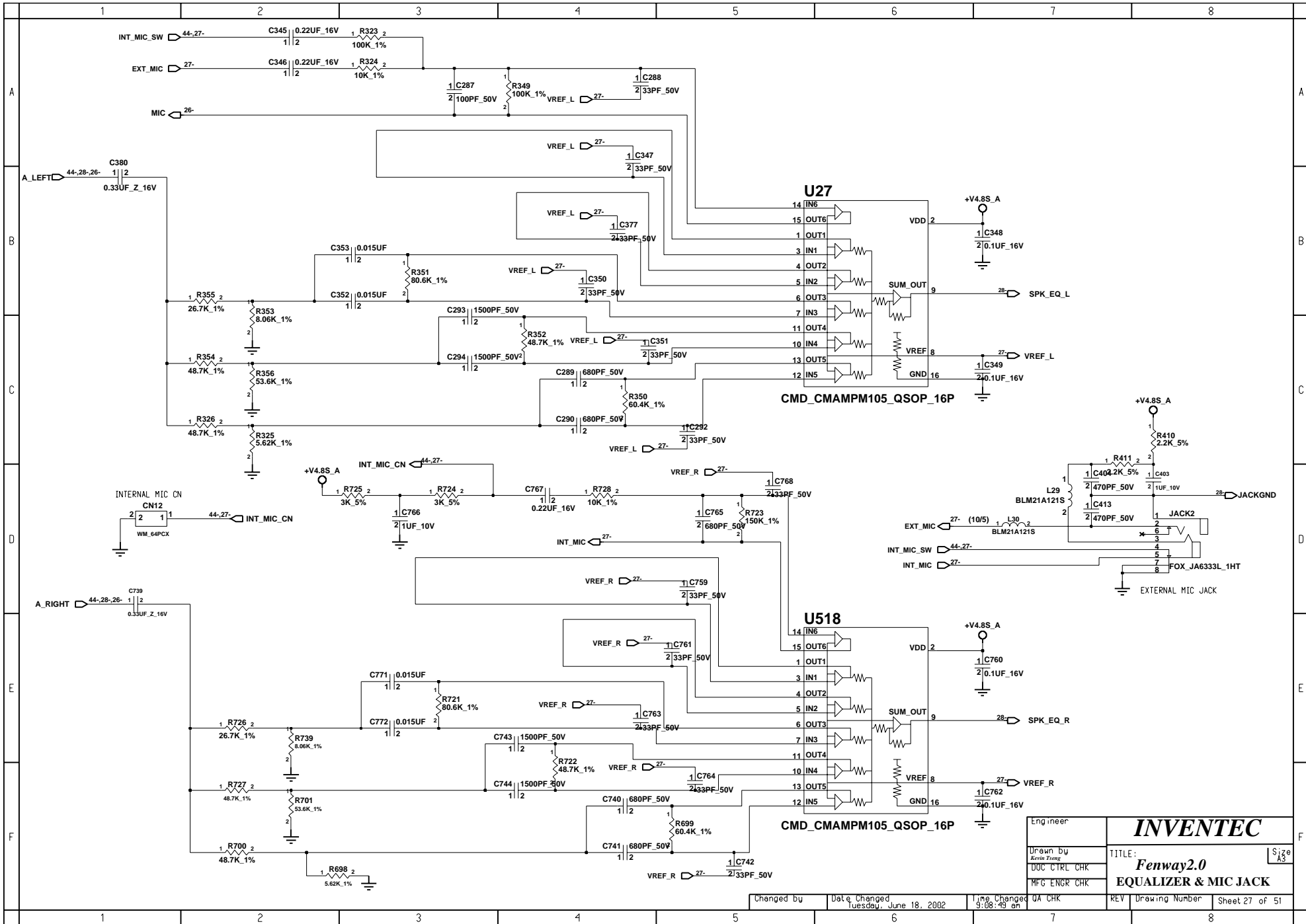


Eng Ineer Engr Name	INVENTEC		S:1/8
Drawn by Kevin Tseng			
DOC CTRL CHK	TITLE: Fenway2.0		REV
MFG ENGR CHK	PC CARD SLOT		
Changed by	Date Changed Wednesday, January 23, 2008	Time Changed 10:05:10 AM	Sheet 25 of 51

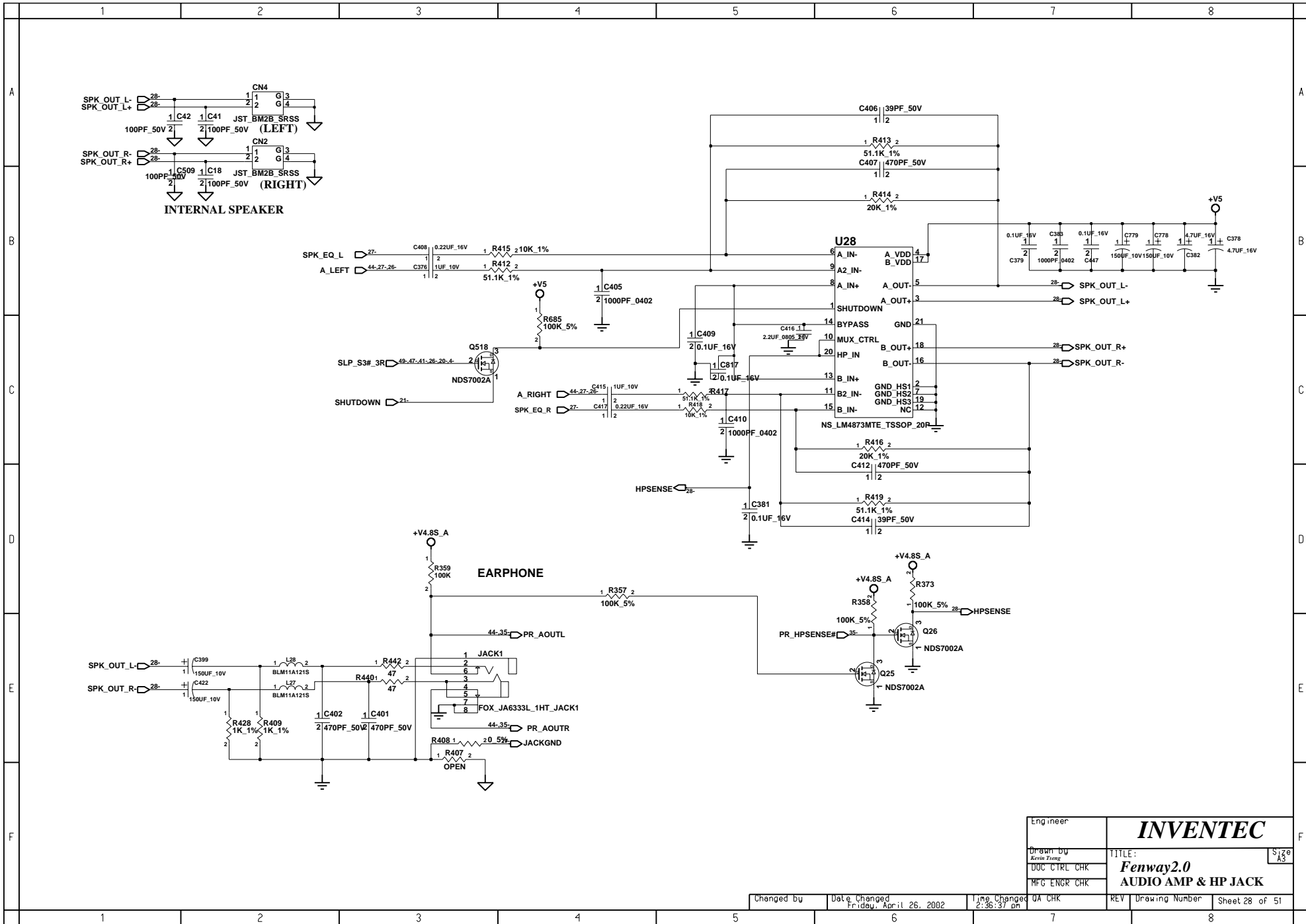


Engineer	INVENTEC	
Drawn by Kevin Tseng	TITLE: Fenway2.0	
DOC CTRL CHK	AUDIO CONTROLLER	
MFG ENGR CHK		

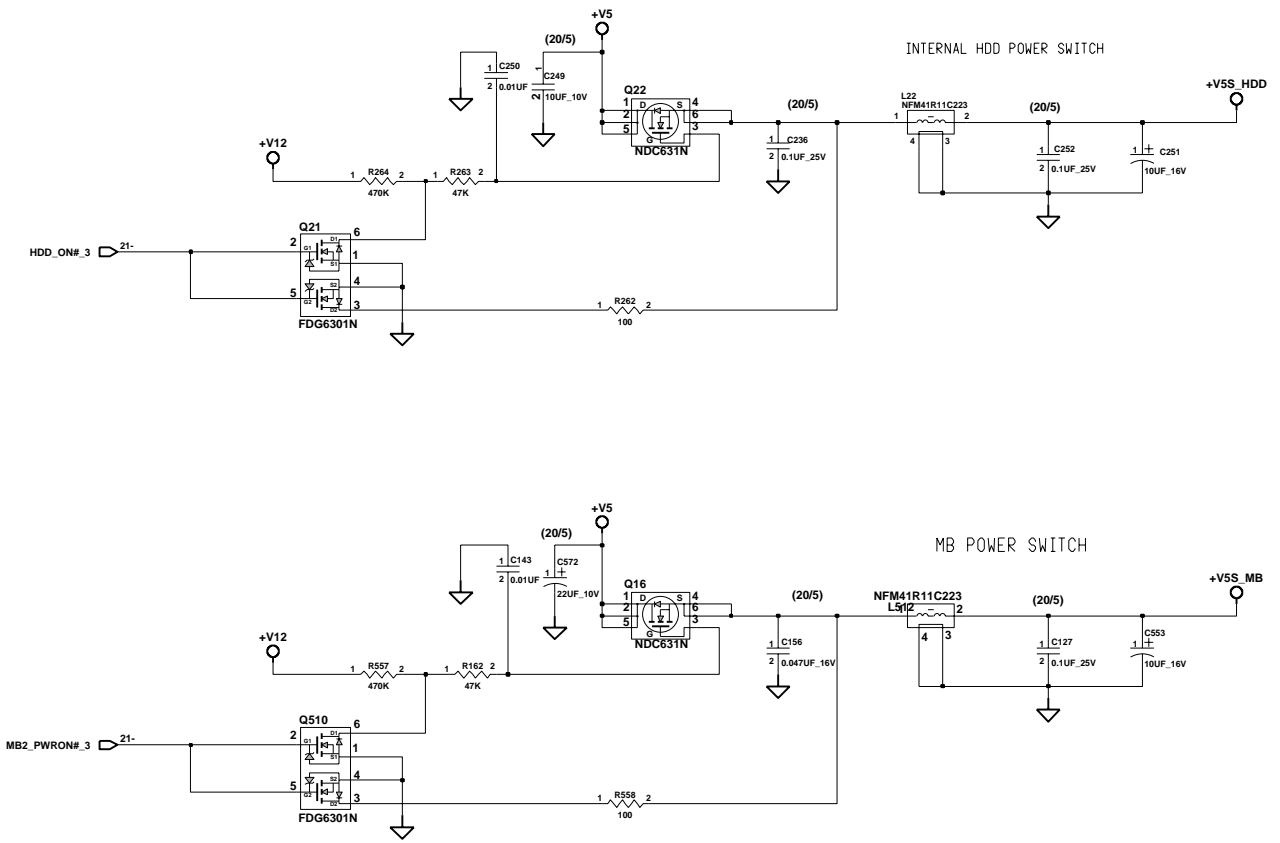
Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:06:19 am	UA CHK	REV	Drawing Number	Sheet 26 of 51
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Engineer	INVENTEC	
Drawn by Kevin Tseng	TITLE: Fenway2.0	Size A3
DOC CTRL CHK	EQUALIZER & MIC JACK	
MFG ENGR CHK	REV	Drawing Number
Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:08:49 am
UA CHK	REV	Sheet 27 of 51



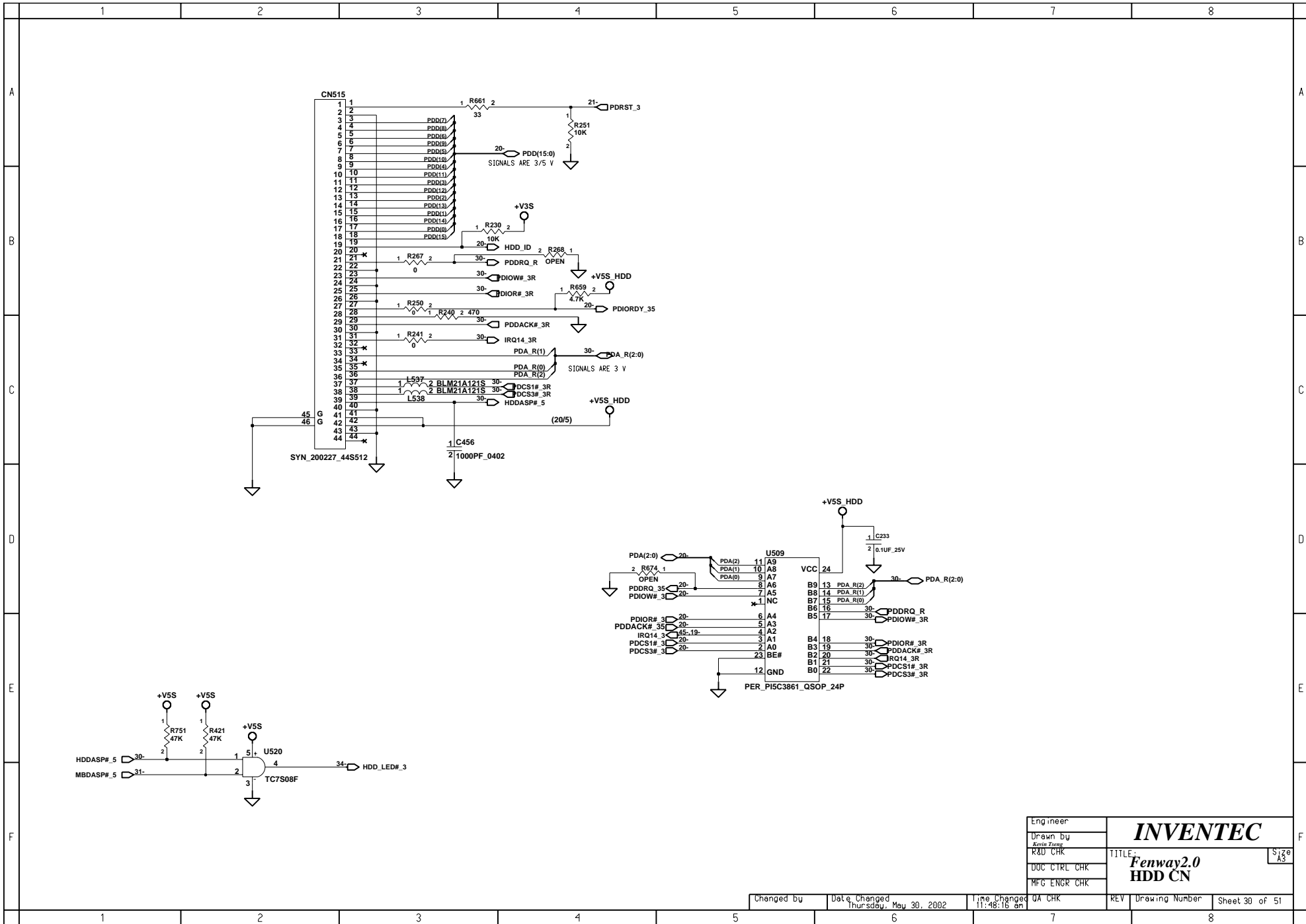
Engineer	INVENTEC	
Drawn by Kevin Tseng	TITLE: Fenway2.0	Size A3
DOC CTRL CHK	AUDIO AMP & HP JACK	
MFG ENGR CHK		
Changed by	Date Changed Friday, April 26, 2002	Time Changed 2:36:37 pm
QA CHK	REV	Drawing Number
		Sheet 28 of 51



SIGNAL NAME	TRACE WIDTH (MILS)
+VSS_HDD	60
HDDPWR	60
+VSS_MB	60
MBPWR	60

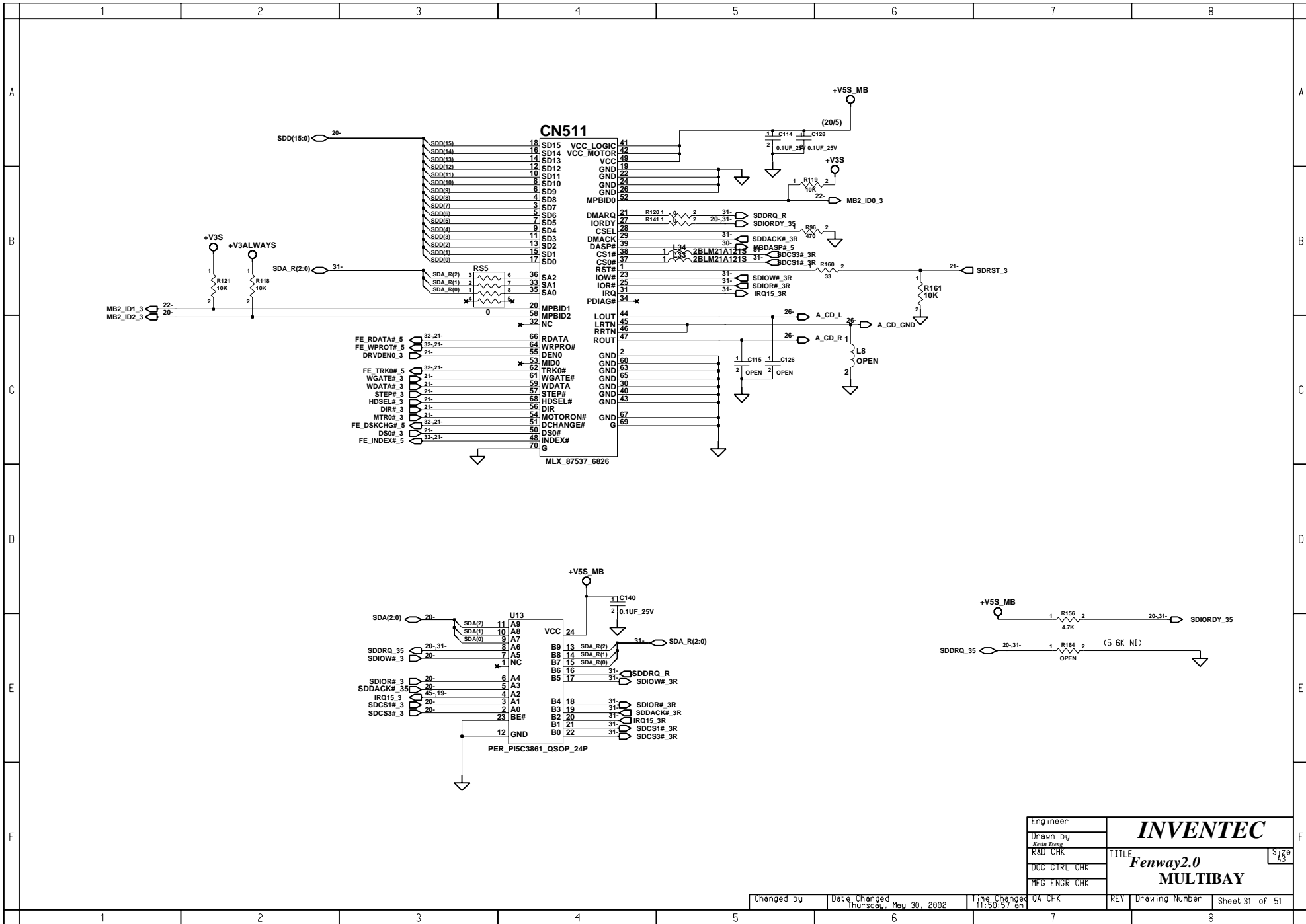
Engineer	
Drawn by	INVENTEC
Rev'd by	
R&D CHK	TITLE: Fenway2.0
DOC CTRL CHK	
MFG ENGR CHK	HDD & MB PWR & SWITCH

Changed by: [] Date Changed: Wednesday, January 23, 2002 Time Changed: 10:05:24 pm
 REV: [] Drawing Number: [] Sheet 29 of 51

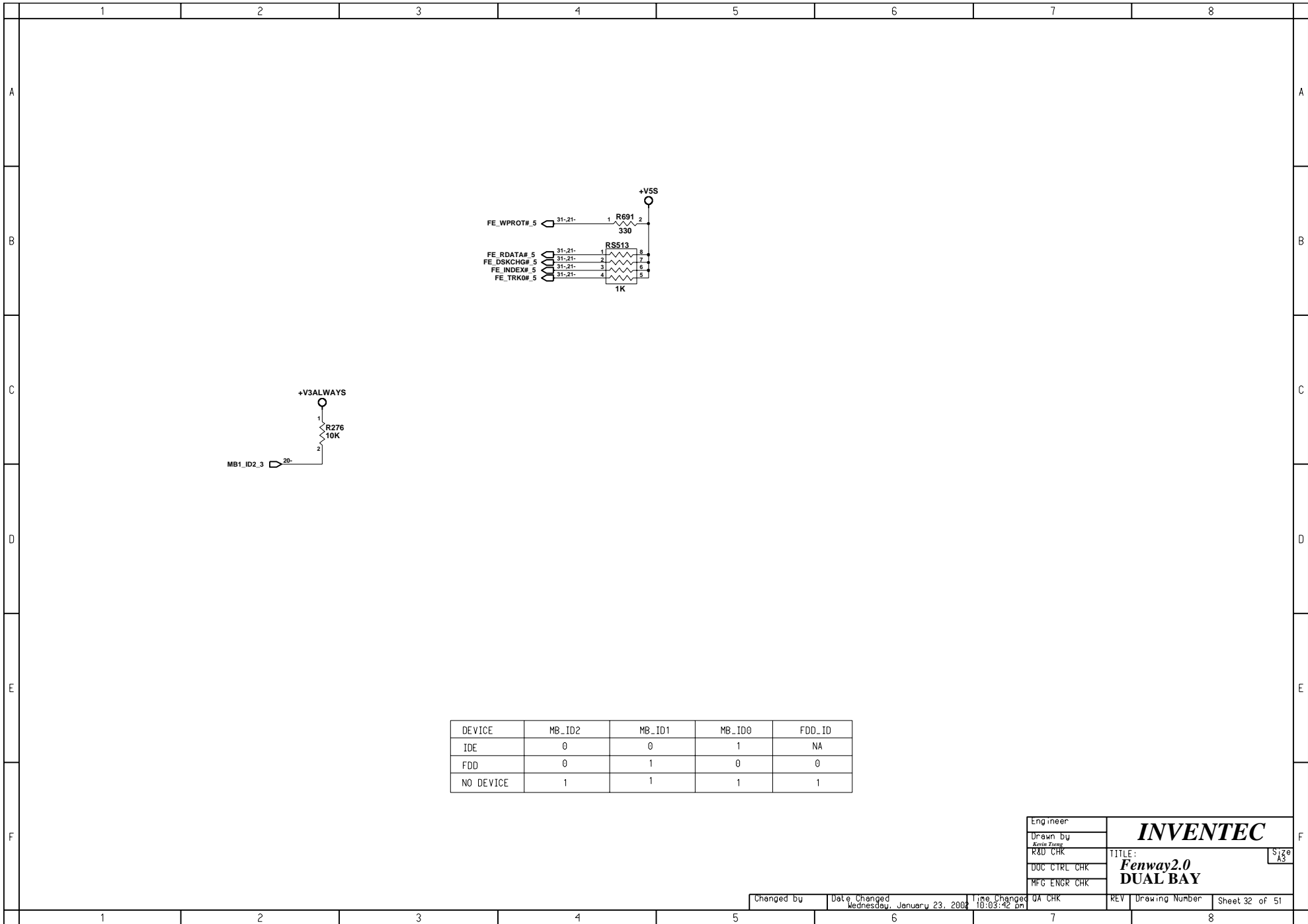


Engineer	INVENTEC	
Drawn by		
Rev'd		
DOC CTRL		
MFG ENGR		
TITLE: Fenway2.0 HDD CN	Size	

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 30 of 51
	Tuesday, May 30, 2002	11:48:16 am				



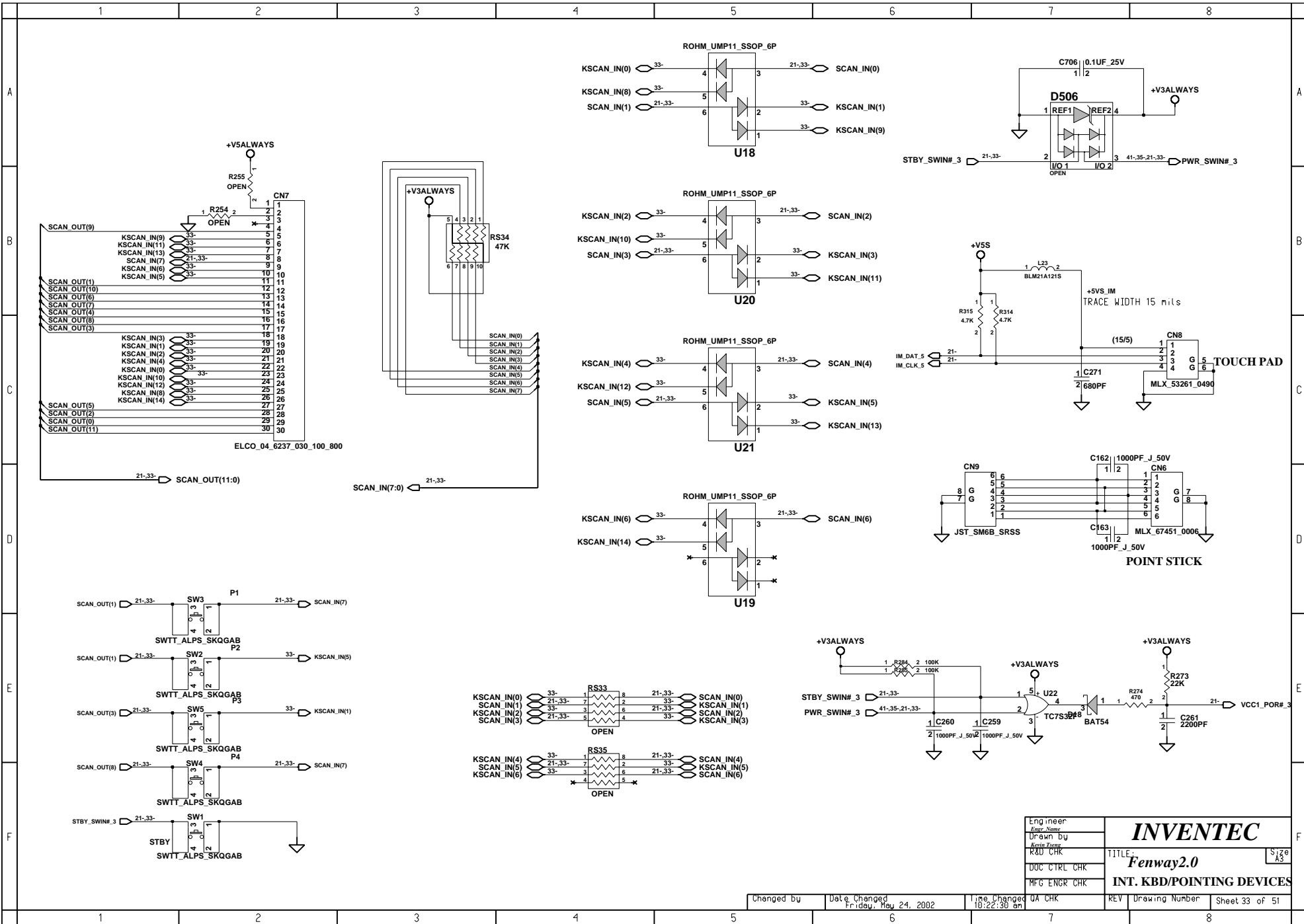
Engineer	
Drawn by	
Rev'd	
RDW CHK	TITLE: Fenway2.0
DOC CTRL CHK	SIZE: A3
MFG ENGR CHK	



DEVICE	MB_ID2	MB_ID1	MB_ID0	FDD_ID
IDE	0	0	1	NA
FDD	0	1	0	0
NO DEVICE	1	1	1	1

Engineer	INVENTEC	
Drawn by		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK	TITLE: Fenway2.0 DUAL BAY	Size

Changed by	Date Changed	Time Changed	REV	Drawing Number	Sheet 32 of 51
	Wednesday, January 23, 2008	10:05:42 am	0A CHK		



Eng Inveer	
Drawn by	
Rev'd	
DOC CTRL	
MFG ENGR	
INVENTEC	
TITLE: Fenway2.0	
INT. KBD/POINTING DEVICES	

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 33 of 51
	Friday, May 24, 2002	10:22:30 am				

1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

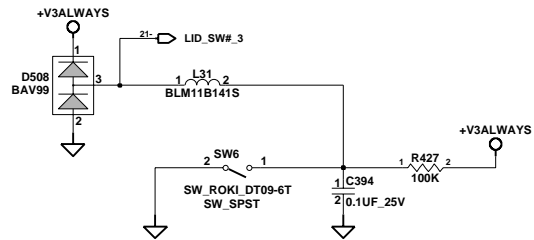
B

C

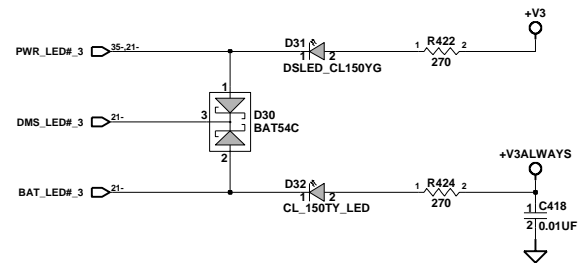
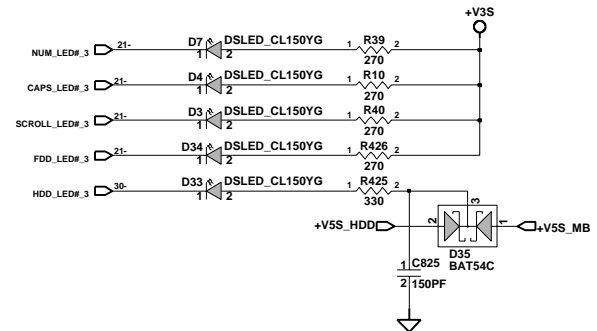
D

E

F



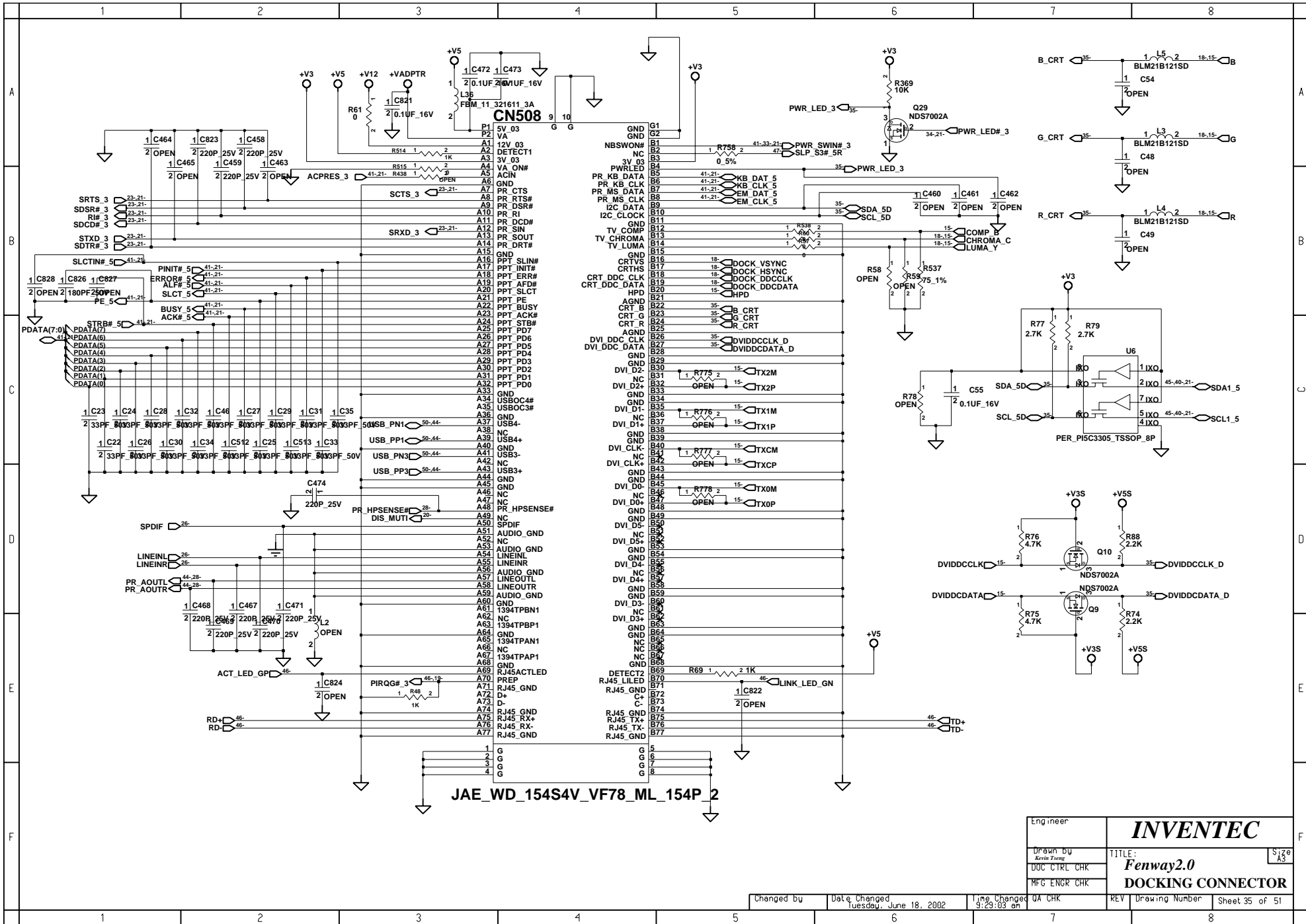
LID SWITCH



Engineer	INVENTEC	
Drawn by Kevin Tseng	TITLE: Fenway2.0	Size A3
DOC CTRL CHK	LID SWITCHES / LEDS	
MFG ENGR CHK		

Changed by	Date Changed Thursday, May 30, 2002	Time Changed 6:33:31 pm	QA CHK	REV	Drawing Number	Sheet 34 of 51
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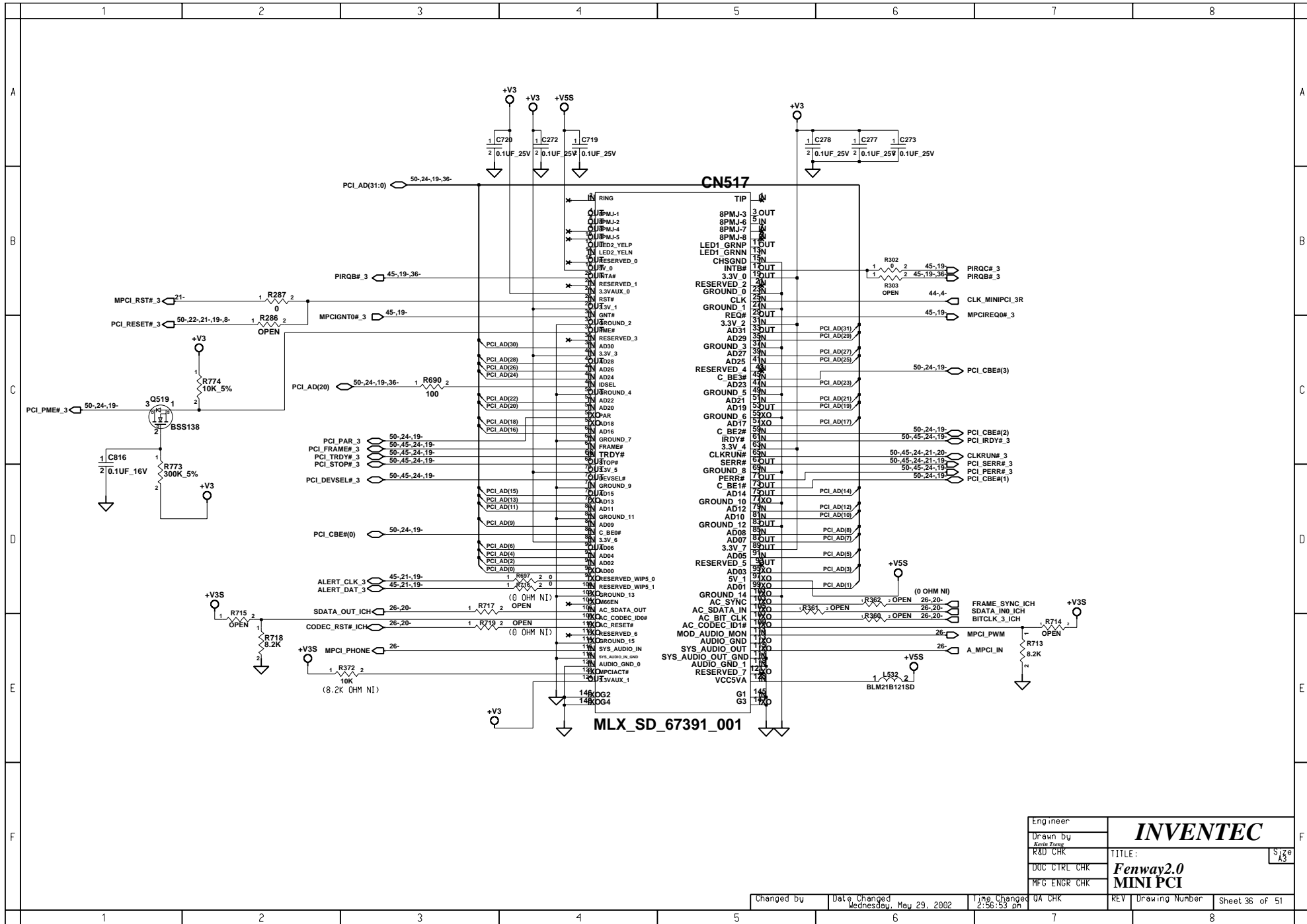
1 2 3 4 5 6 7 8



JAE_WD_154S4V_VF78_ML_154P_2

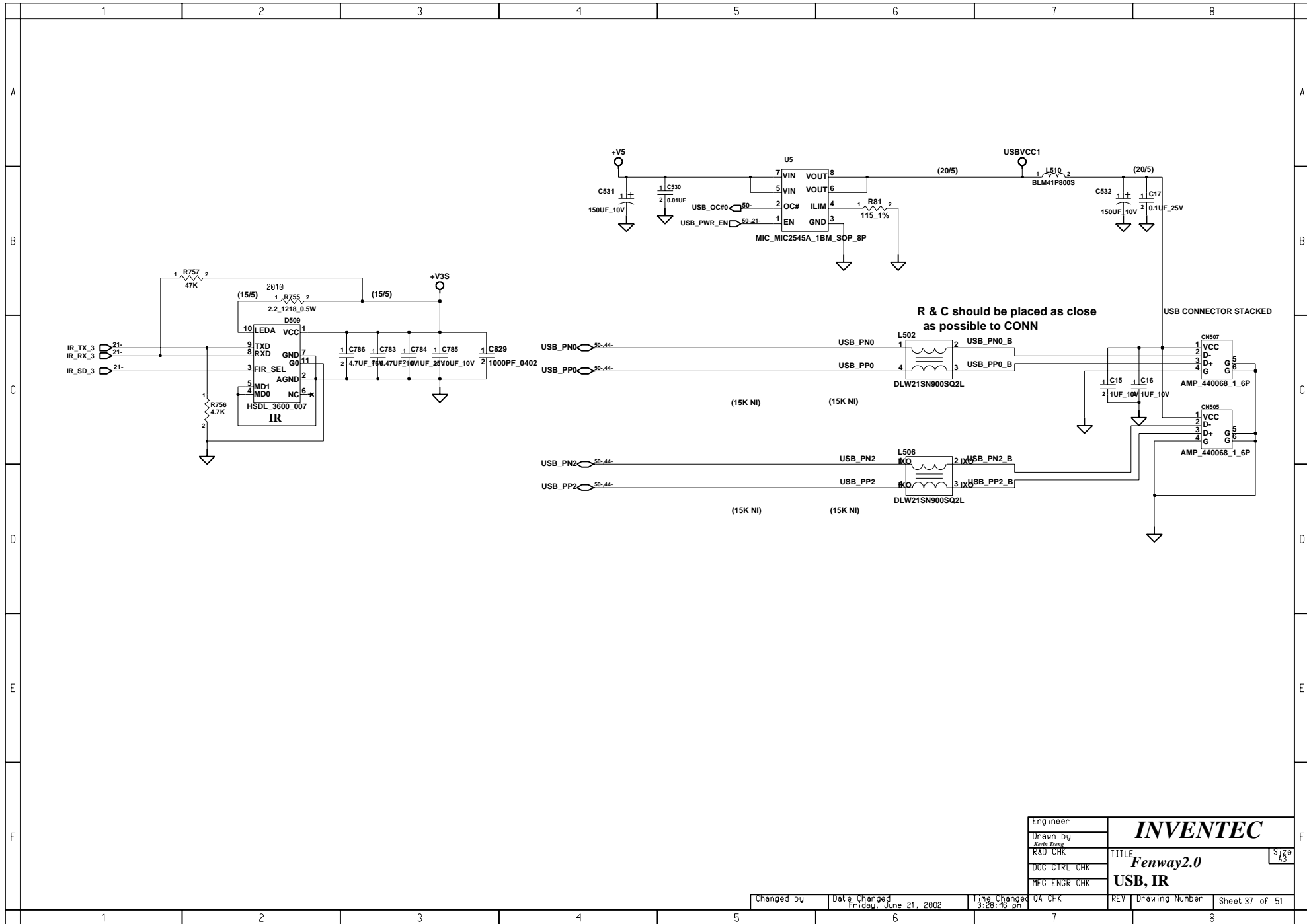
Engineer	INVENTEC	
Drawn by Kevin Tseng	TITLE: Fenway2.0	Size A3
DOC CTRL CHK	DOCKING CONNECTOR	
MFG ENGR CHK		

Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:29:03 am	UA CHK	REV	Drawing Number	Sheet 35 of 51
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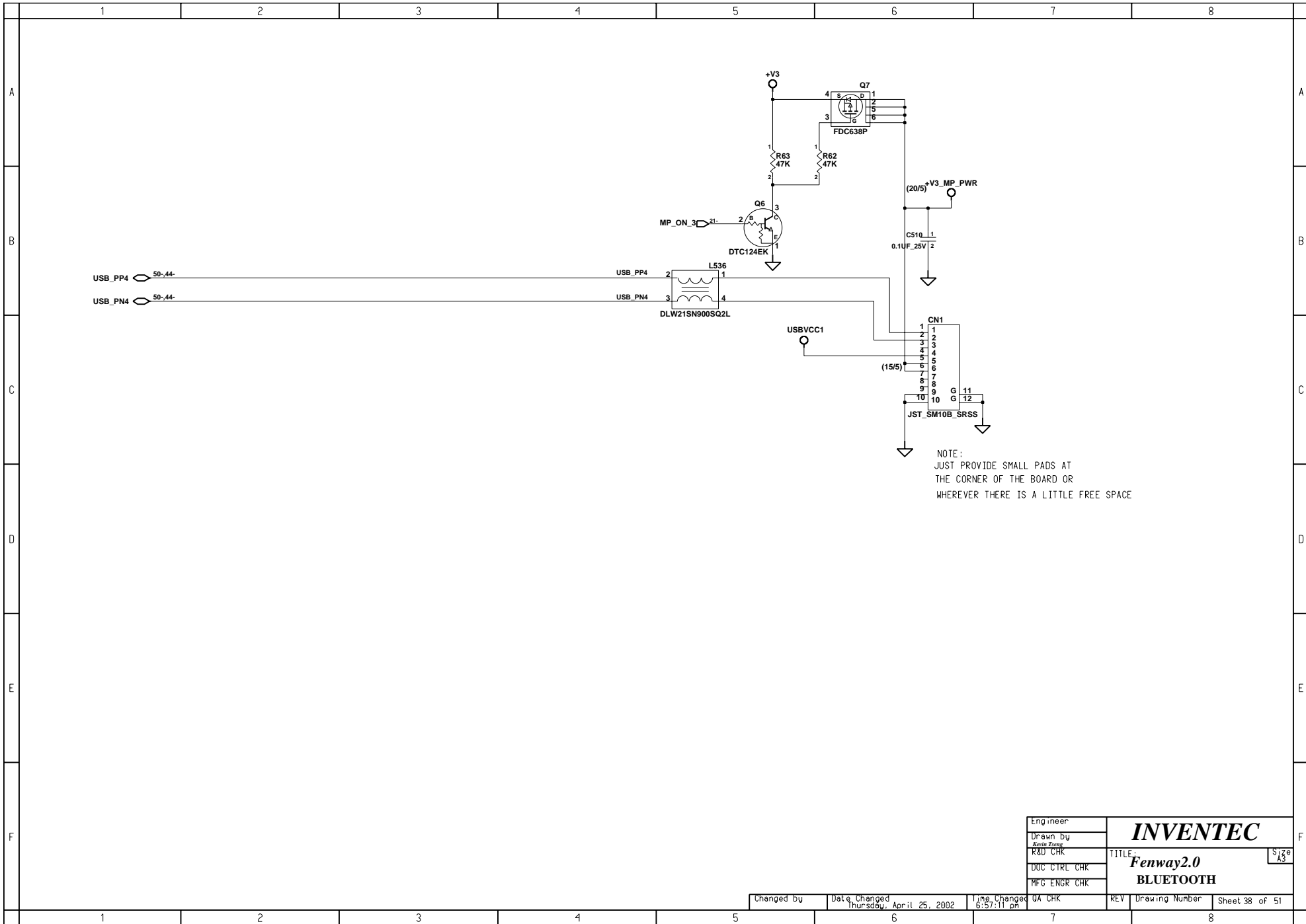
Eng Ineer	
Drawn by	INVENTEC
Rev'd	
DOC CTRL CHK	TITLE: Fenway2.0
MFG ENGR CHK	MINI PCI
Changed by	REV
Date Changed	Drawing Number
Time Changed	Sheet 36 of 51

Changed by _____ Date Changed **Wednesday, May 29, 2002** Time Changed **2:56:53 pm** UA CHK
 Drawing Number _____ Sheet 36 of 51



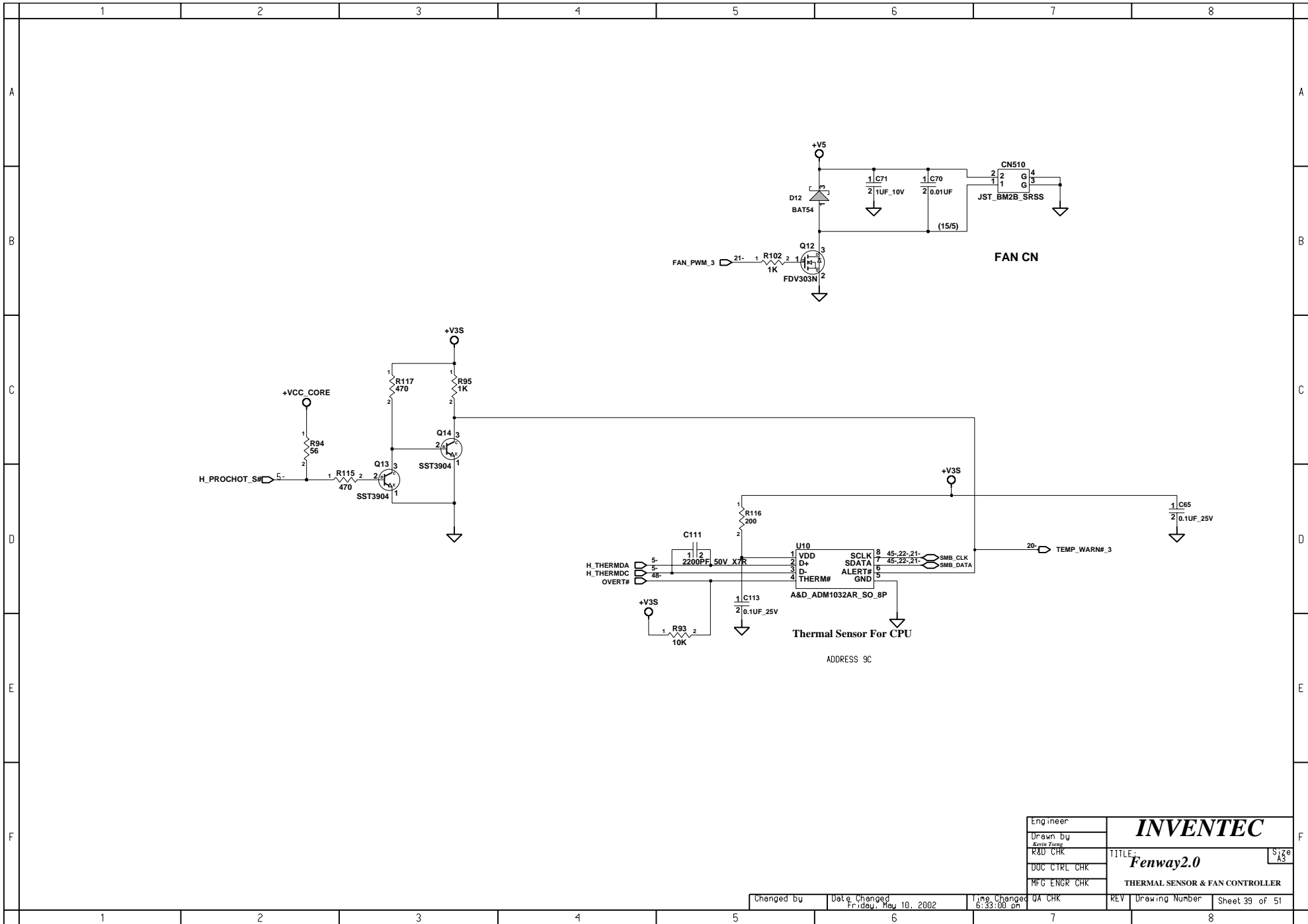
Engineer	INVENTEC		TITLE: Fenway2.0	Sheet 37 of 51
Drawn by				
Rev'd				
DOC CTRL				
MFG ENGR				
REV	Drawing Number	Sheet 37 of 51		

Changed by: [] Date Changed: Friday, June 21, 2002 Time Changed: 3:28:46 pm



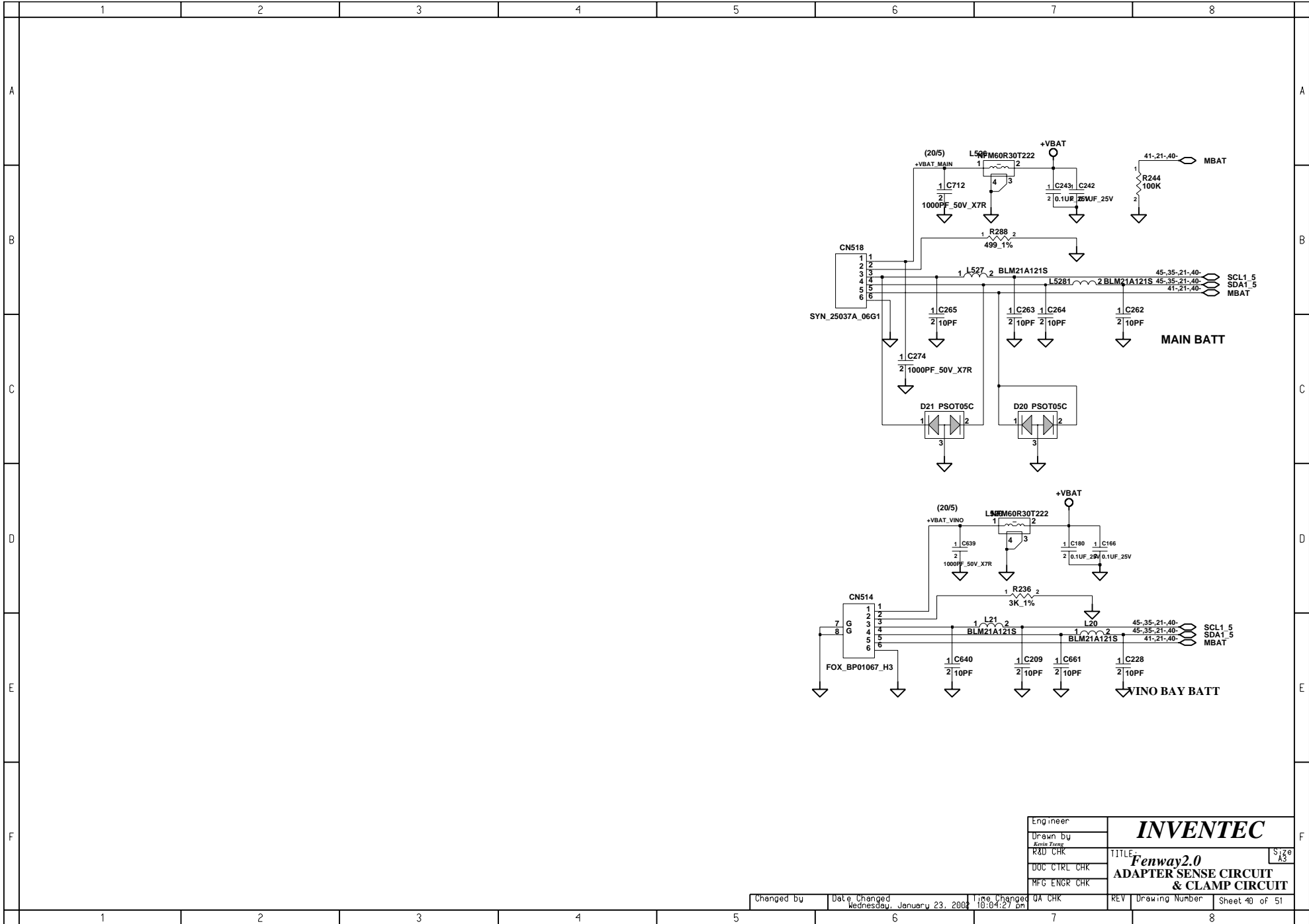
Engineer	INVENTEC	
Drawn by	Kevin Tseng	
R&D CHK	TITLE: Fenway2.0	Size A3
DOC CTRL CHK	BLUETOOTH	
MFG ENGR CHK		

Changed by	Date Changed Thursday, April 25, 2002	Time Changed 6:57:11 PM	QA CHK	REV	Drawing Number	Sheet 38 of 51
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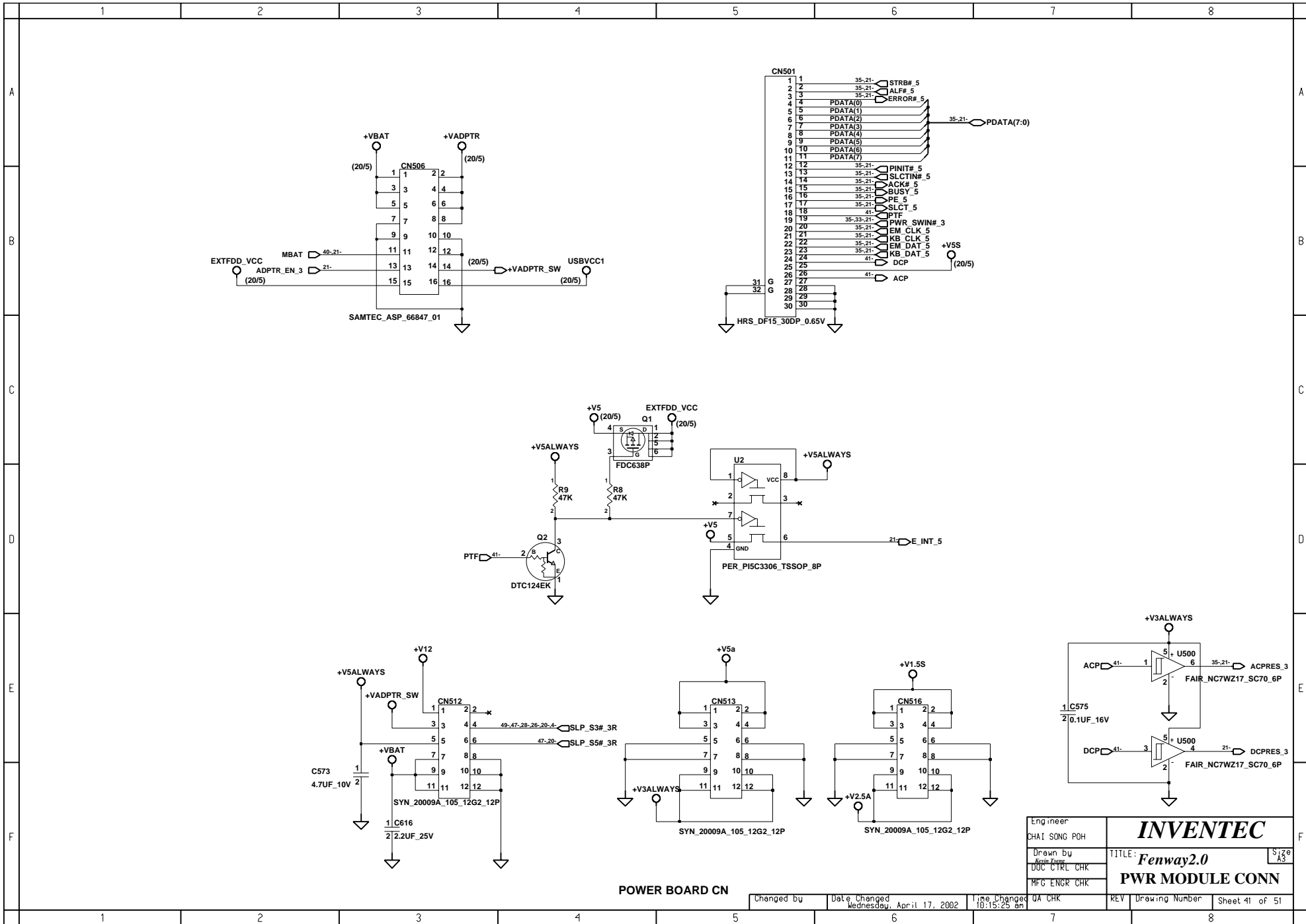
Engineer	INVENTEC
Drawn by	
Rev'd by	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	THERMAL SENSOR & FAN CONTROLLER
TITLE: Fenway2.0 Size	

Changed by	Date Changed Friday, Nov 10, 2002	Time Changed 6:33:00 pm	QA CHK	REV	Drawing Number	Sheet 39 of 51
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Engineer	INVENTEC	
Drawn By		
Rev'd By		
DOC CTRL CHK		
MFG ENGR CHK		
TITLE: Fenway2.0	Size	
ADAPTER SENSE CIRCUIT & CLAMP CIRCUIT		A3

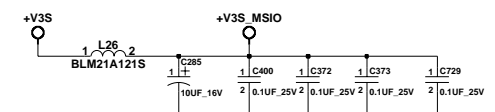
Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 40 of 51
	Wednesday, January 23, 2008	10:04:27 am				



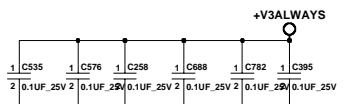
POWER BOARD CN

Engineer CHAI SONG POH	INVENTEC	
Drawn by Kuo Jeng		
DOC CTRL CHK	TITLE: Fenway2.0	Size A3
MFG ENGR CHK	PWR MODULE CONN	
Changed by	Date Changed Wednesday, April 17, 2002	Time Changed 10:15:25 am
UA CHK	REV	Drawing Number
		Sheet 41 of 51

A

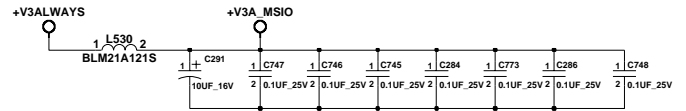


PLACE CAPS NEAR TO KAHUNA

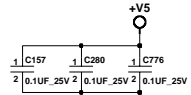


DECOPULING CAPACITORS FOR EMI

B

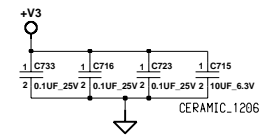


DECOPULING CAPACITORS FOR KAHUNA

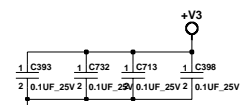


DECOPULING CAPACITORS FOR EMI

C

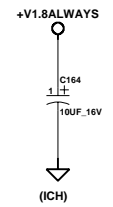


DECOPULING CAPS FOR CARDBUS CONTROLLER

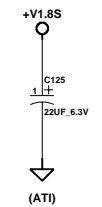


DECOPULING CAPACITORS FOR EMI

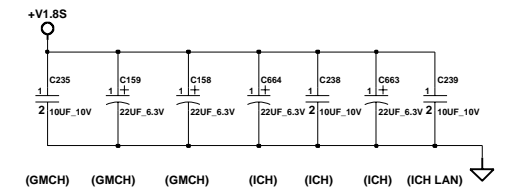
D



(ICH)

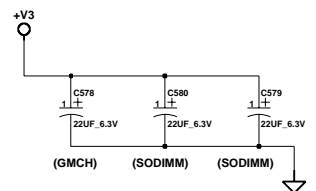


(ATI)

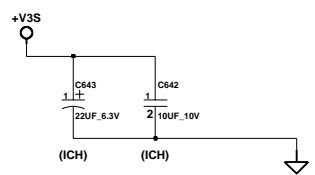


(GMCH) (GMCH) (GMCH) (ICH) (ICH) (ICH) (ICH LAN)

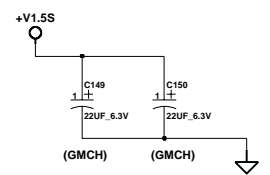
E



(GMCH) (SODIMM) (SODIMM)



(ICH) (ICH)

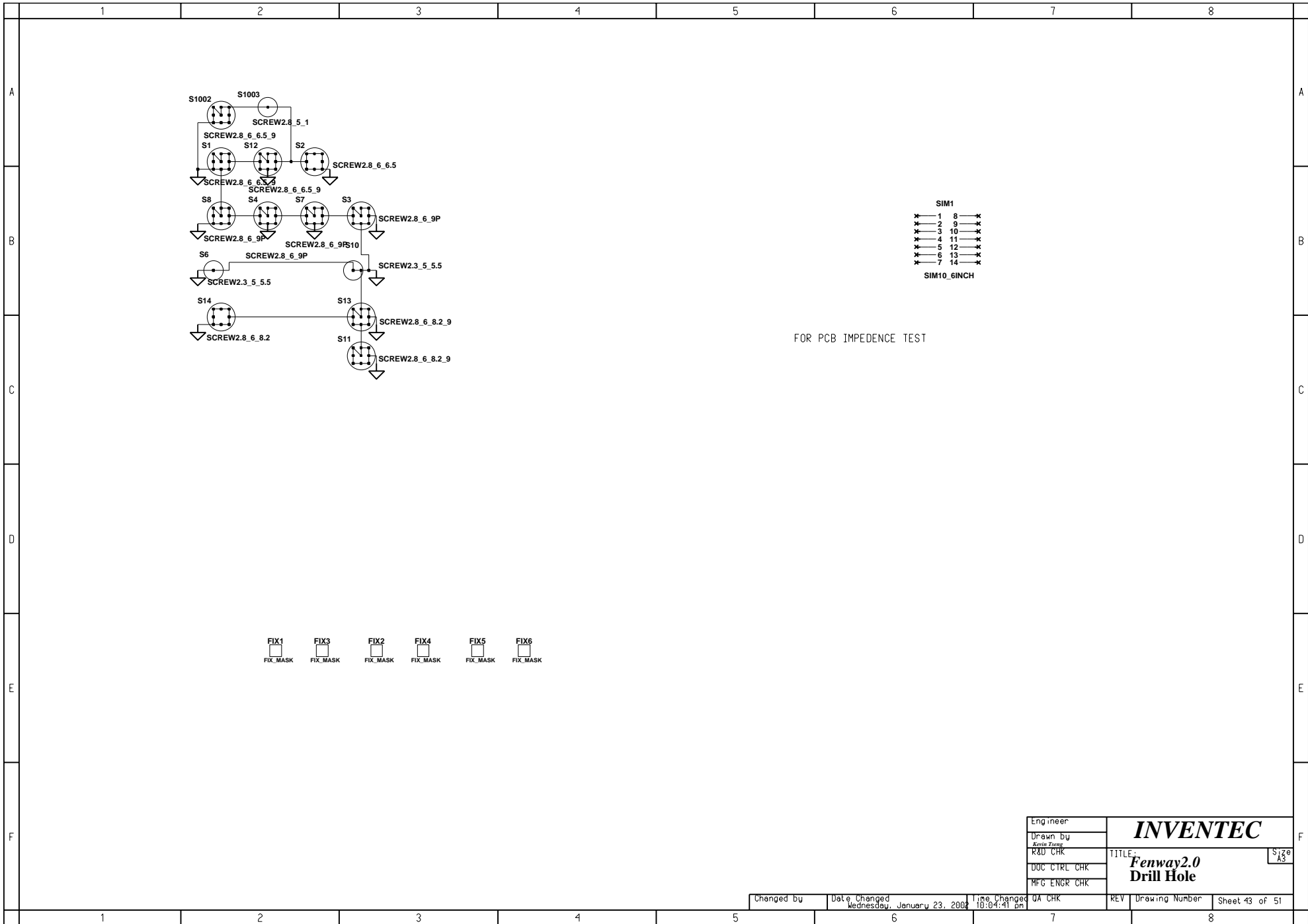


(GMCH) (GMCH)

F

Eng ineer	INVENTEC TITLE: Fenway2.0 DECOPULING CAPS	Size
Drawn by		AS
Revise		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by	Date Changed	Time Changed	QA CHK	REV	Drawing Number	Sheet 42 of 51
EE1	Friday, April 26, 2002	2:43:49 pm				



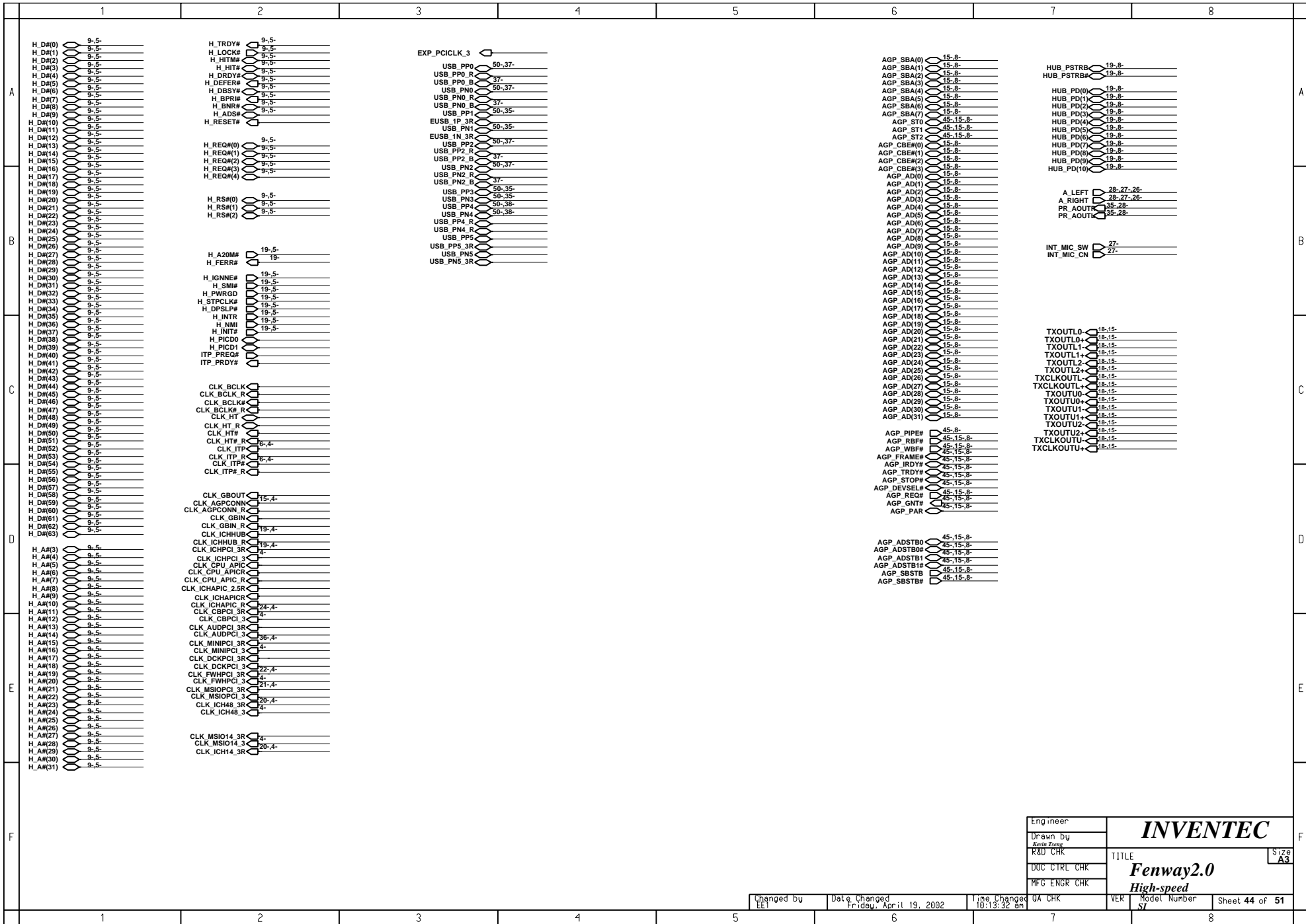
- SIM1
- * 1 8 *
 - * 2 9 *
 - * 3 10 *
 - * 4 11 *
 - * 5 12 *
 - * 6 13 *
 - * 7 14 *
- SIM10_6INCH

FOR PCB IMPEDENCE TEST

- FIX1
FIX_MASK
- FIX3
FIX_MASK
- FIX2
FIX_MASK
- FIX4
FIX_MASK
- FIX5
FIX_MASK
- FIX6
FIX_MASK

Engineer	INVENTEC	
Drawn by <i>Kevin Young</i>		
R&D CHK	TITLE: Fenway2.0	Size
DOC CTRL CHK	Drill Hole	
MFG ENGR CHK		

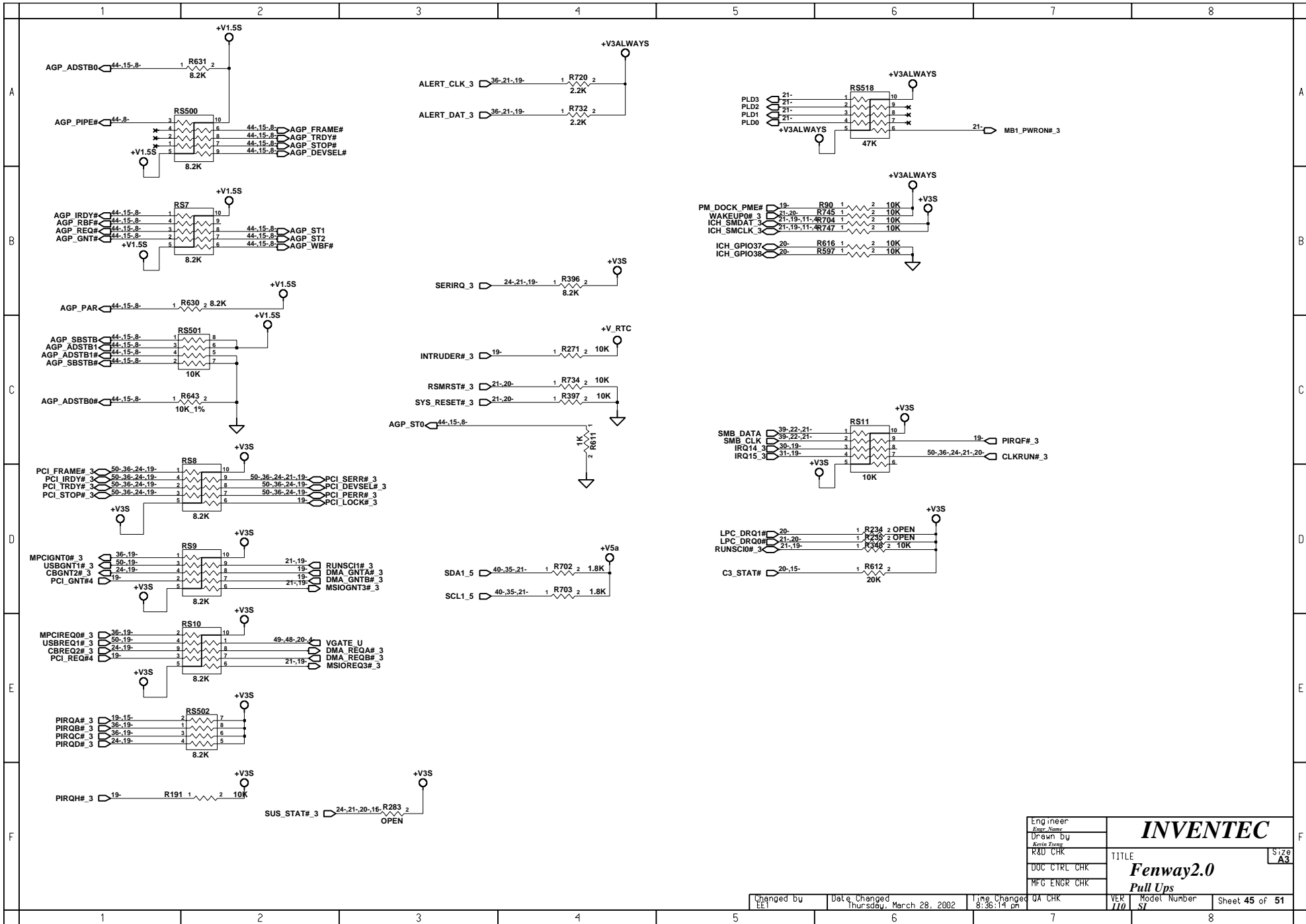
Changed by	Date Changed Wednesday, January 23, 2002	Time Changed 10:04:41 AM	REV	Drawing Number	Sheet 43 of 51
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Engineer	
Drawn by	
Rev'd by	
Rev'd Date	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Fenway2.0
Model Number	High-speed
Sheet 44 of 51	

Changed by: EET
 Date Changed: Friday, April 19, 2002
 Time Changed: 10:13:32 am

Size A3



Engineer	
Drawn by	
Rev'd	
Checked	
DOC CTRL	
ENG ENGR	
INVENTEC	
TITLE	S178 A3
Fenway2.0	
Pull Ups	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
EE1	Thursday, March 28, 2002	8:36:14 pm		110	S1	45 of 51

Layout notes:
Use 50 ohm traces.

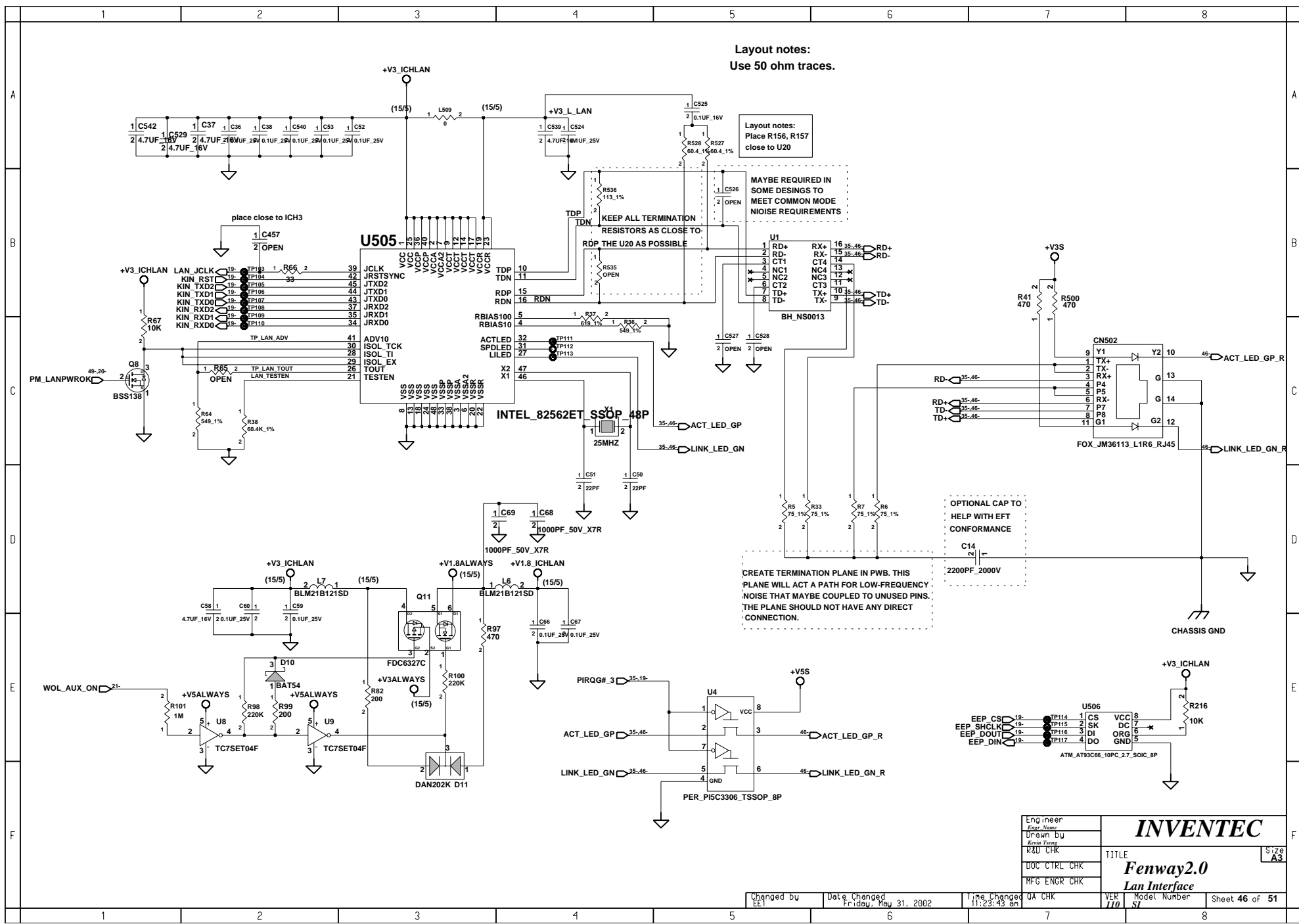
Layout notes:
Place R156, R157
close to U20

MAYBE REQUIRED IN
SOME DESIGNS TO
MEET COMMON MODE
NOISE REQUIREMENTS

KEEP ALL TERMINATION
RESISTORS AS CLOSE TO
RDP THE U20 AS POSSIBLE

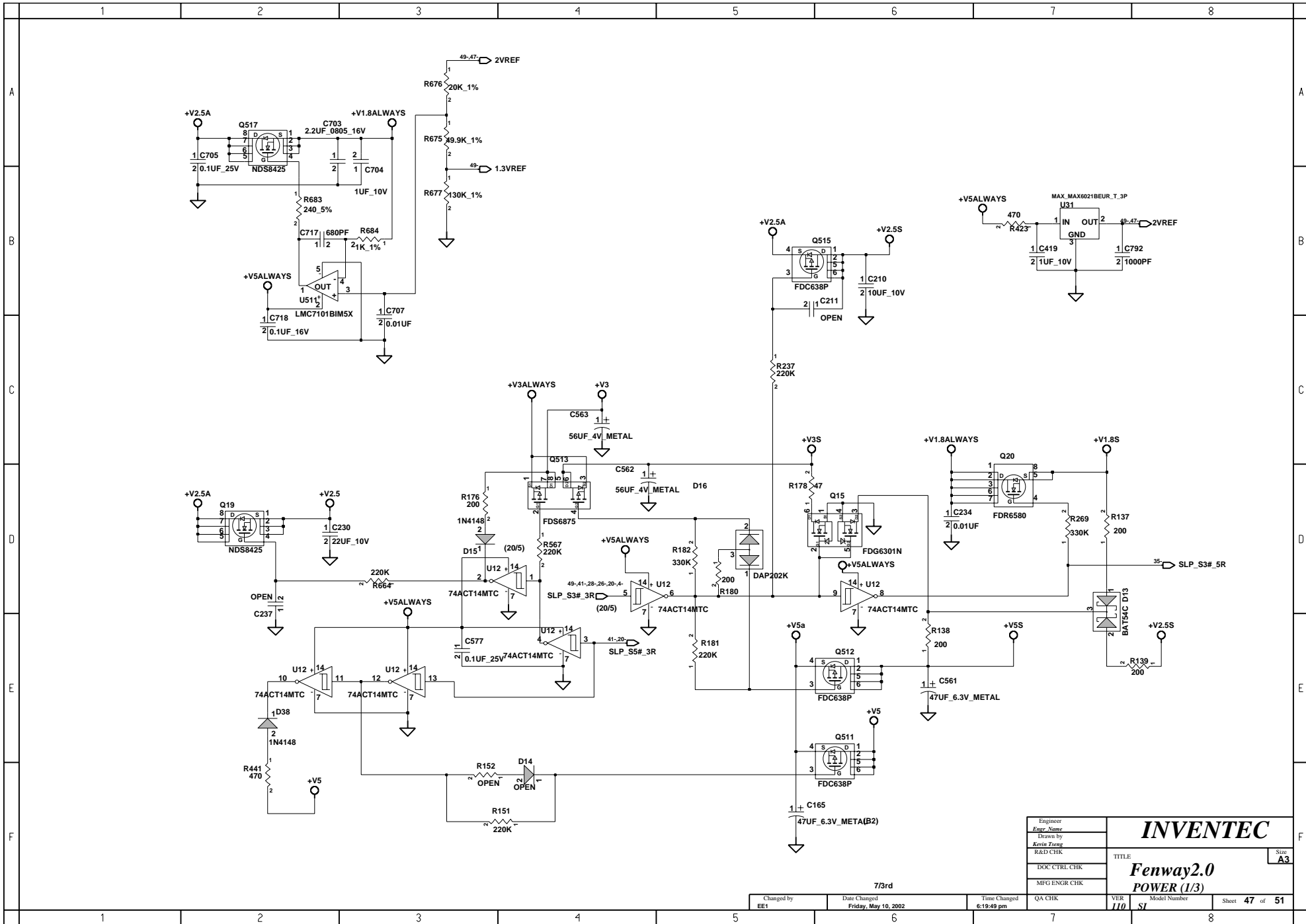
OPTIONAL CAP TO
HELP WITH EFT
CONFORMANCE

CREATE TERMINATION PLANE IN PWB. THIS
PLANE WILL ACT A PATH FOR LOW-FREQUENCY
NOISE THAT MAYBE COUPLED TO UNUSED PINS.
THE PLANE SHOULD NOT HAVE ANY DIRECT
CONNECTION.



Eng Name		INVENTEC
Drawn By		
Rev'd		TITLE
DOC CTRL		Fenway2.0
MFG ENGR		Lan Interface

Changed by	Date Changed	Time Changed	UA CHK	VER	Model Number	Sheet 46 of 51
EET	Friday, Nov 31, 2002	11:23:43 am		110	S1	A3



Engineer Engr Name	
Drawn by Kevin Tseng	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

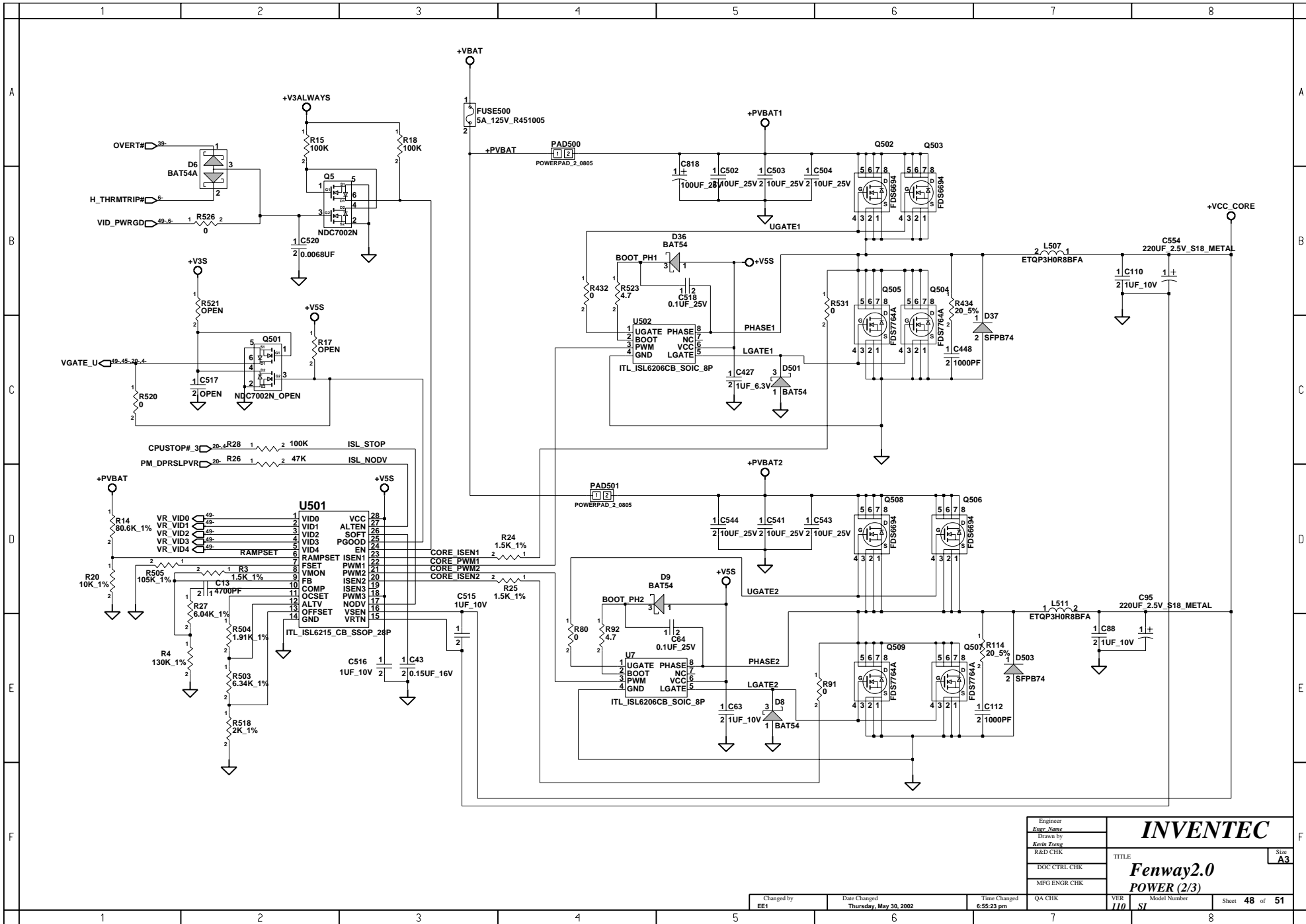
INVENTEC

TITLE: **Fenway2.0**

POWER (1/3)

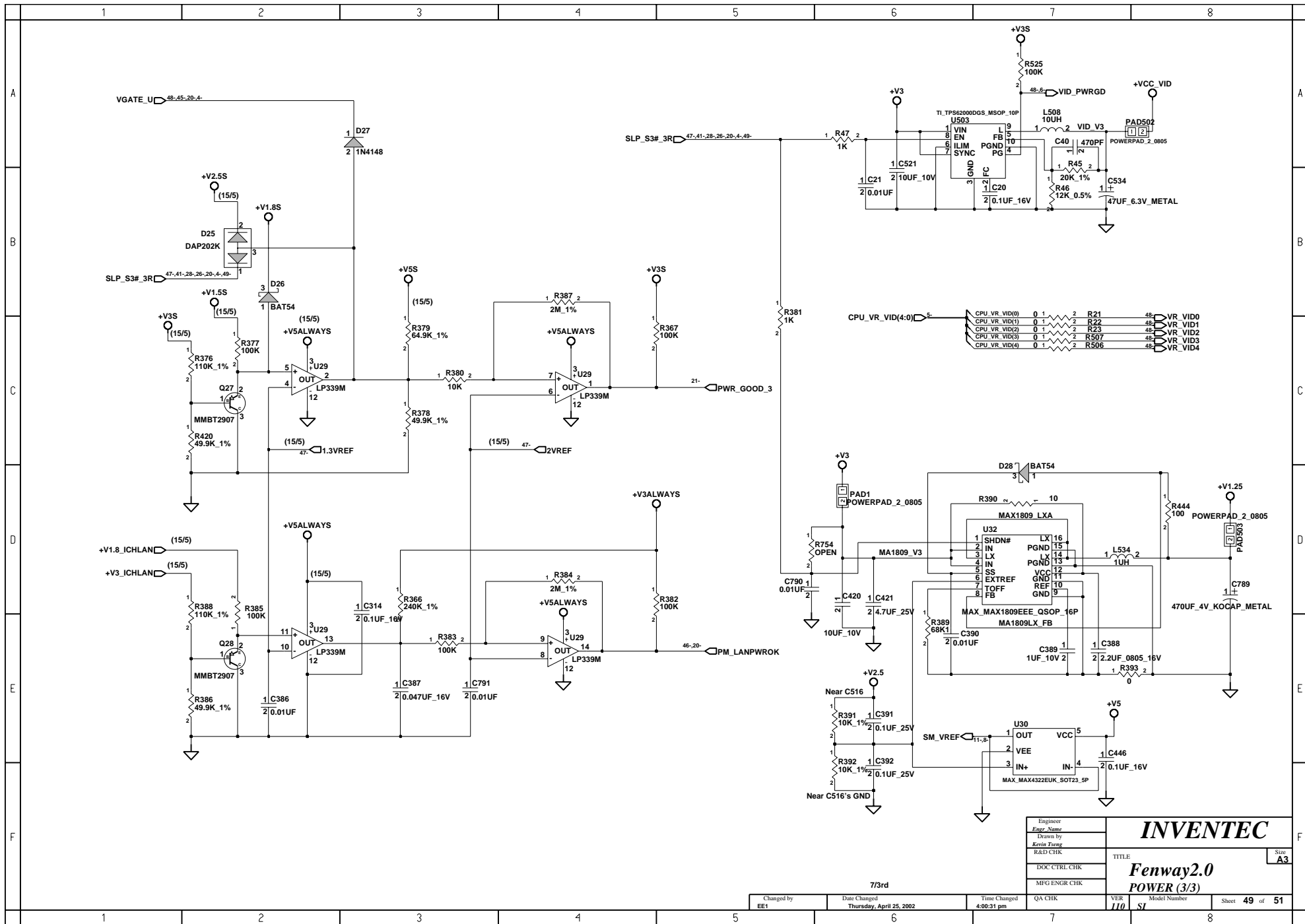
Size: **A3**

Changed by EE1	Date Changed Friday, May 10, 2002	Time Changed 6:19:49 pm	QA CHK	VER 110	Model Number SI	Sheet 47	of 51
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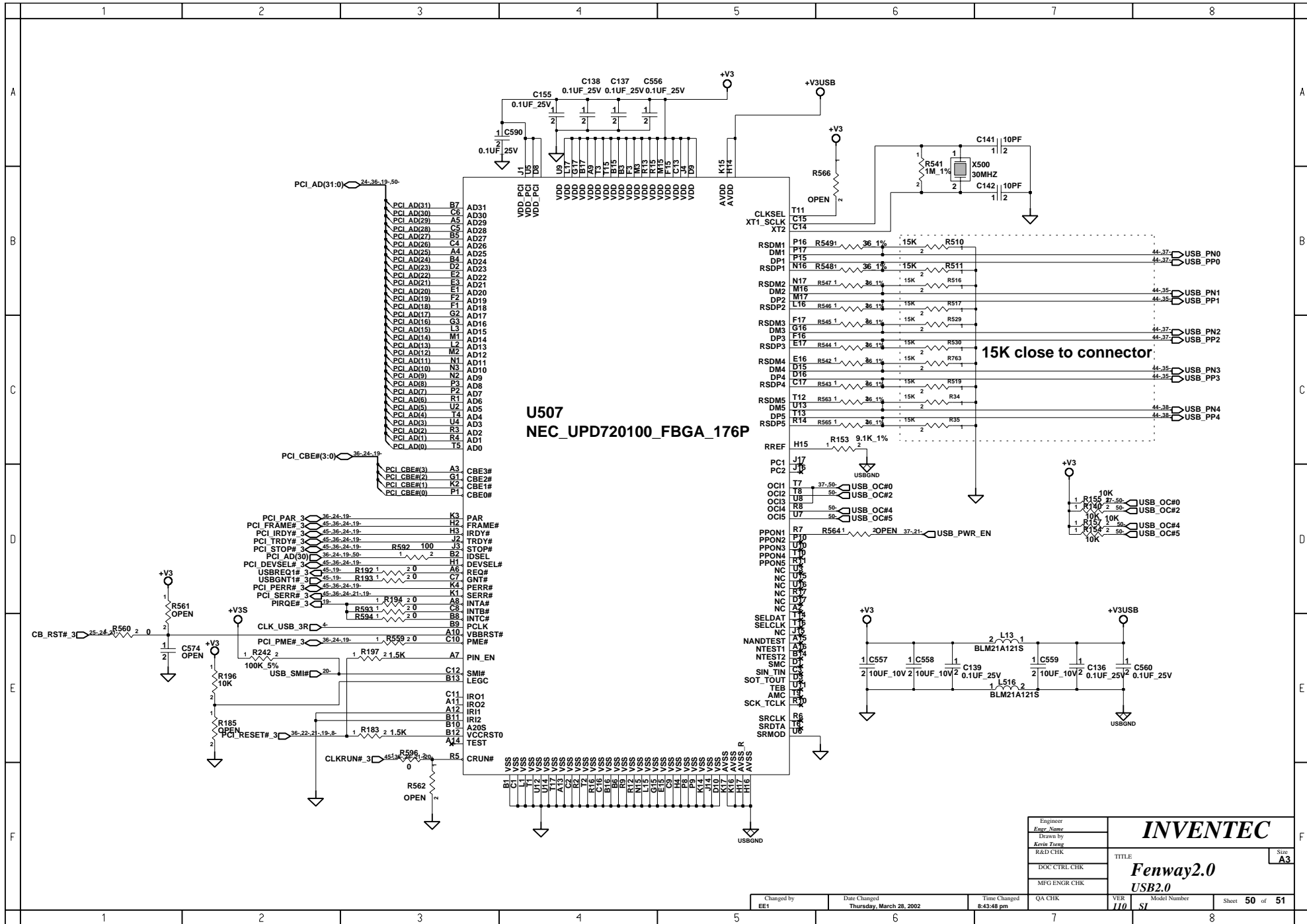
Engineer	
Engr Name	
Drawn by	Kevin Tseng
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Fenway2.0 POWER (2/3)
Size	A3

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
EE1	Thursday, May 30, 2002	6:55:23 pm		110	SI	48 of 51



Engineer <i>Engy Name</i>	INVENTEC Fenway2.0 POWER (3/3)	Model Number
Drawn by <i>Kevin Tseng</i>		Sheet 49 of 51
R&D CHK		Size A3
DOC CTRL CHK		
MFG ENGR CHK		

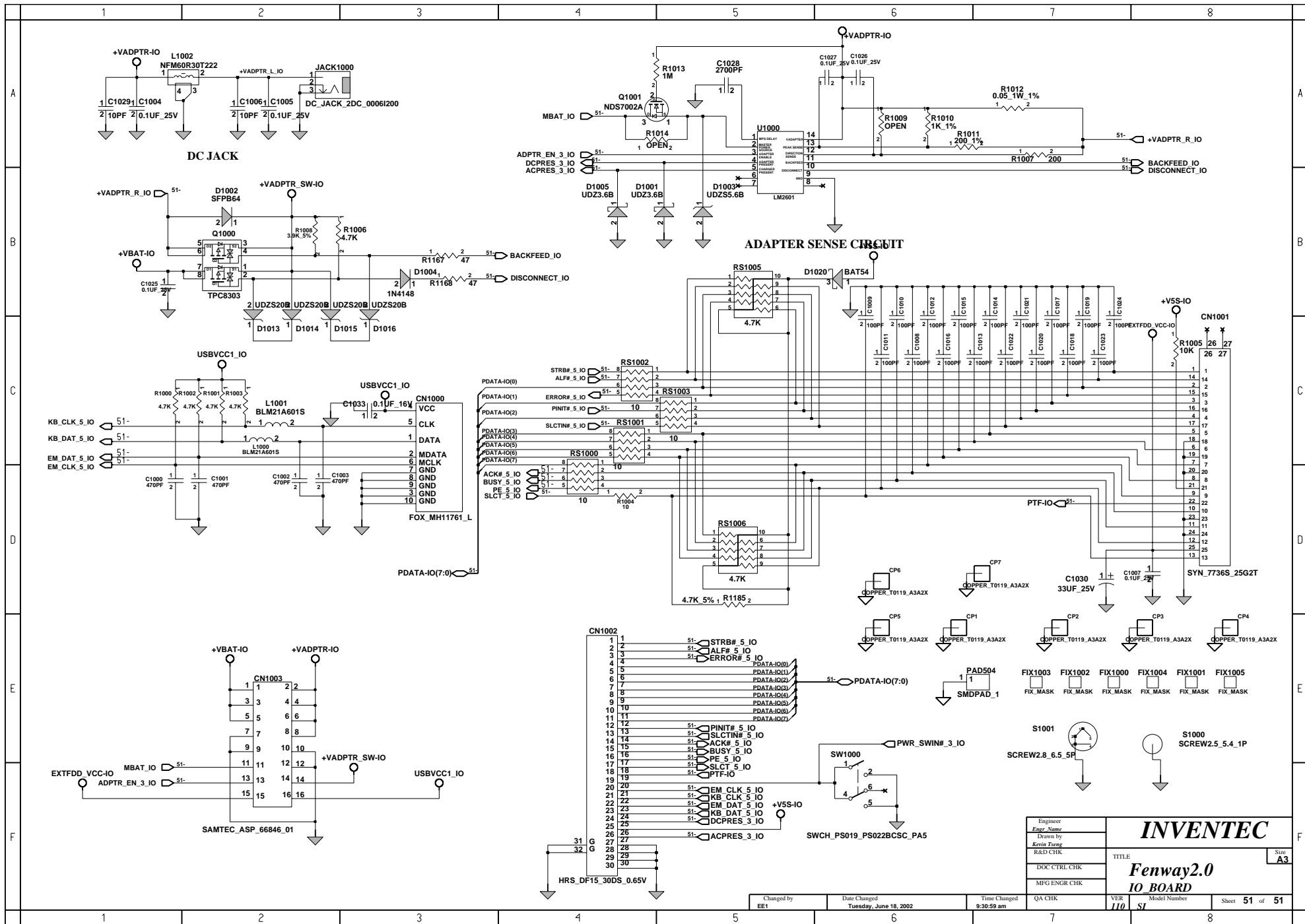
7/3rd			
Changed by EE1	Date Changed Thursday, April 25, 2002	Time Changed 4:00:31 pm	QA CHK



**U507
NEC_UPD720100_FPGA_176P**

Engineer Eng Name		INVENTEC Fenway2.0 USB2.0	Size A3
Drawn by Kevin Tseng			
R&D CHK			TITLE
DOC CTRL CHK			
MFG ENGR CHK			

Changed by EE1	Date Changed Thursday, March 28, 2002	Time Changed 8:43:48 pm	QA CHK	VER 110	Model Number SI	Sheet 50	of 51
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Engineer	
Engy Name	
Drawn by	
Rev'd CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	Fenway2.0 IO BOARD
Model Number	
VER	110 SI
QA CHK	
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