

INVENTEC

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Preliminary Test

2008/03/25

| DATE | CHANGE NO. | REV |
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| DRAWER | EE | DATE | POWER | DATE | INVENTEC |
| DESIGN | | | | | |
| CHECK | | | | | |
| RESPONSIBLE | | | | | |
| TITLE | | | | | Preliminary Test |
| SIZE = | 3 | VER : | | | |
| FILE NAME : | XXXX XXXXXX-XX | | | | SIZE |
| P/N | XXXXXXXXXXXX | | | | A3 |
| | | | | | CODE |
| | | | | | CS |
| | | | | | DOC. NUMBER |
| | | | | | Model No |
| | | | | | REV |
| | | | | | X01 |
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| 31.N/A | 57.AUDIO AMP & MIC & HP | |
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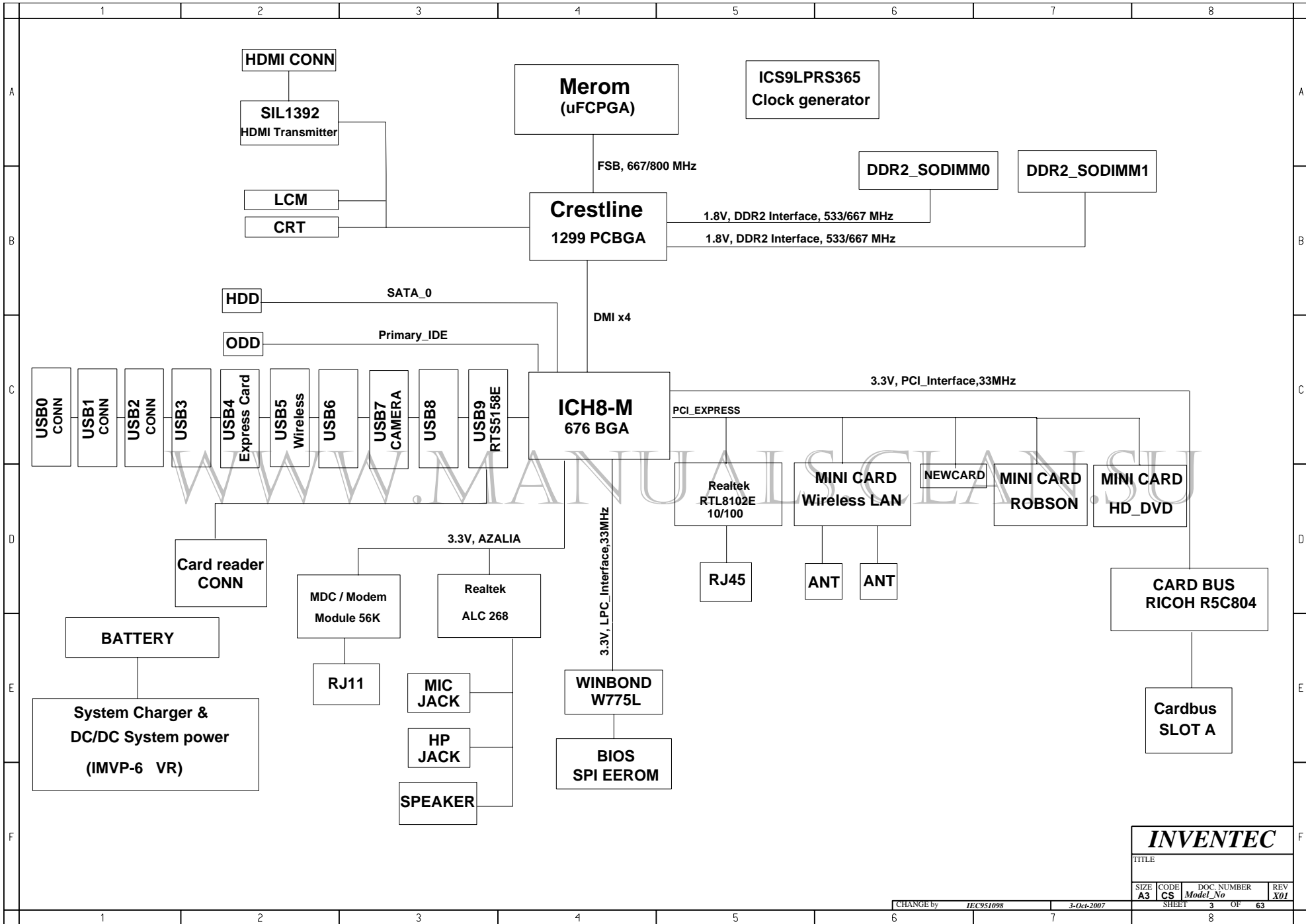
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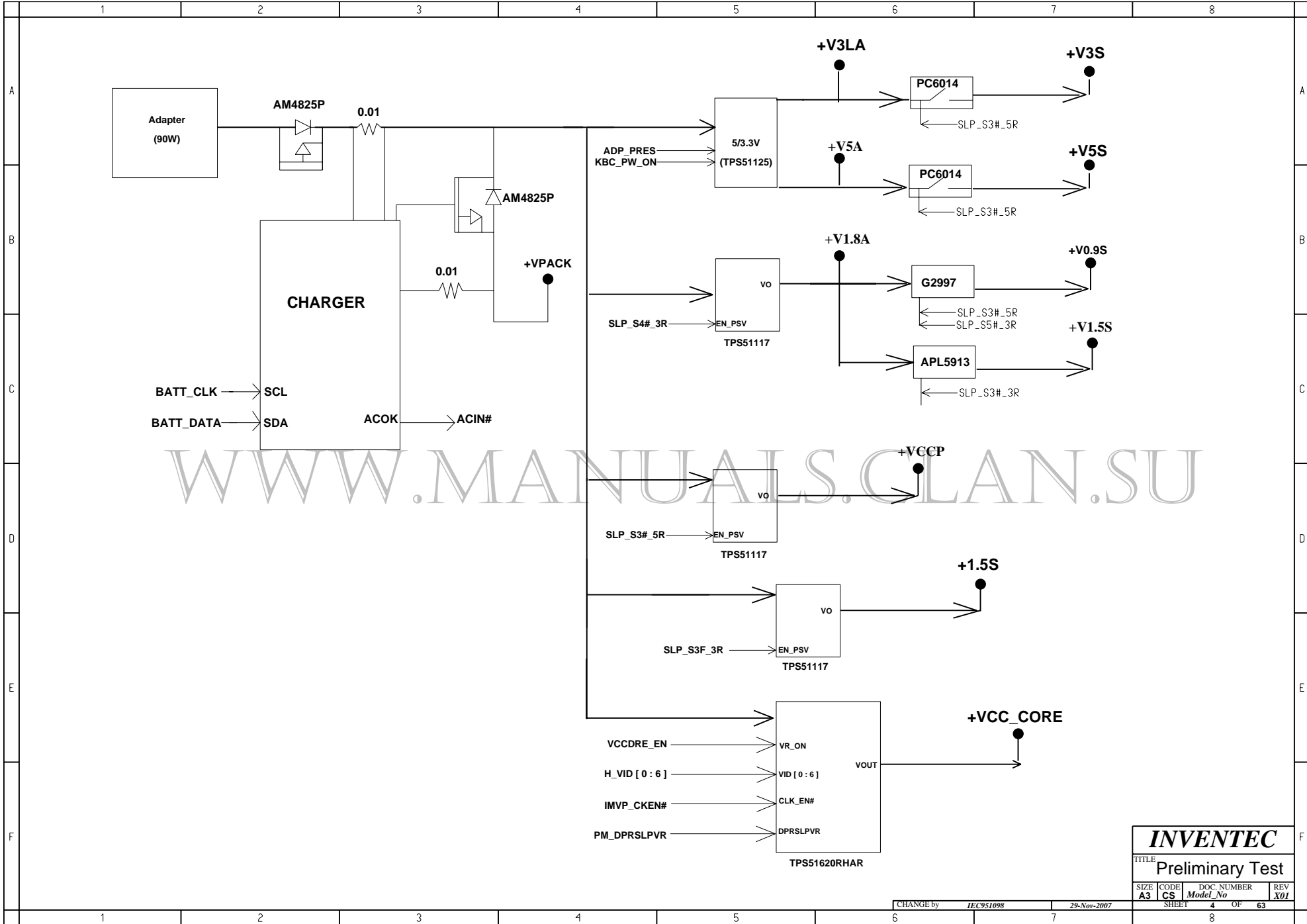
TITLE
Preliminary Test

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| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
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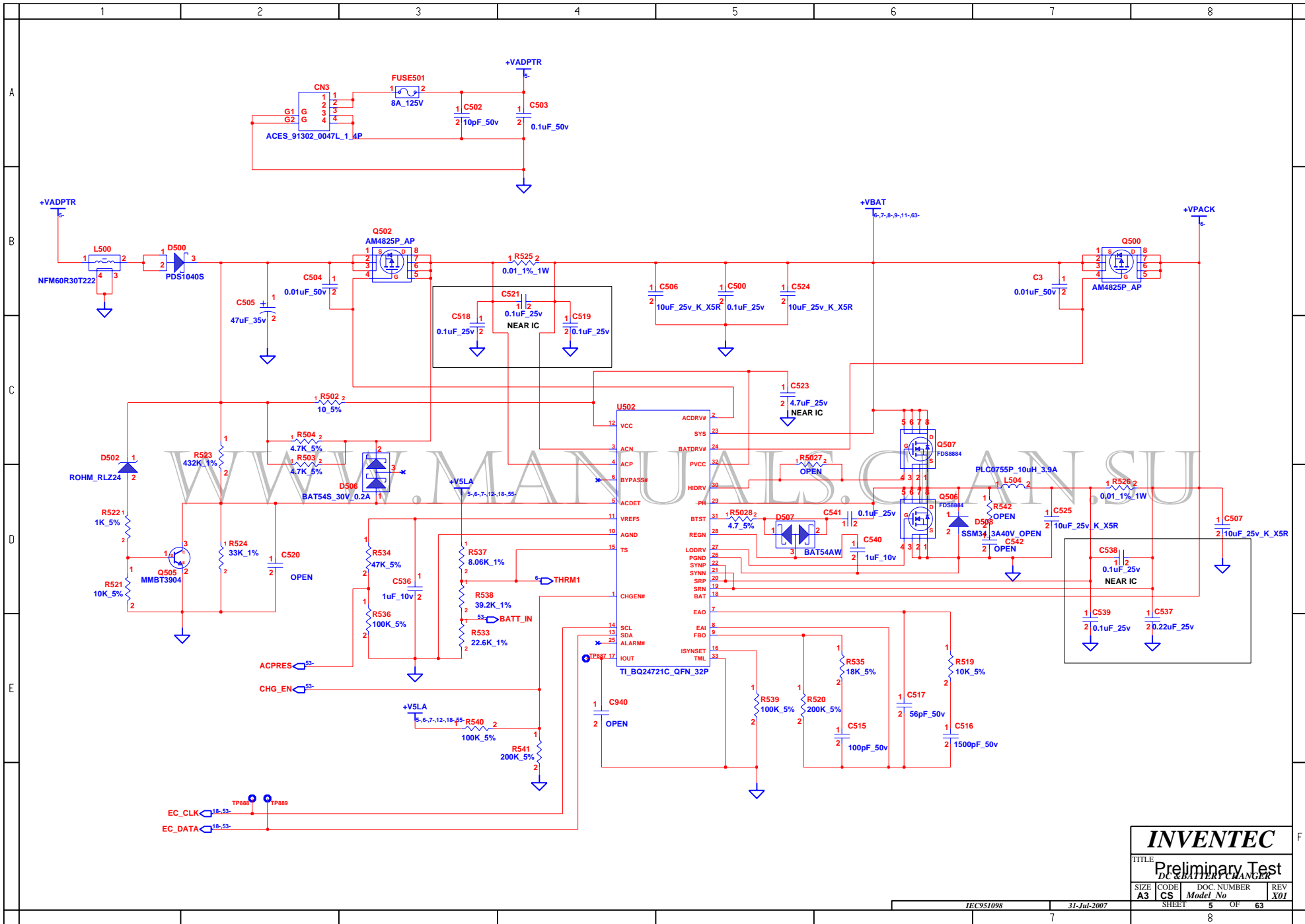
CHANGE by IEC951098 28-Aug-2007

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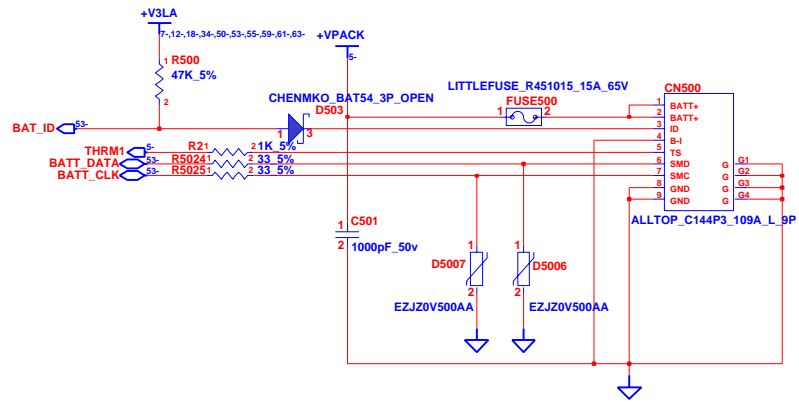




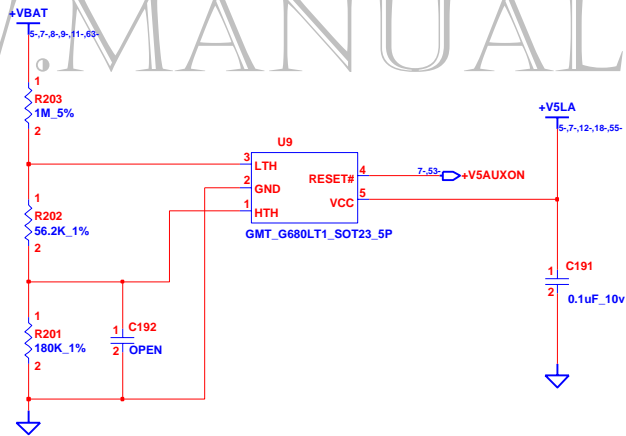
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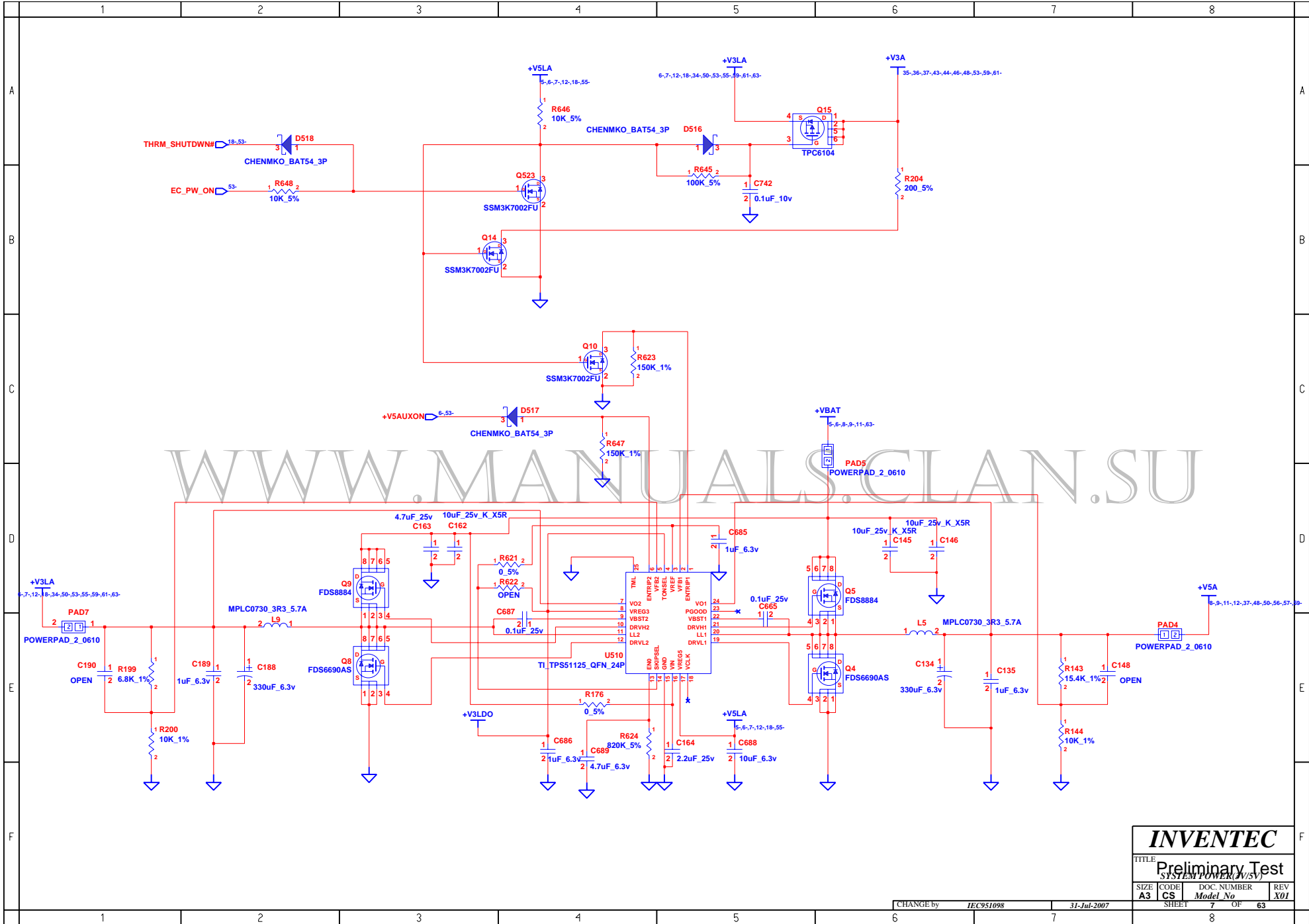
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| TITLE DC & BATTERY CHARGER | | | |
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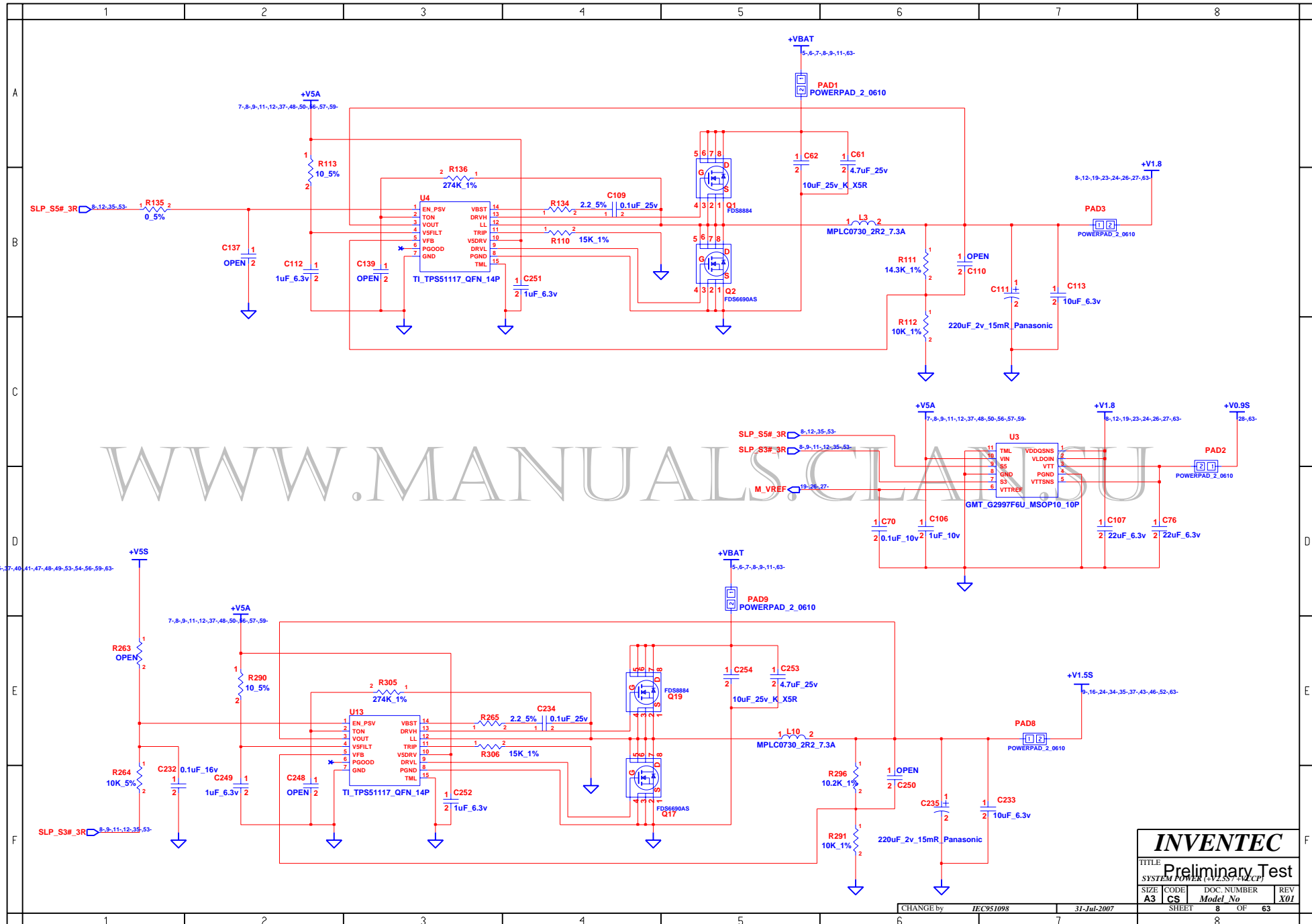
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| TITLE Preliminary Test | | | |
| BATTERY CUMV | | | |
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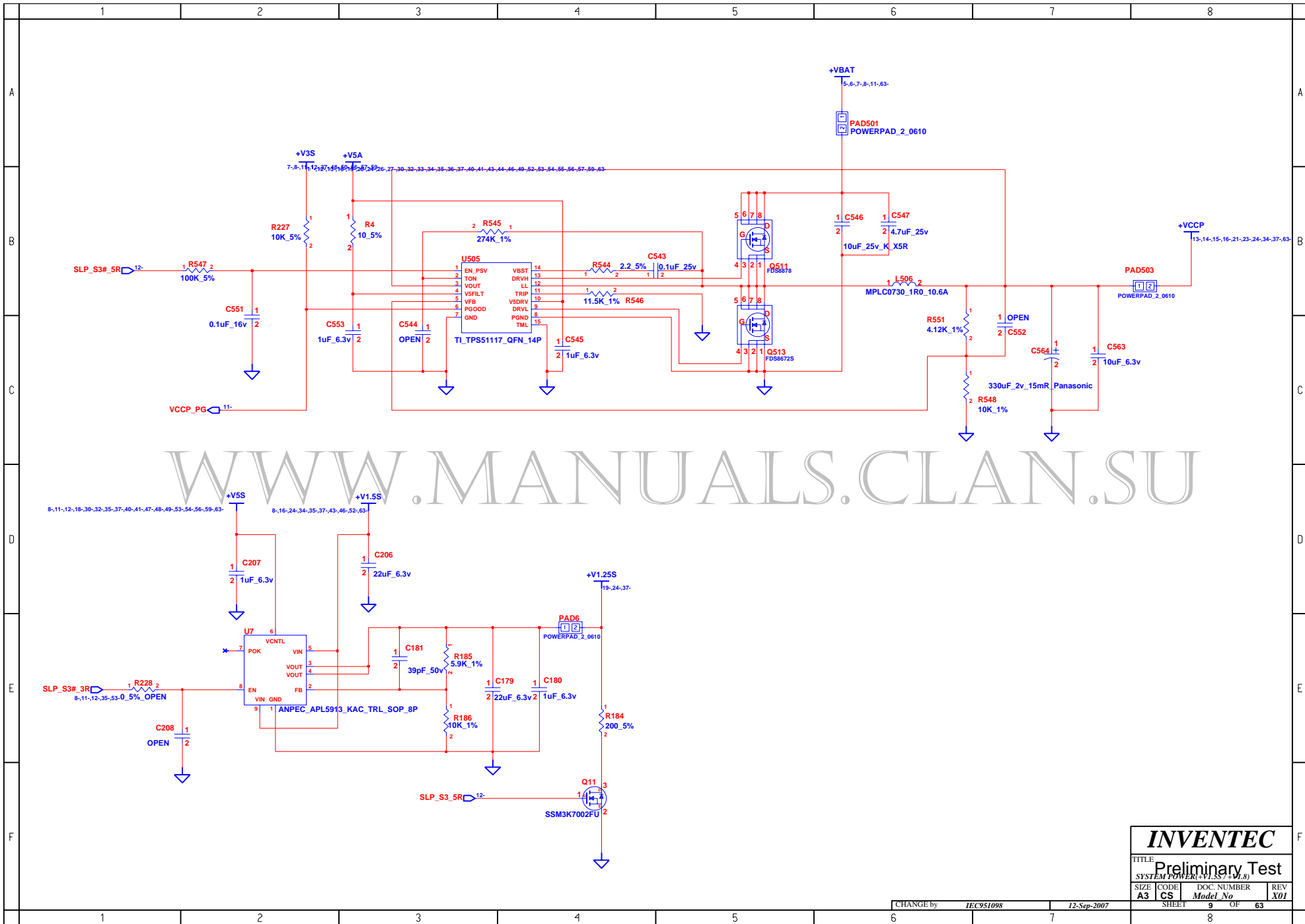
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| SYSTEM POWER (V/SV) | | | |
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 TITLE: Preliminary Test
 SYSTEM POWER (V2.S57-ALCP)

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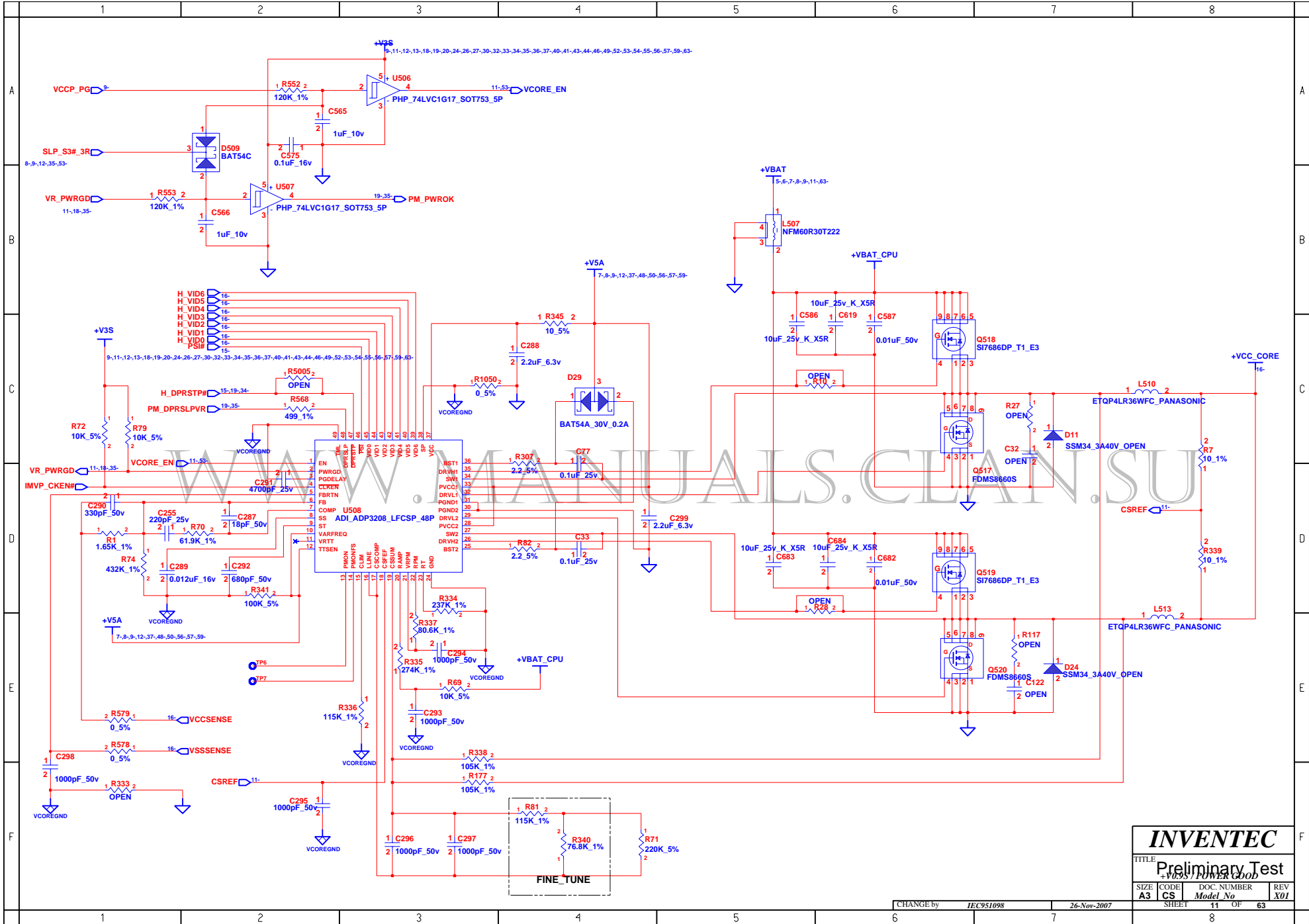
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| SYSTEM POWER(+V1.5S7+V1.8) | | | |
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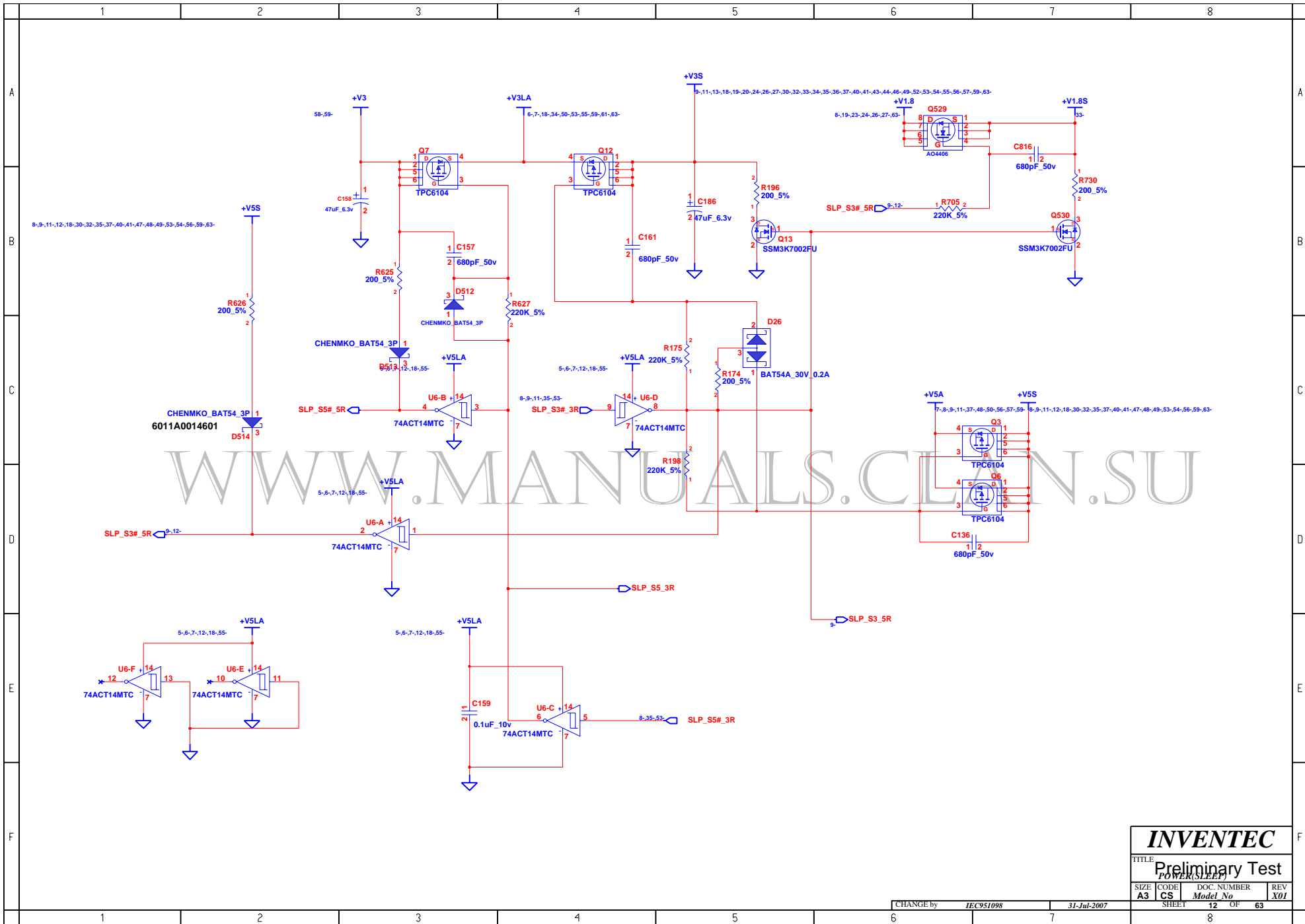
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| TITLE | | | |
| Preliminary Test | | | |
| CPU POWER (VCC CORE) | | | |
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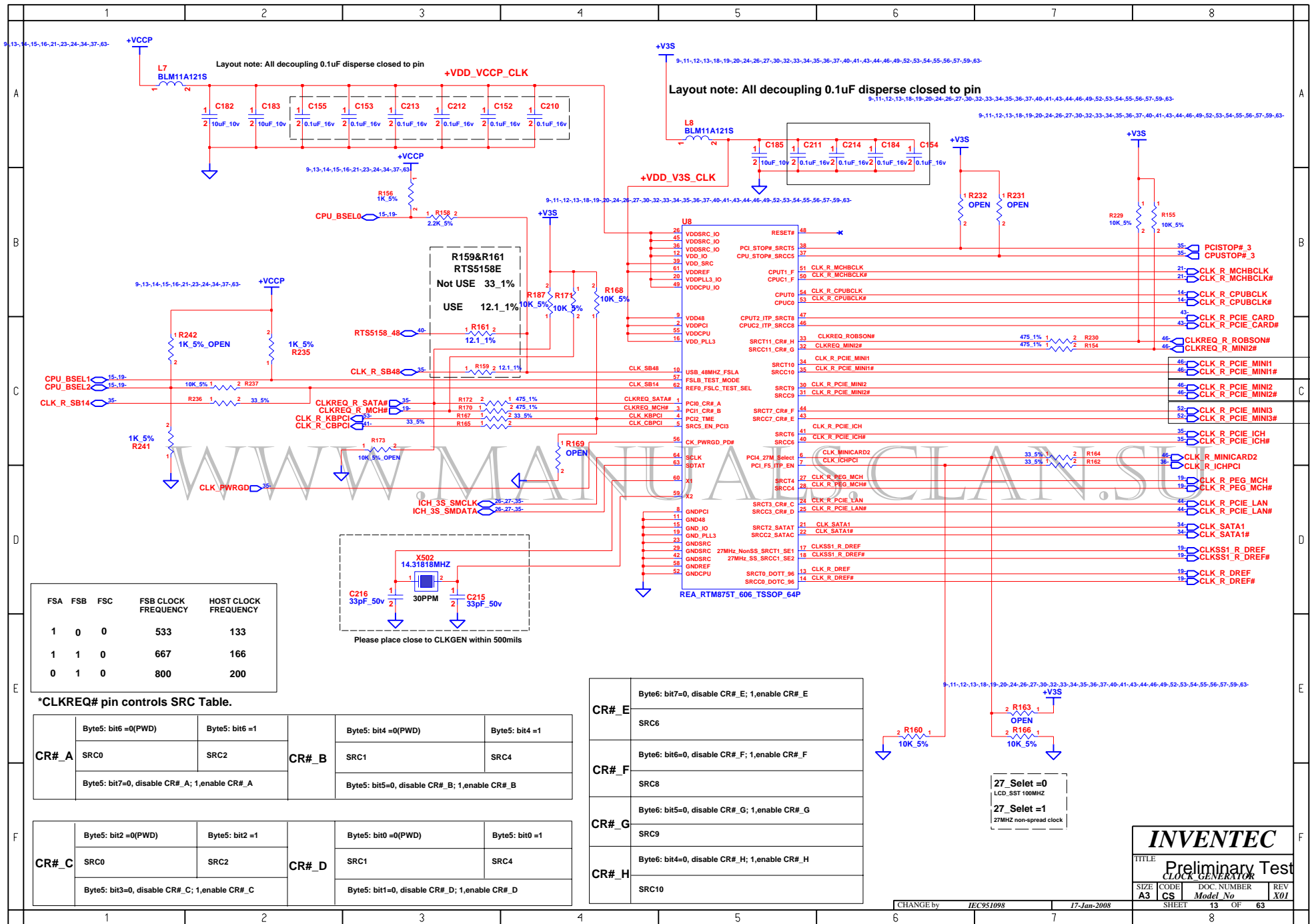


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Layout note: All decoupling 0.1uF disperse closed to pin

Layout note: All decoupling 0.1uF disperse closed to pin

| FSA | FSB | FSC | FSB CLOCK FREQUENCY | HOST CLOCK FREQUENCY |
|-----|-----|-----|---------------------|----------------------|
| 1 | 0 | 0 | 533 | 133 |
| 1 | 1 | 0 | 667 | 166 |
| 0 | 1 | 0 | 800 | 200 |

*CLKREQ# pin controls SRC Table.

| CR#_A | Byte5: bit6 =0(PWD) | | Byte5: bit6 =1 | |
|--|---------------------|--|----------------|------|
| | SRC0 | SRC2 | SRC1 | SRC4 |
| Byte5: bit7=0, disable CR#_A; 1,enable CR#_A | | Byte5: bit5=0, disable CR#_B; 1,enable CR#_B | | |

| CR#_C | Byte5: bit2 =0(PWD) | | Byte5: bit2 =1 | |
|--|---------------------|--|----------------|------|
| | SRC0 | SRC2 | SRC1 | SRC4 |
| Byte5: bit3=0, disable CR#_C; 1,enable CR#_C | | Byte5: bit1=0, disable CR#_D; 1,enable CR#_D | | |

| | |
|-------|--|
| CR#_E | Byte6: bit7=0, disable CR#_E; 1,enable CR#_E |
| SRC6 | |
| CR#_F | Byte6: bit6=0, disable CR#_F; 1,enable CR#_F |
| SRC8 | |
| CR#_G | Byte6: bit5=0, disable CR#_G; 1,enable CR#_G |
| SRC9 | |
| CR#_H | Byte6: bit4=0, disable CR#_H; 1,enable CR#_H |
| SRC10 | |

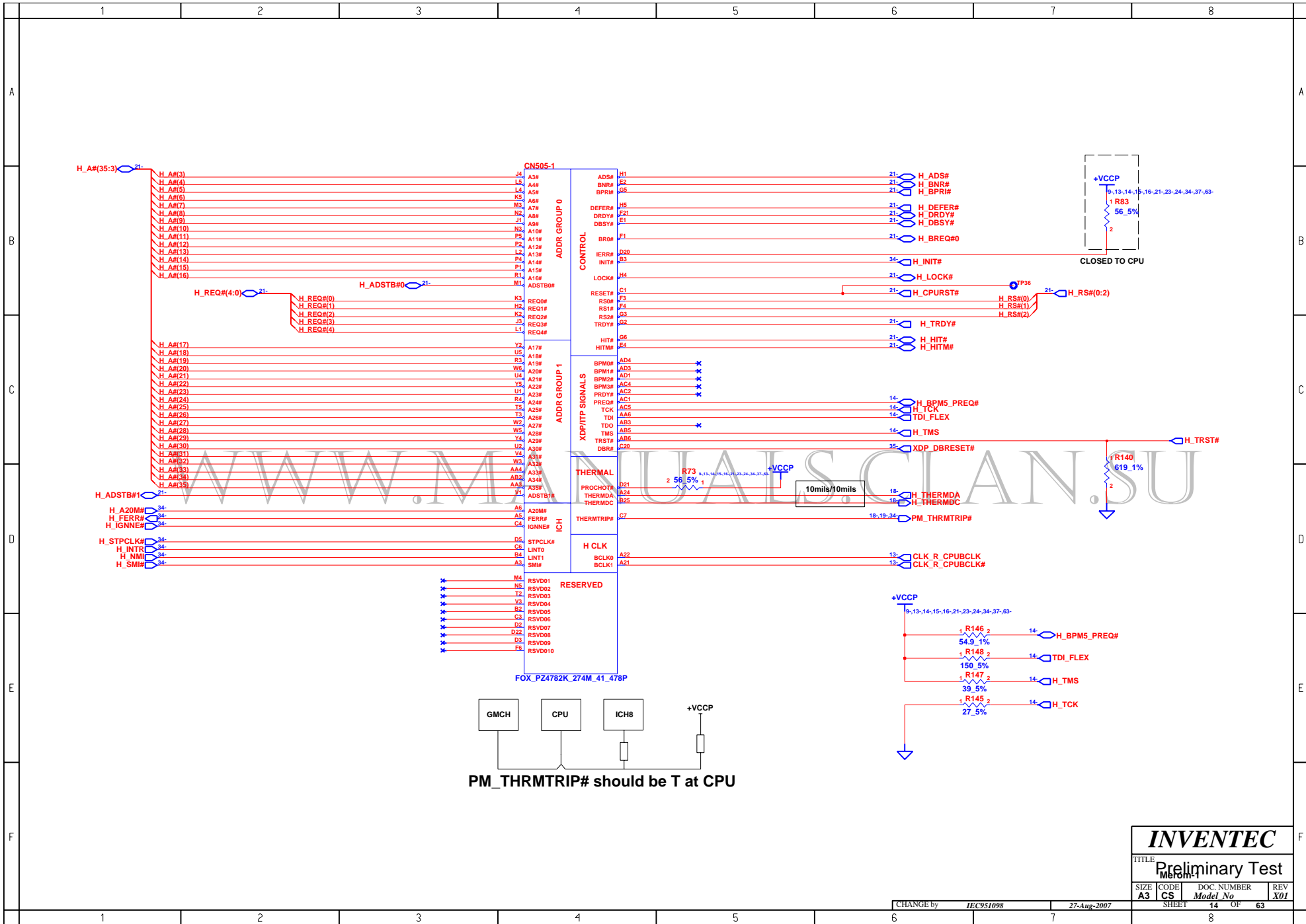
27 Selet =0
LCD_SST 100MHZ

27 Selet =1
27MHZ non-spread clock

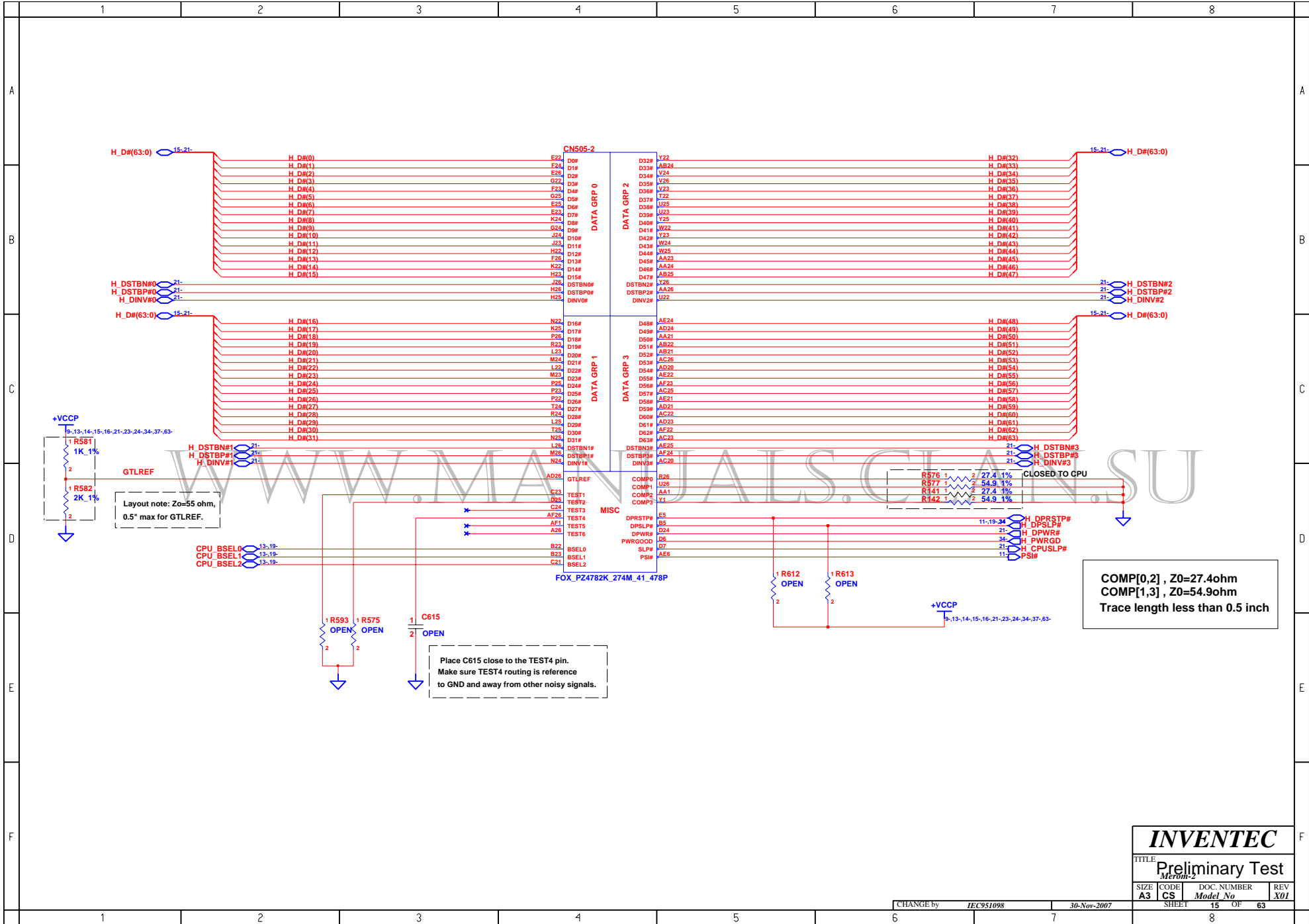
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TITLE Preliminary Test
CLOCK GENERATOR

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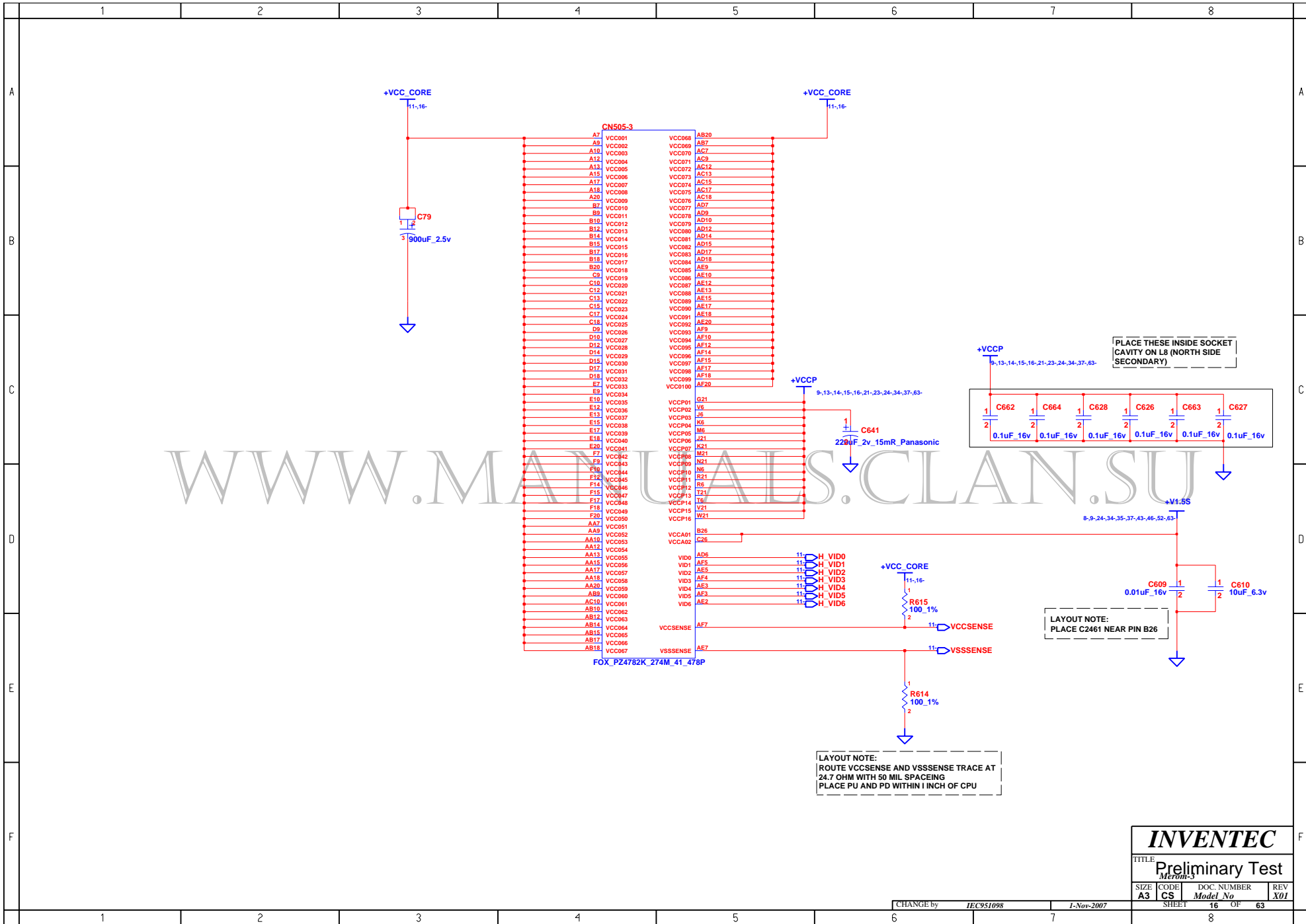
COMP[0,2], Z0=27.4ohm
 COMP[1,3], Z0=54.9ohm
 Trace length less than 0.5 inch

Place C615 close to the TEST4 pin.
 Make sure TEST4 routing is reference
 to GND and away from other noisy signals.

Layout note: Zo=55 ohm,
 0.5" max for GTLREF.

CLOSED TO CPU
 R576 1 2 27.4 1%
 R577 1 2 54.9 1%
 R141 1 2 27.4 1%
 R142 1 2 54.9 1%

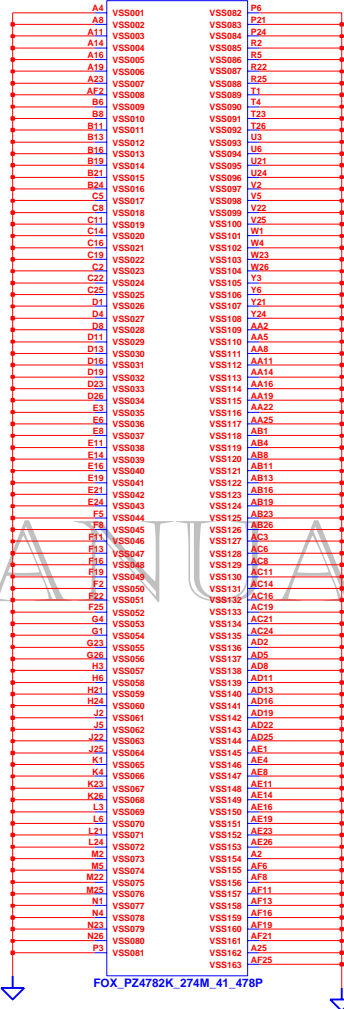
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| Micro-2 | | | |
| SIZE | CODE | DOC. NUMBER | REV |
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| TITLE Preliminary Test | | | |
| Micro-3 | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
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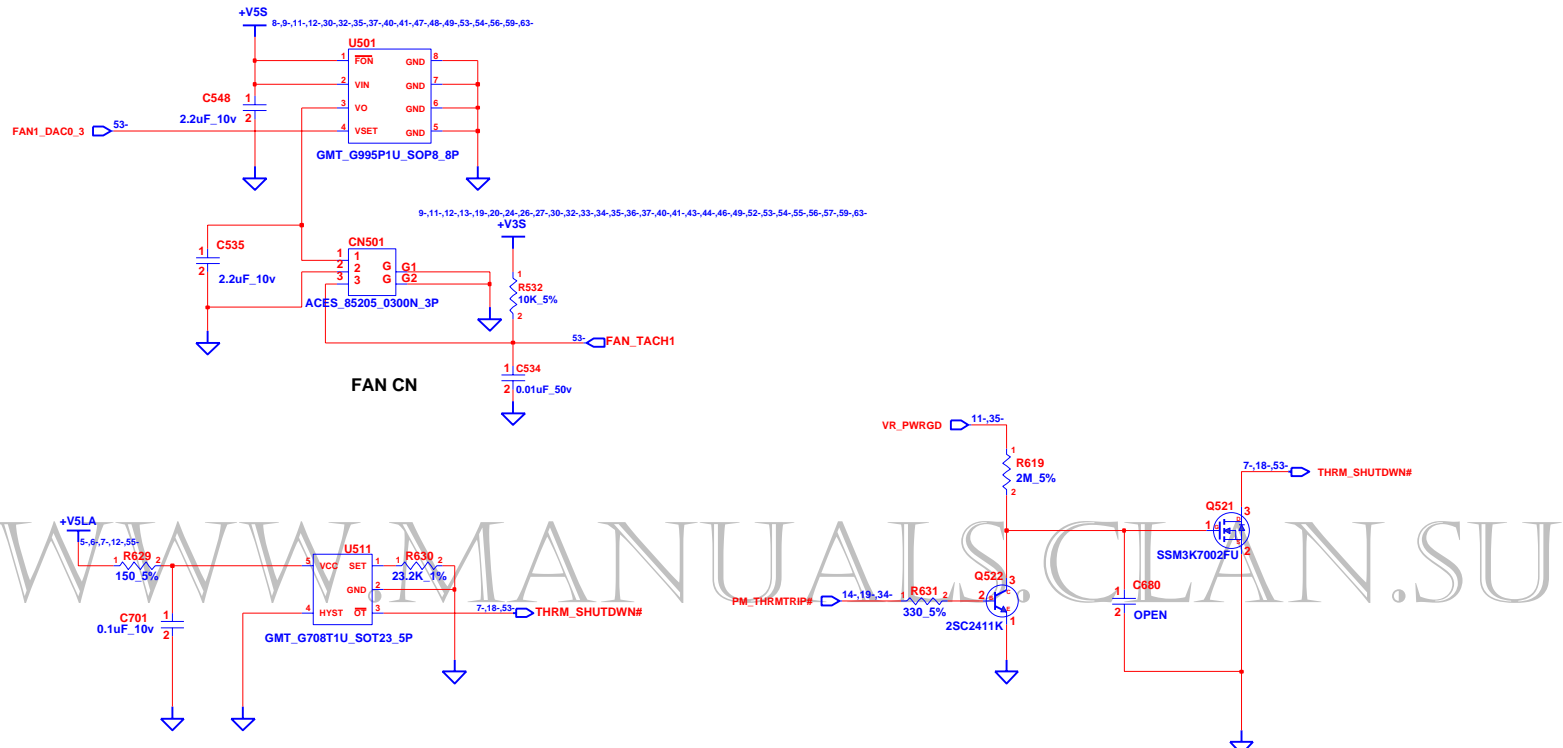
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|---|---|---|---|---|---|---|---|---|---|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | |
| A | | | | | | | | | A |
| B | | | | | | | | | B |
| C | | | | | | | | | C |
| D | | | | | | | | | D |
| E | | | | | | | | | E |
| F | | | | | | | | | F |

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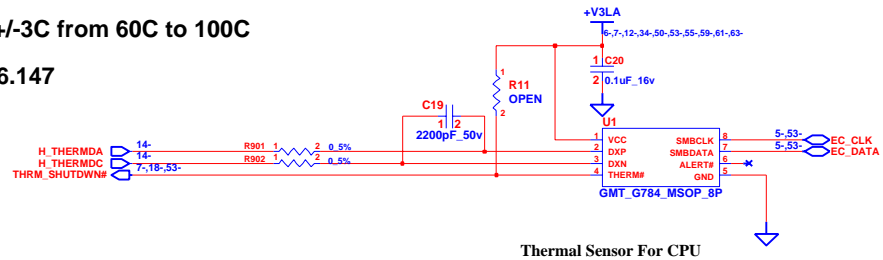
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| TITLE Preliminary Test | | | |
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Thermal shoutdown at 89C +/-3C from 60C to 100C

$$RSET = 0.0012 * T^2 - 0.9308 * T + 96.147$$

Hysteresis is 30C



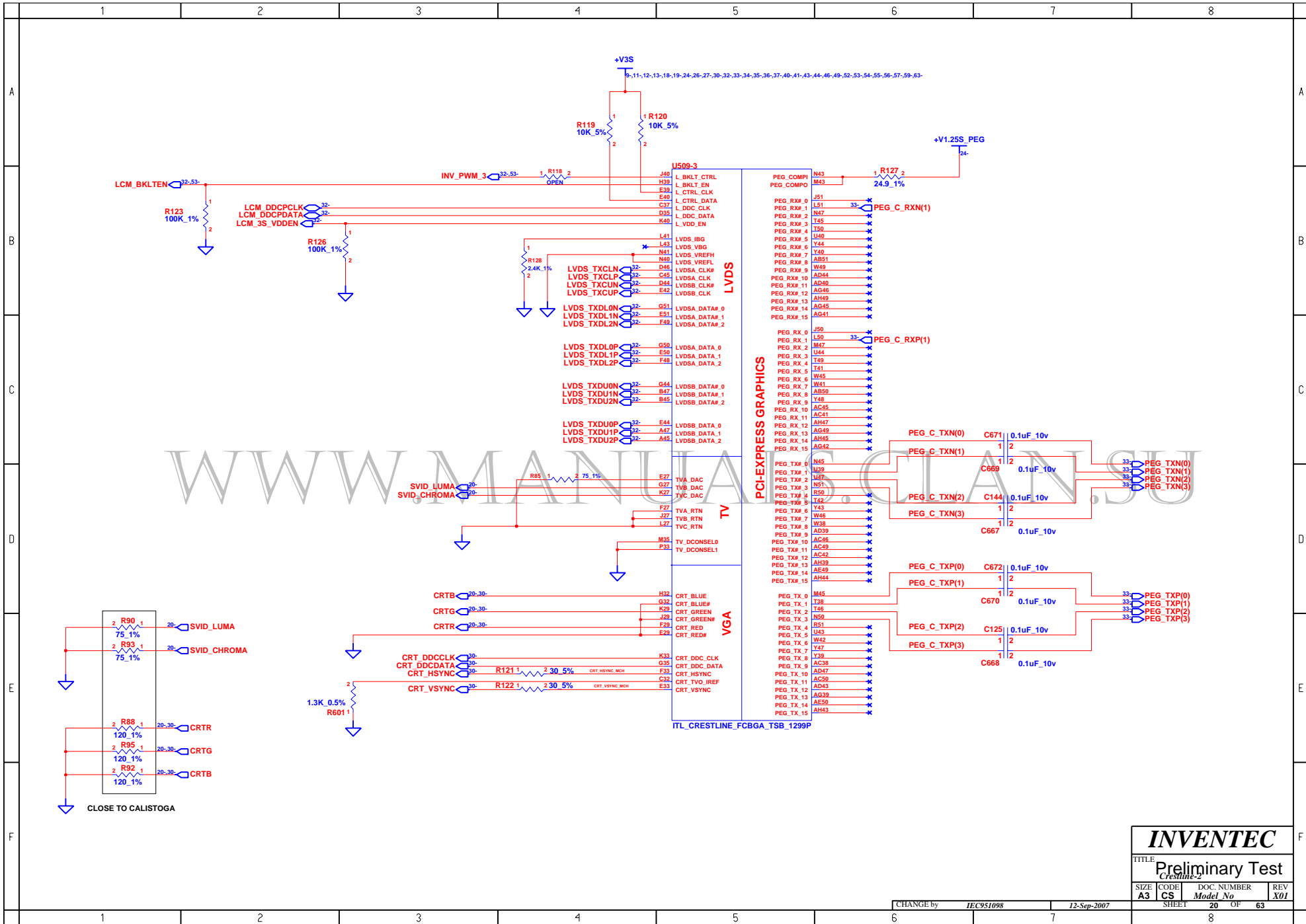
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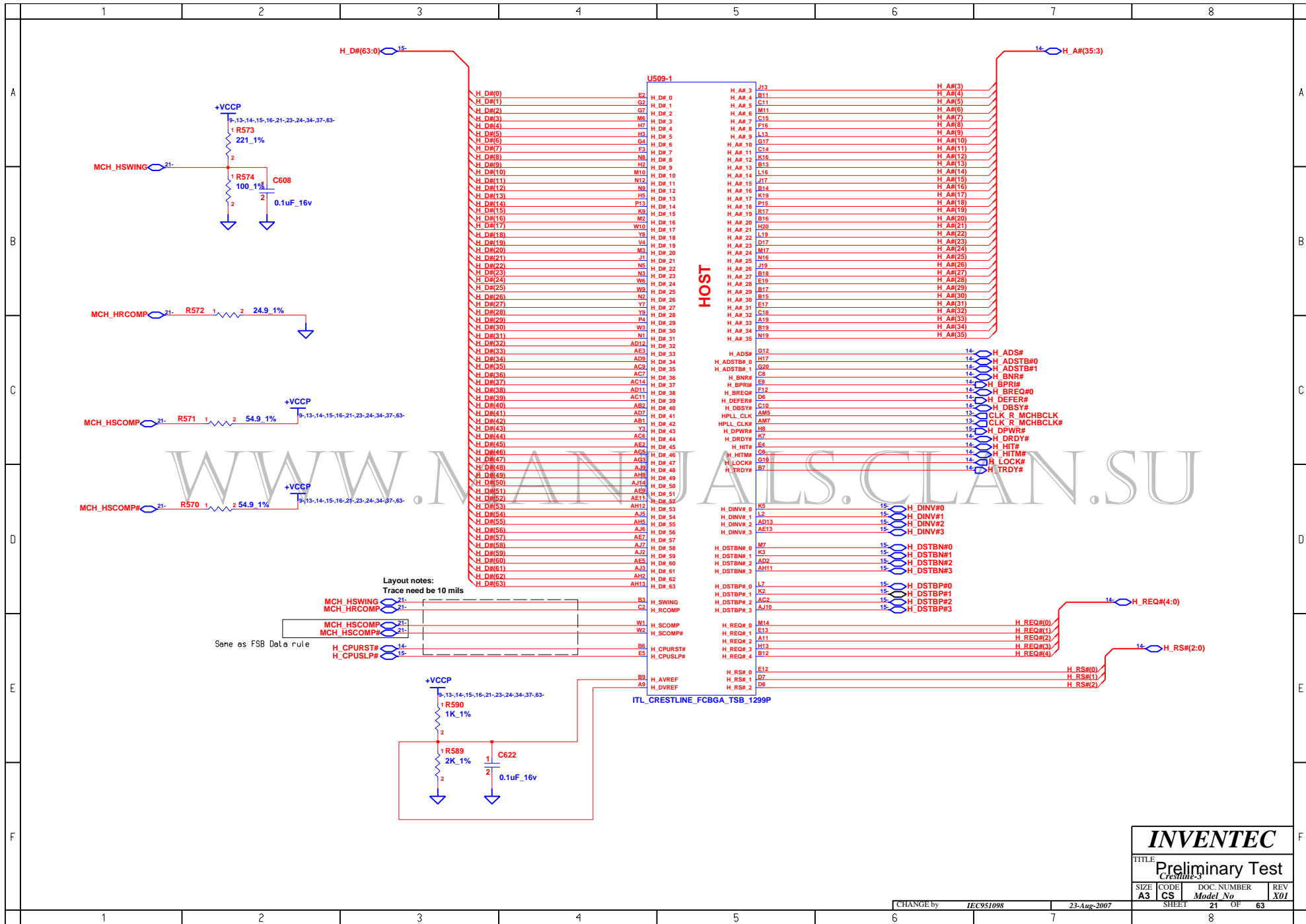
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| TITLE | | | |
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| THERMAL & FAN CONTROLLER | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |

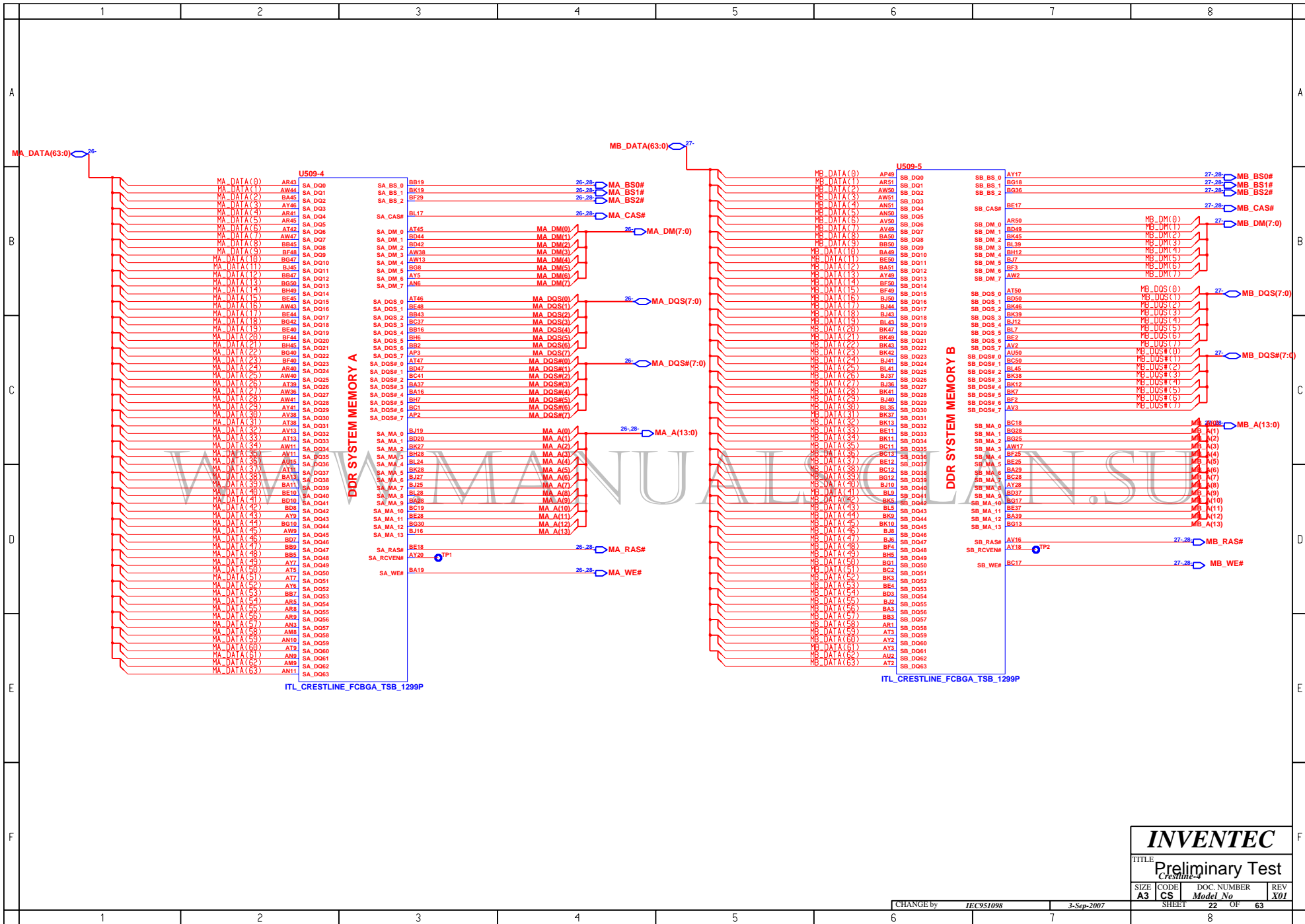
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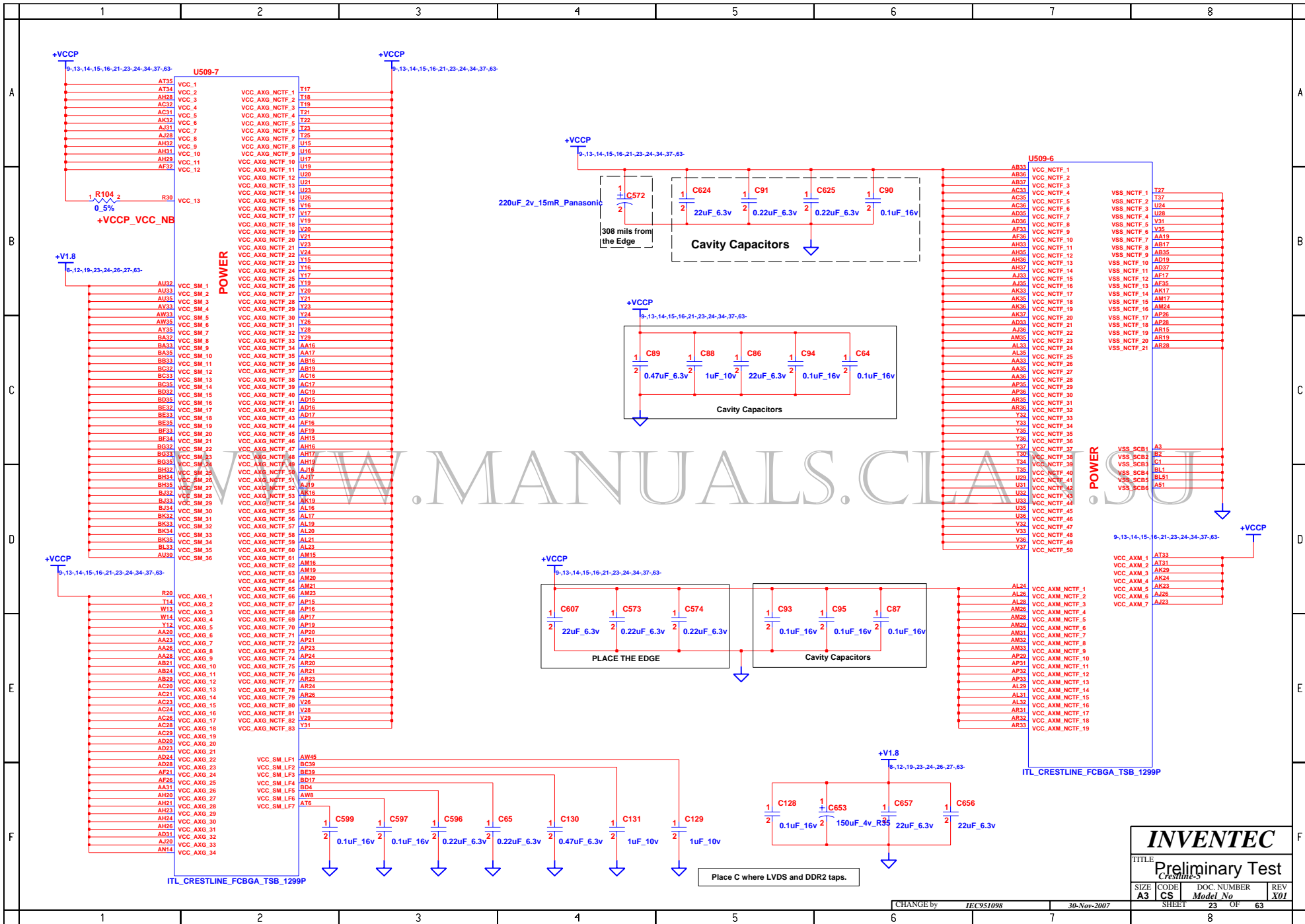
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| SIZE A3 | CODE CS | DOC. NUMBER <i>Model No</i> | REV X01 |
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INVENTEC
 TITLE Preliminary Test
 Crestline-4
 SIZE CODE DOC. NUMBER REV
 A3 CS Model No X01
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CHANGE by IEC951098 3-Sep-2007

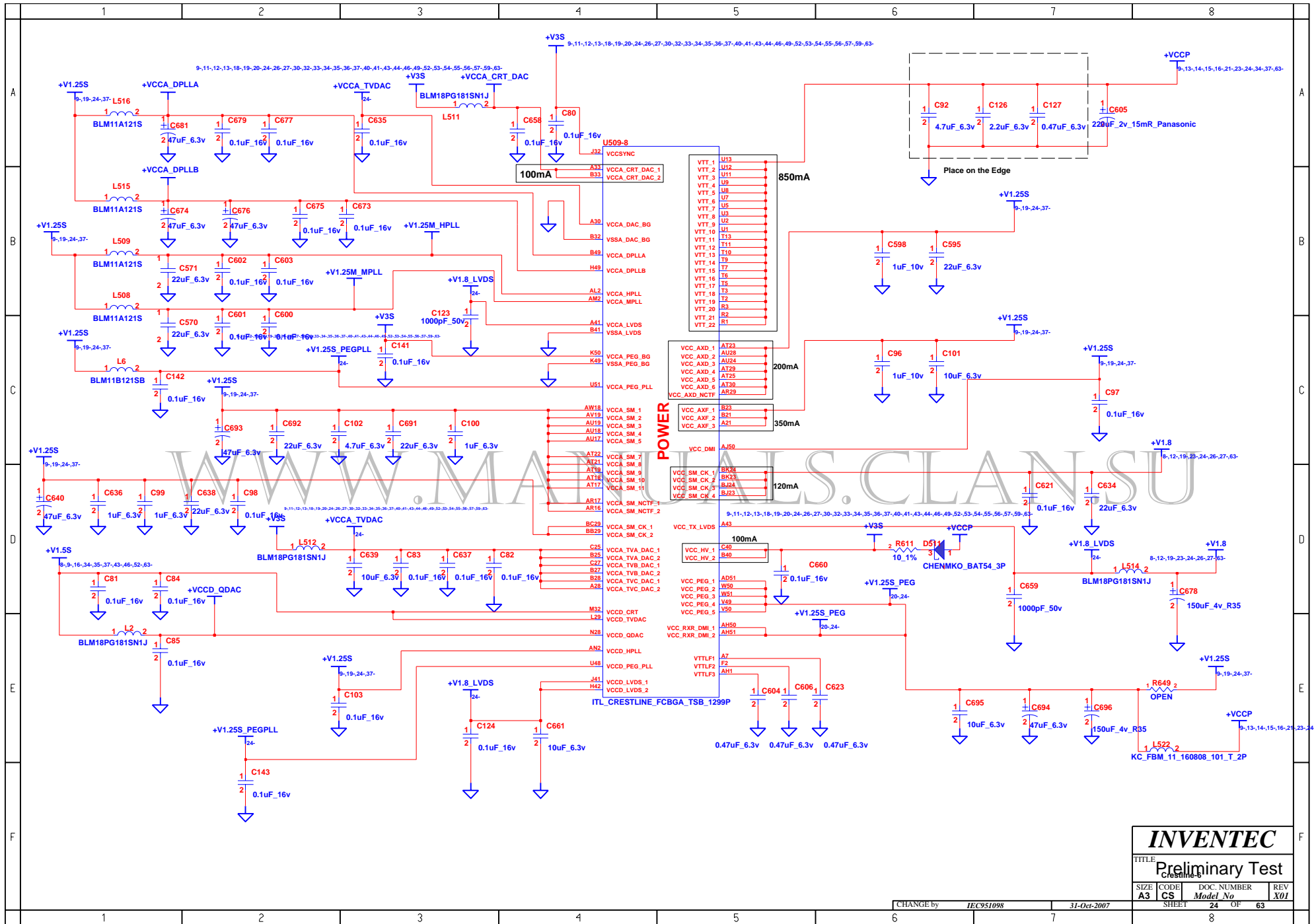


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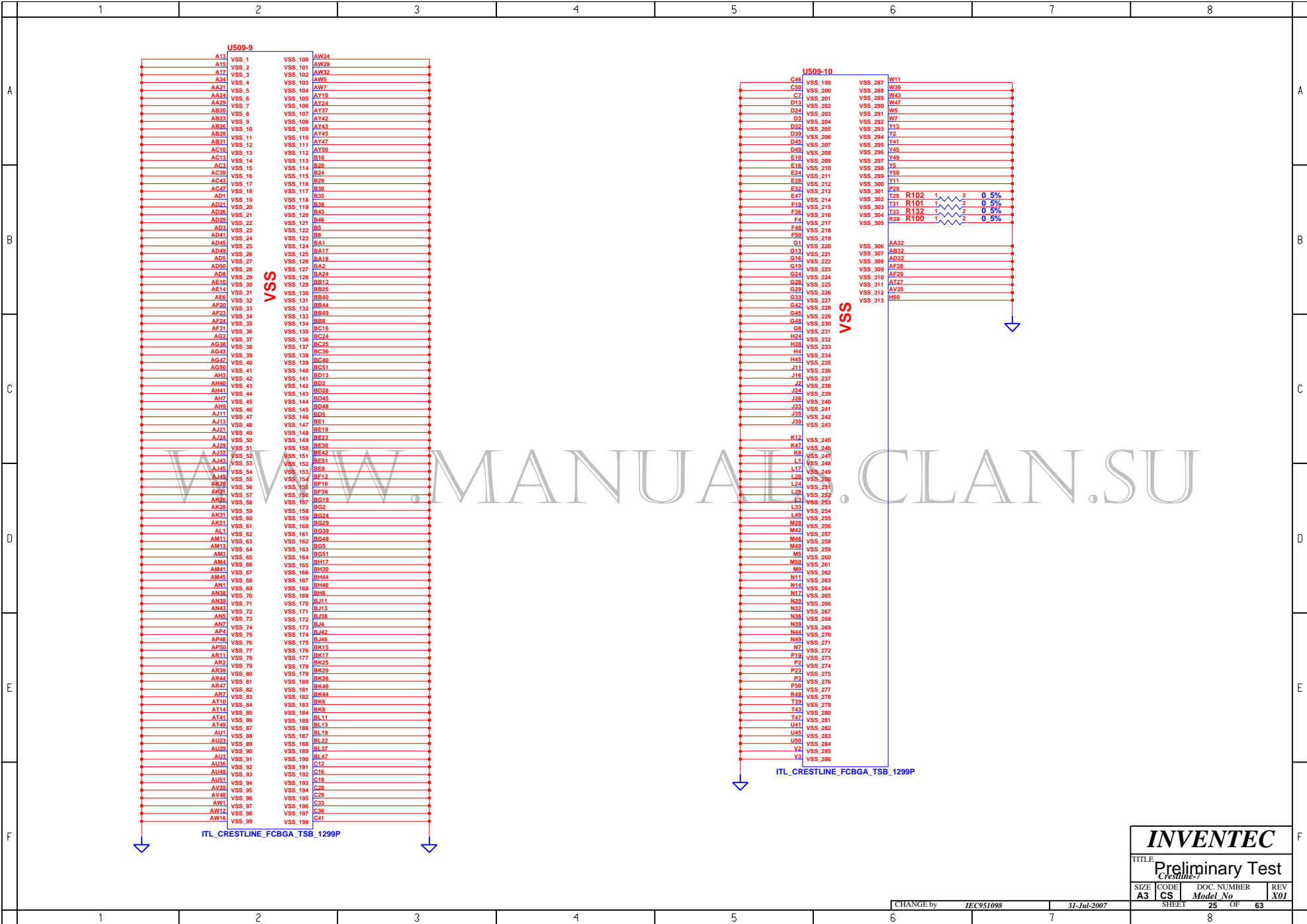
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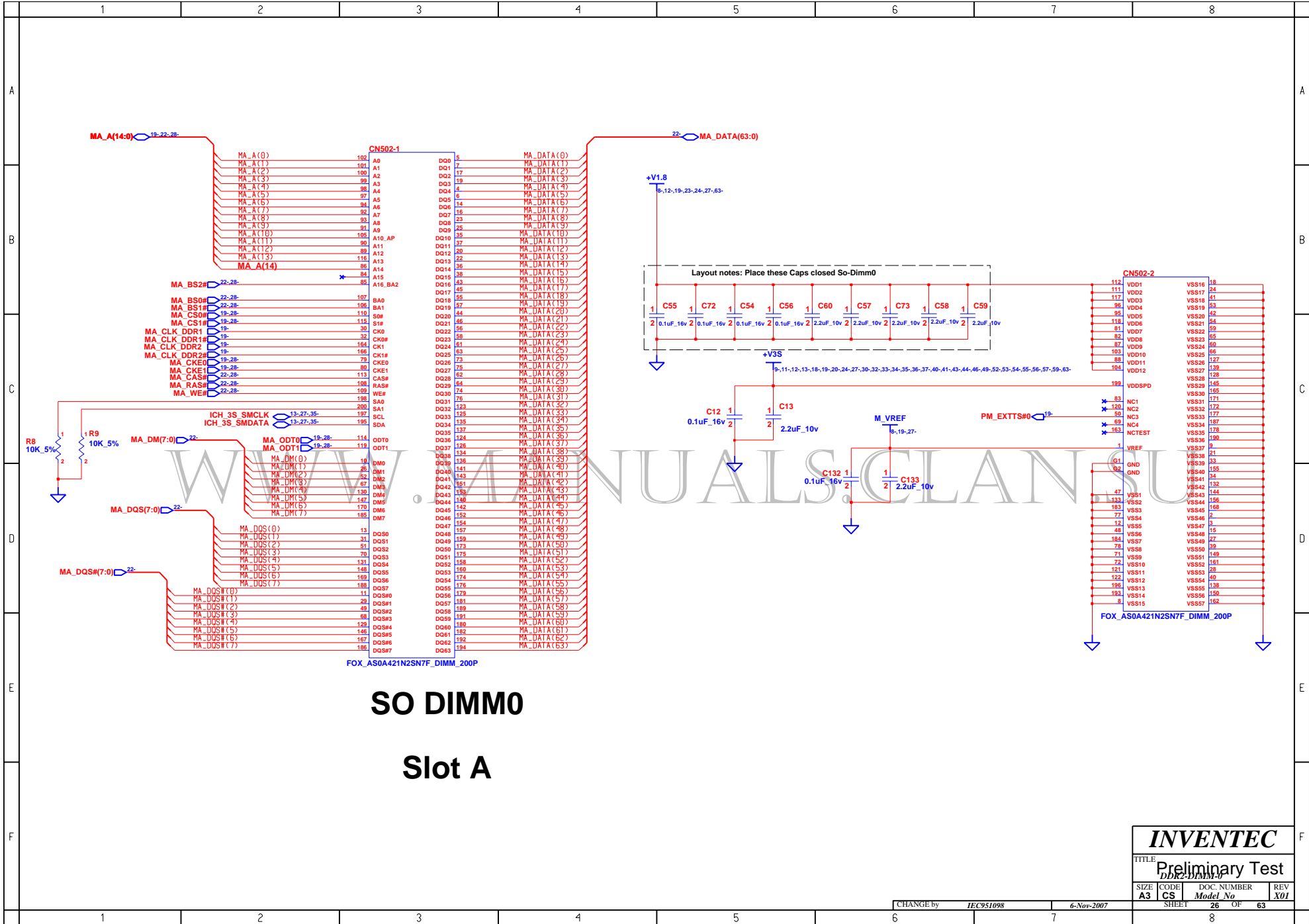
Place C where LVDS and DDR2 taps.



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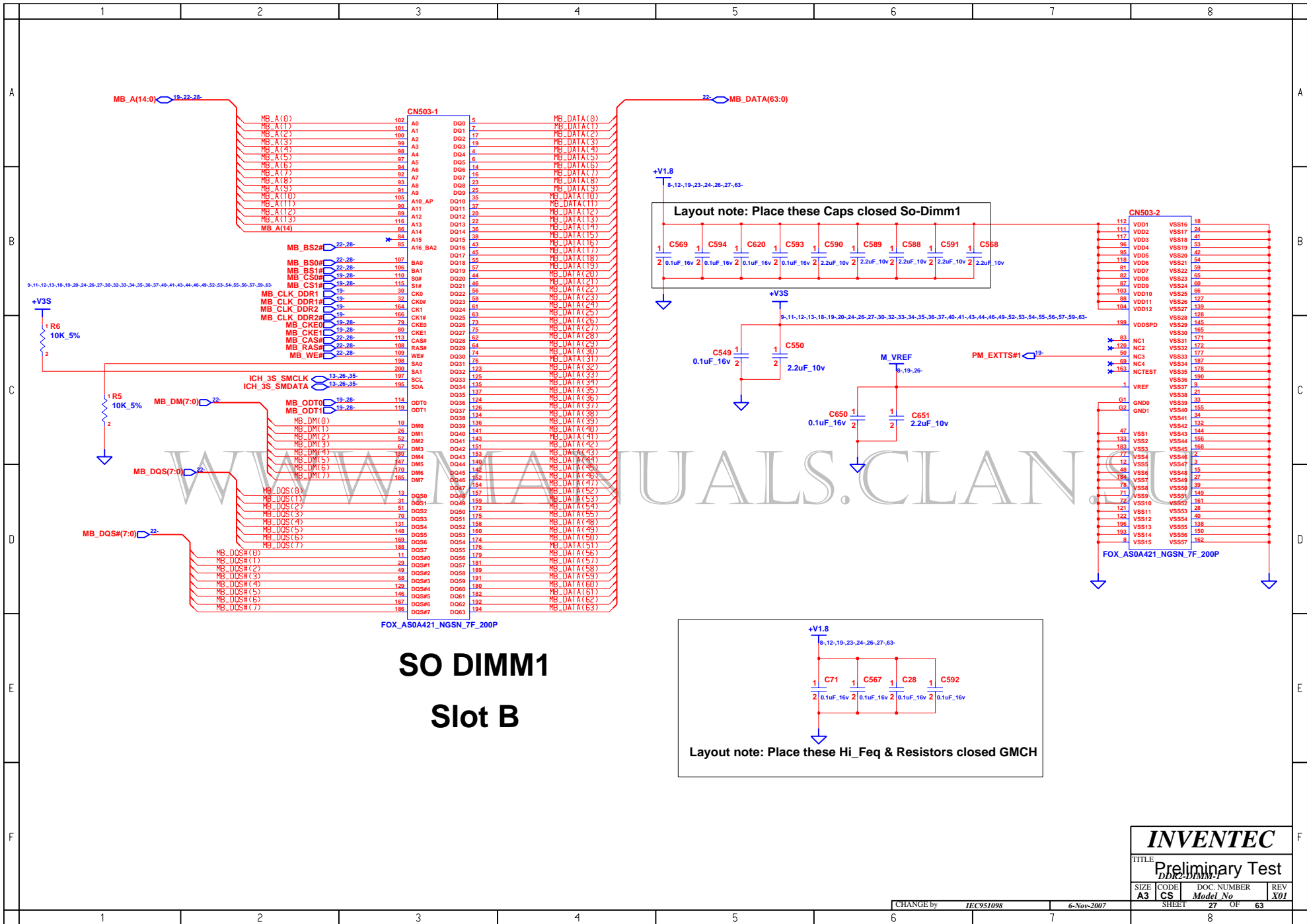


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| SIZE A3 | CODE CS | DOC. NUMBER <i>Crestrline-7</i> Model No | REV X01 |
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SO DIMMO Slot A

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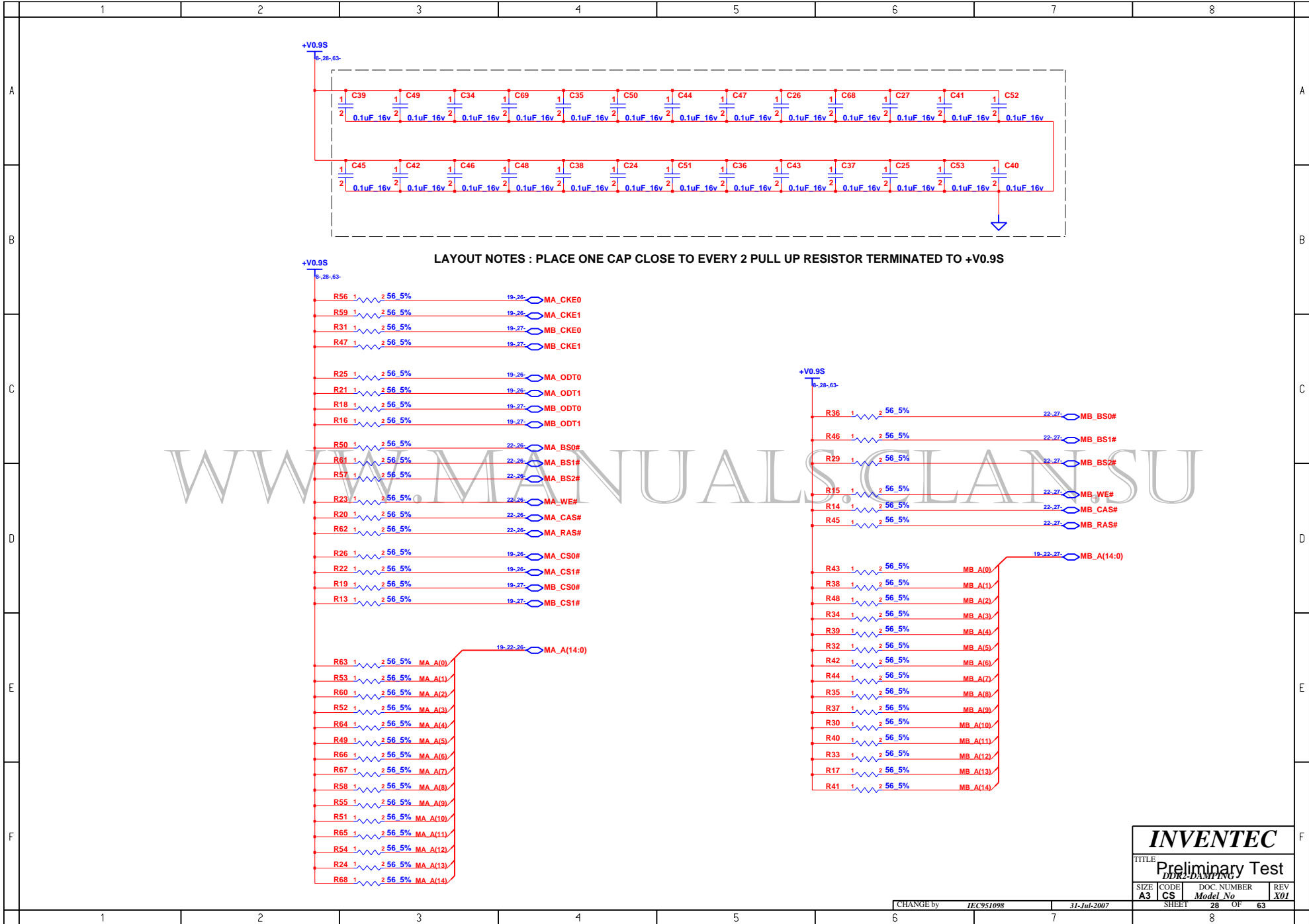
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DBK2-DIMM1

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TITLE
Preliminary Test
DDR2-DAMPING

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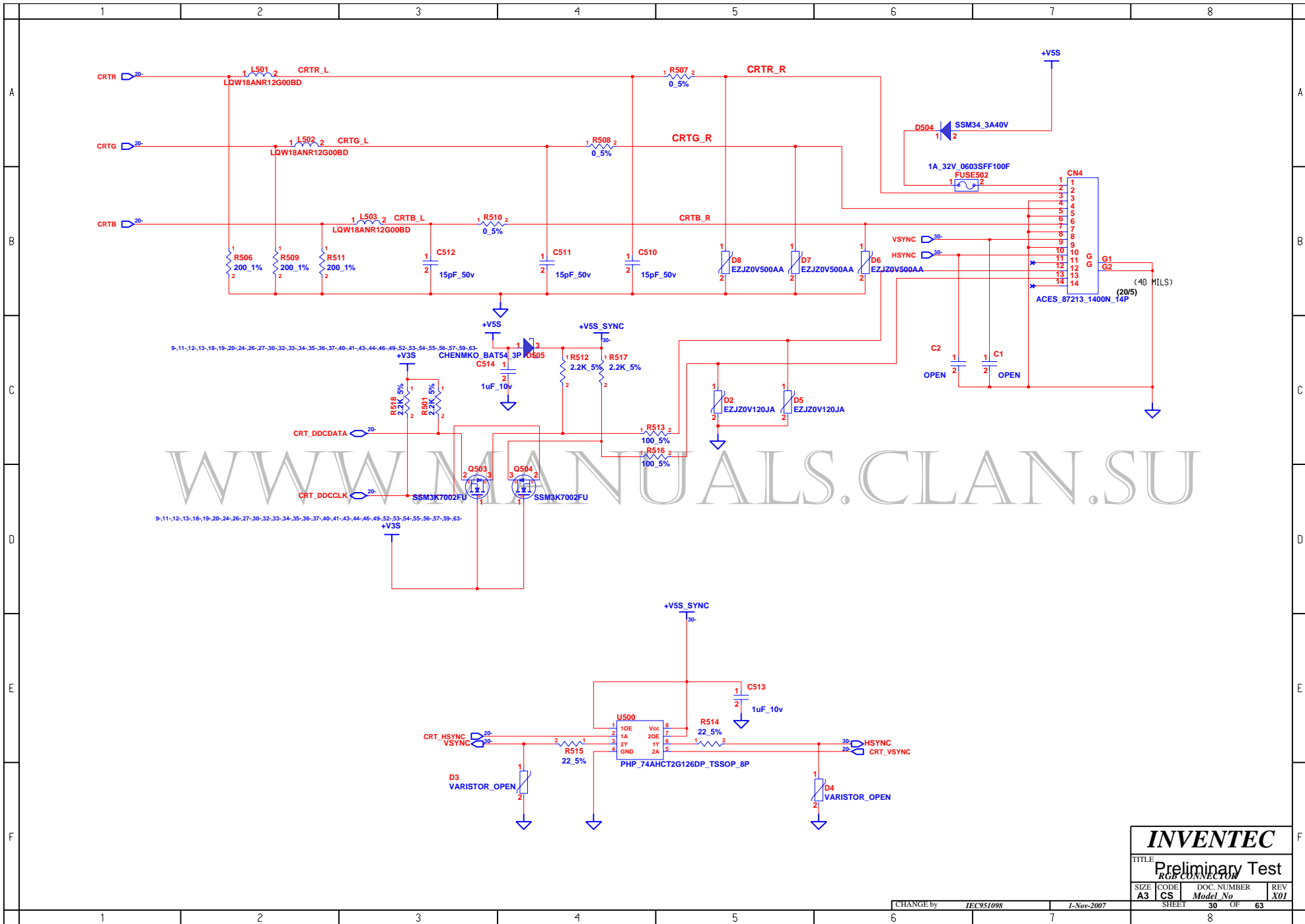
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| TITLE RGB Connector | | | |
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1 2 3 4 5 6 7 8

A

A

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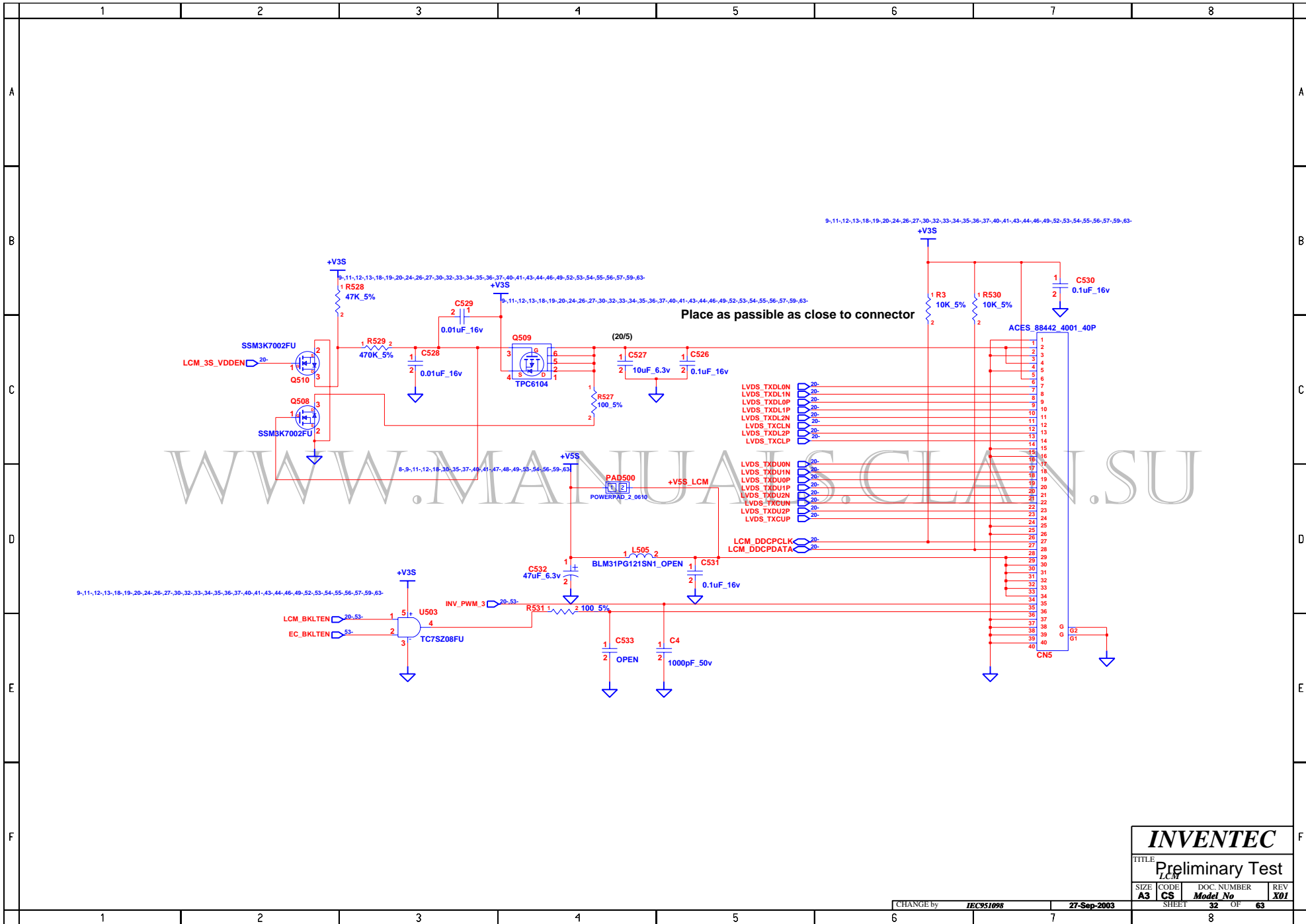
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Preliminary Test

SIZE CODE DOC. NUMBER REV
A3 CS Model No. X01

CHANGE by **IEC951098** **30-Nov-2007**

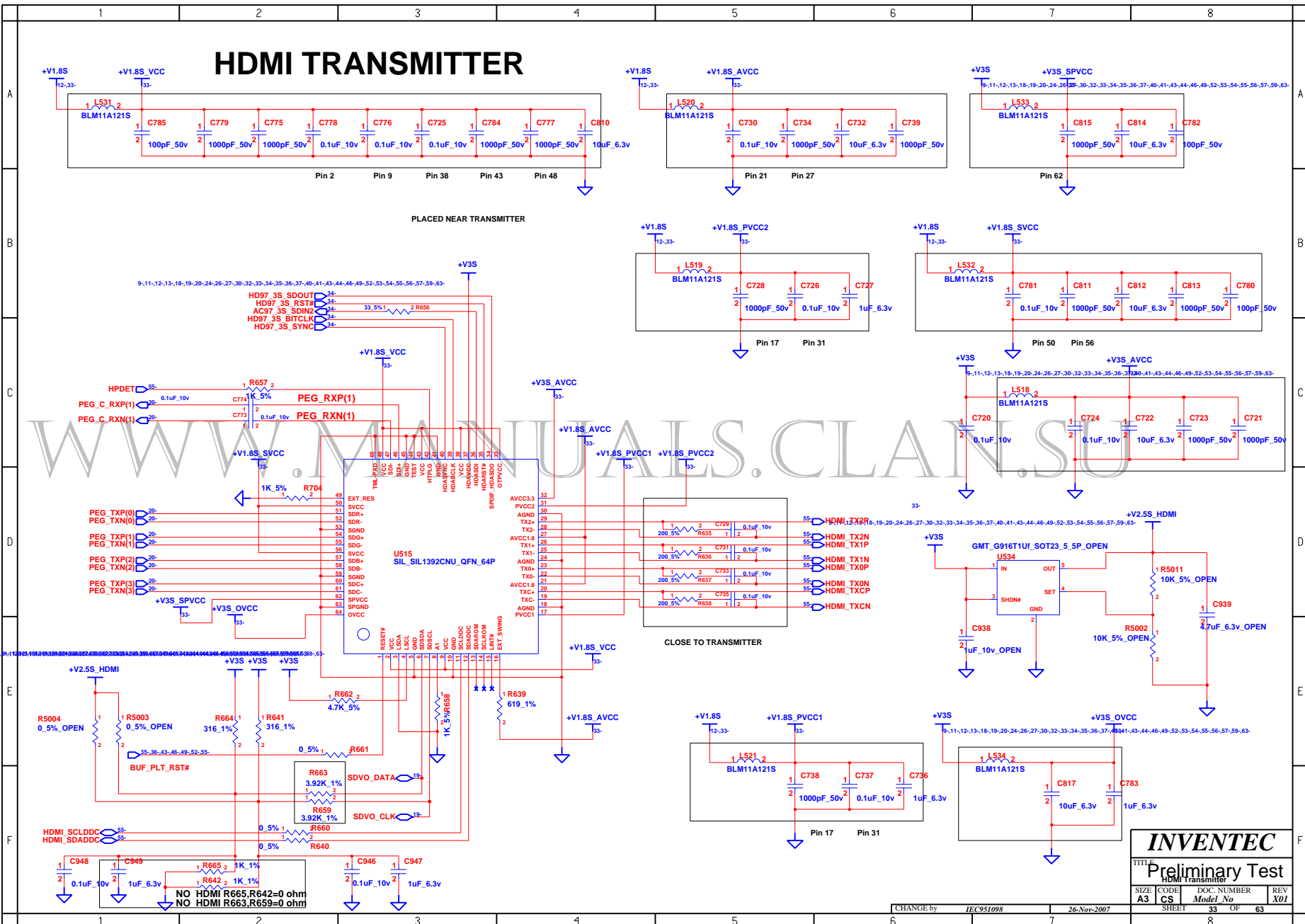
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1 2 3 4 5 6 7 8



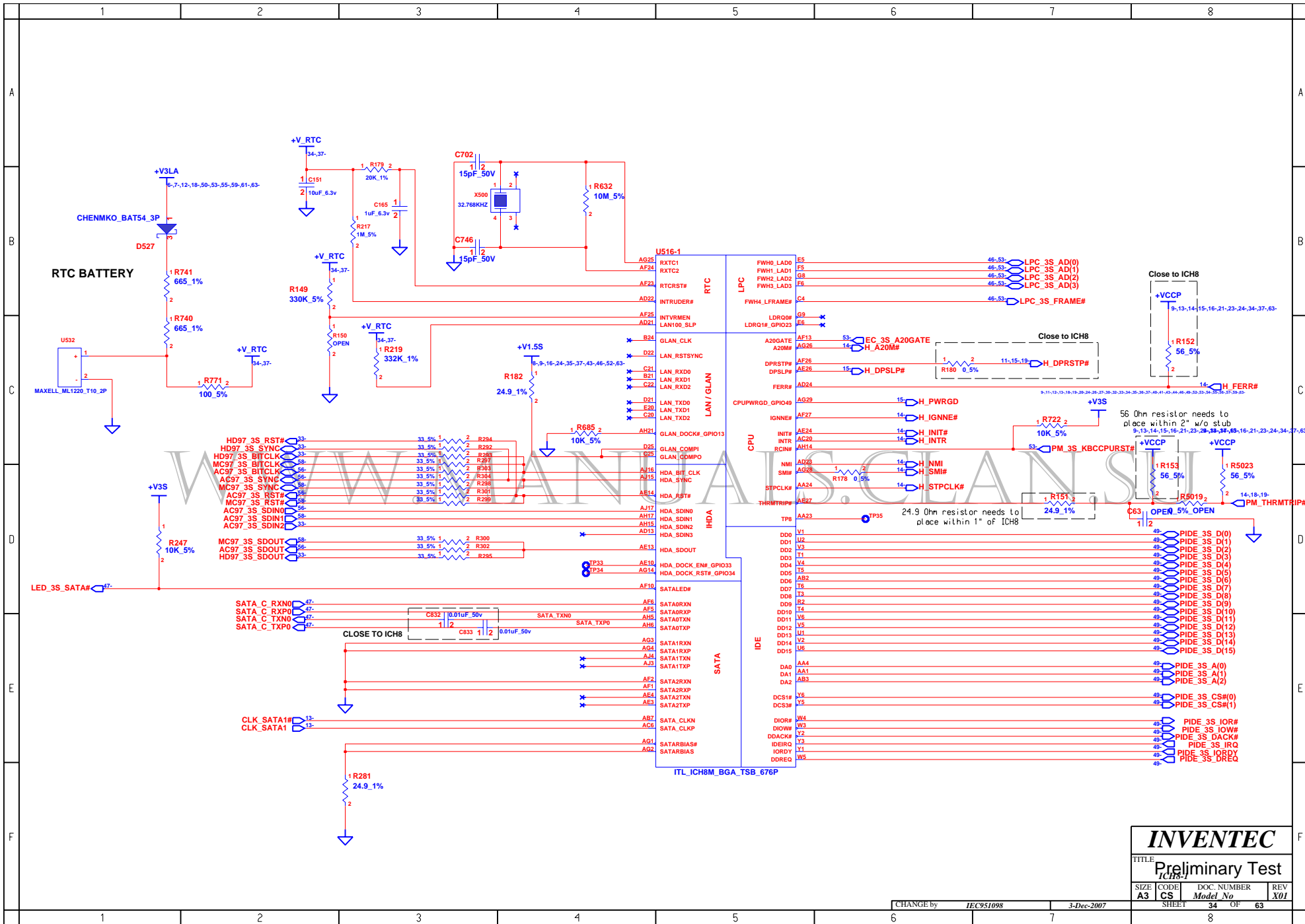
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HDMI TRANSMITTER



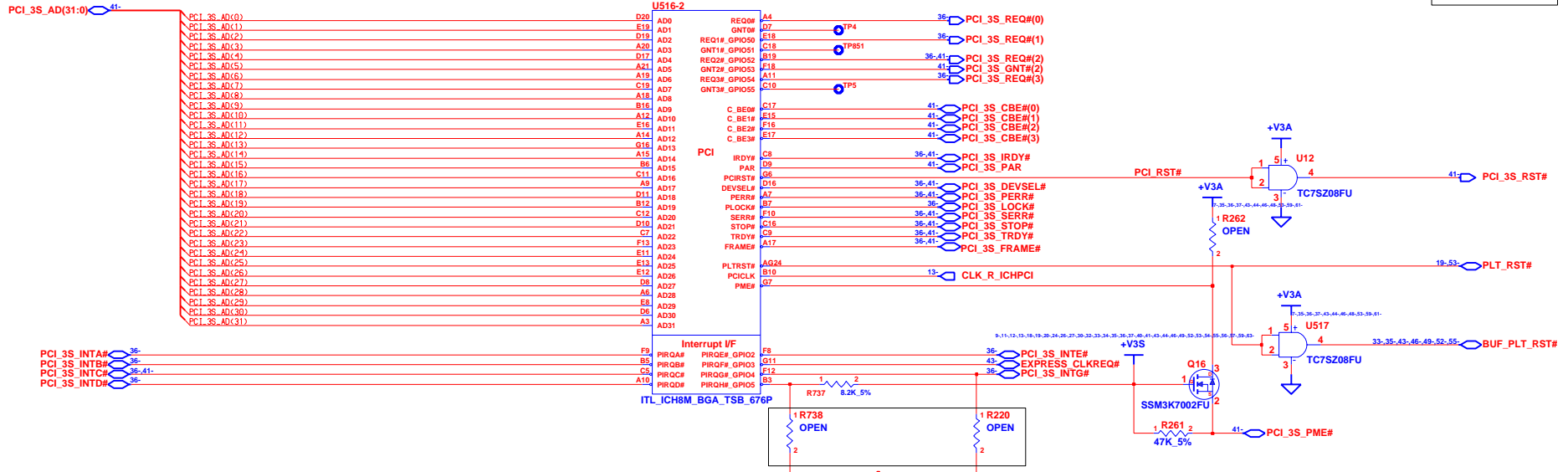
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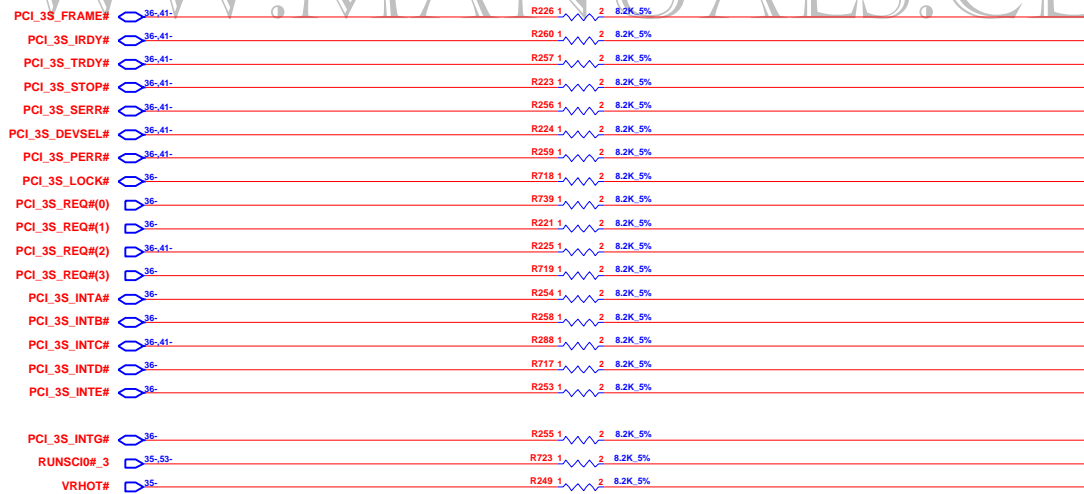


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Boot BIOS from SPI
 GNT0# = 0
 SPI_CS1# = 1



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INTG --- MACHINE_ID7
 INTH --- MACHINE_ID8

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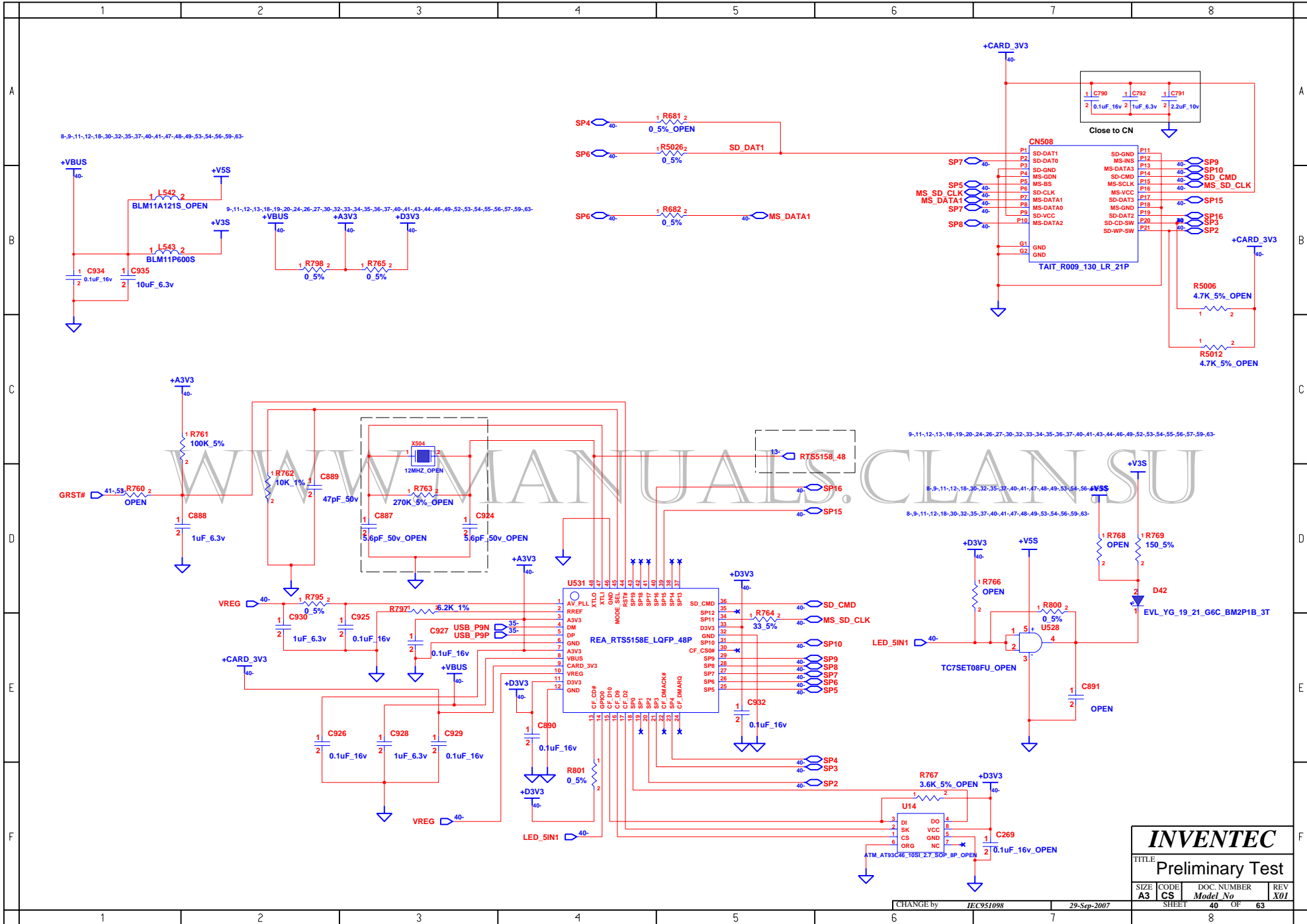
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| A3 | CS | Model No | X01 |
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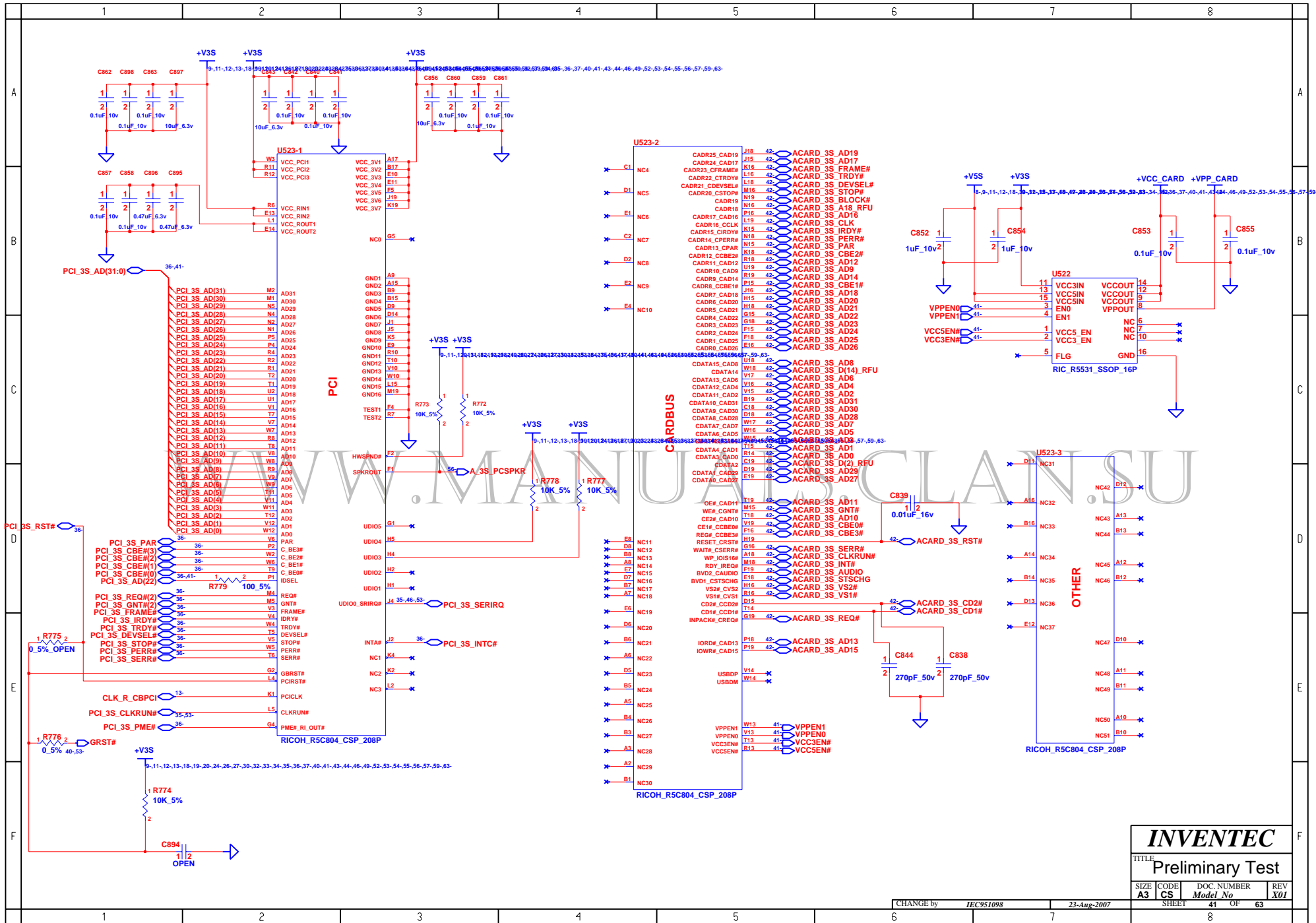
CHANGE by IEC951098 30-Nov-2007

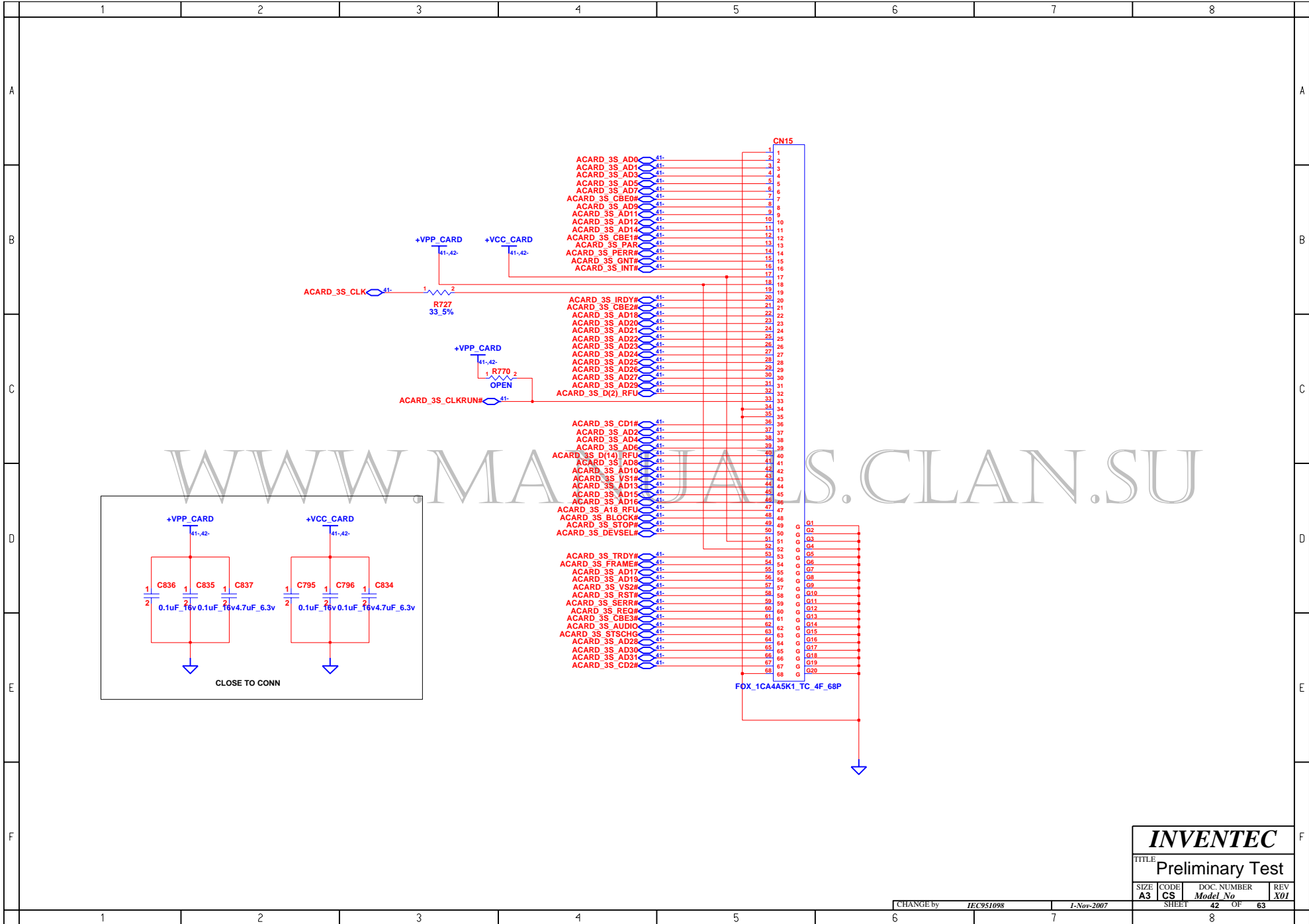
SHEET 39 OF 63



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|---------------------------|------------|------------------------|------------|
| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC NUMBER Model No | REV X01 |
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CHANGE by IEC951098 29-Sep-2007





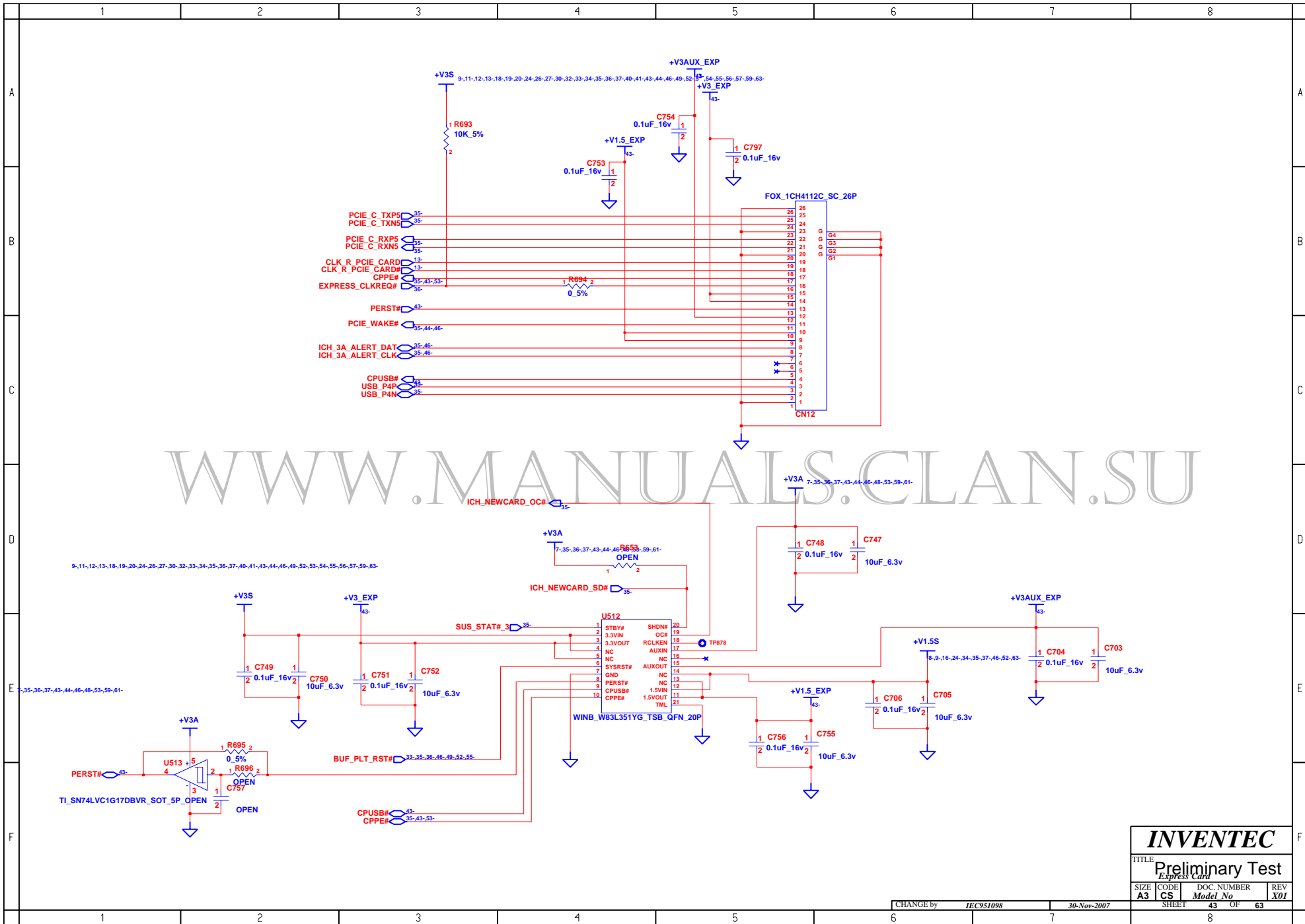
INVENTEC

TITLE
Preliminary Test

| | | | |
|------------|------------|-------------------------|------------|
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
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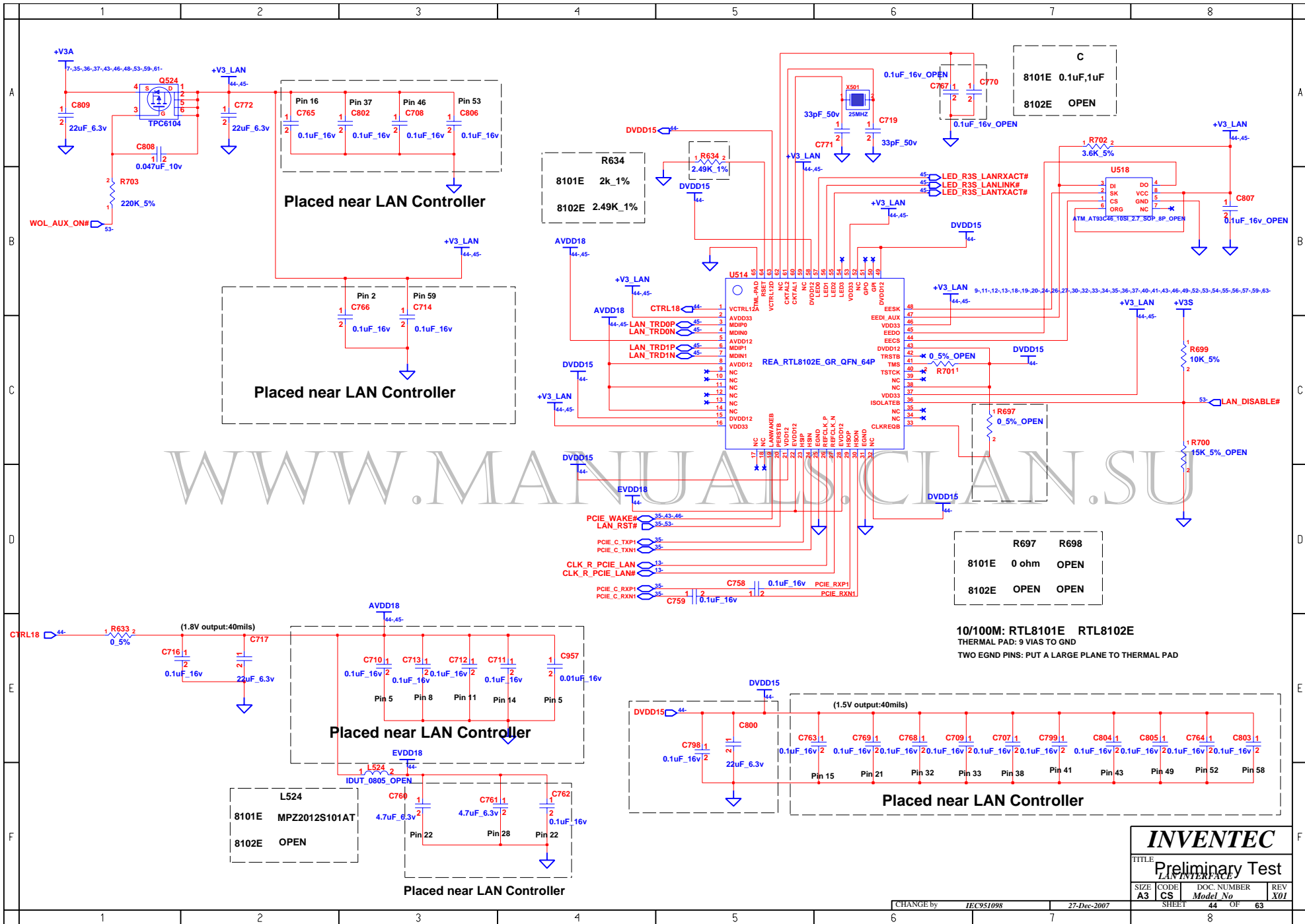
CHANGE by IEC951098 1-Nov-2007

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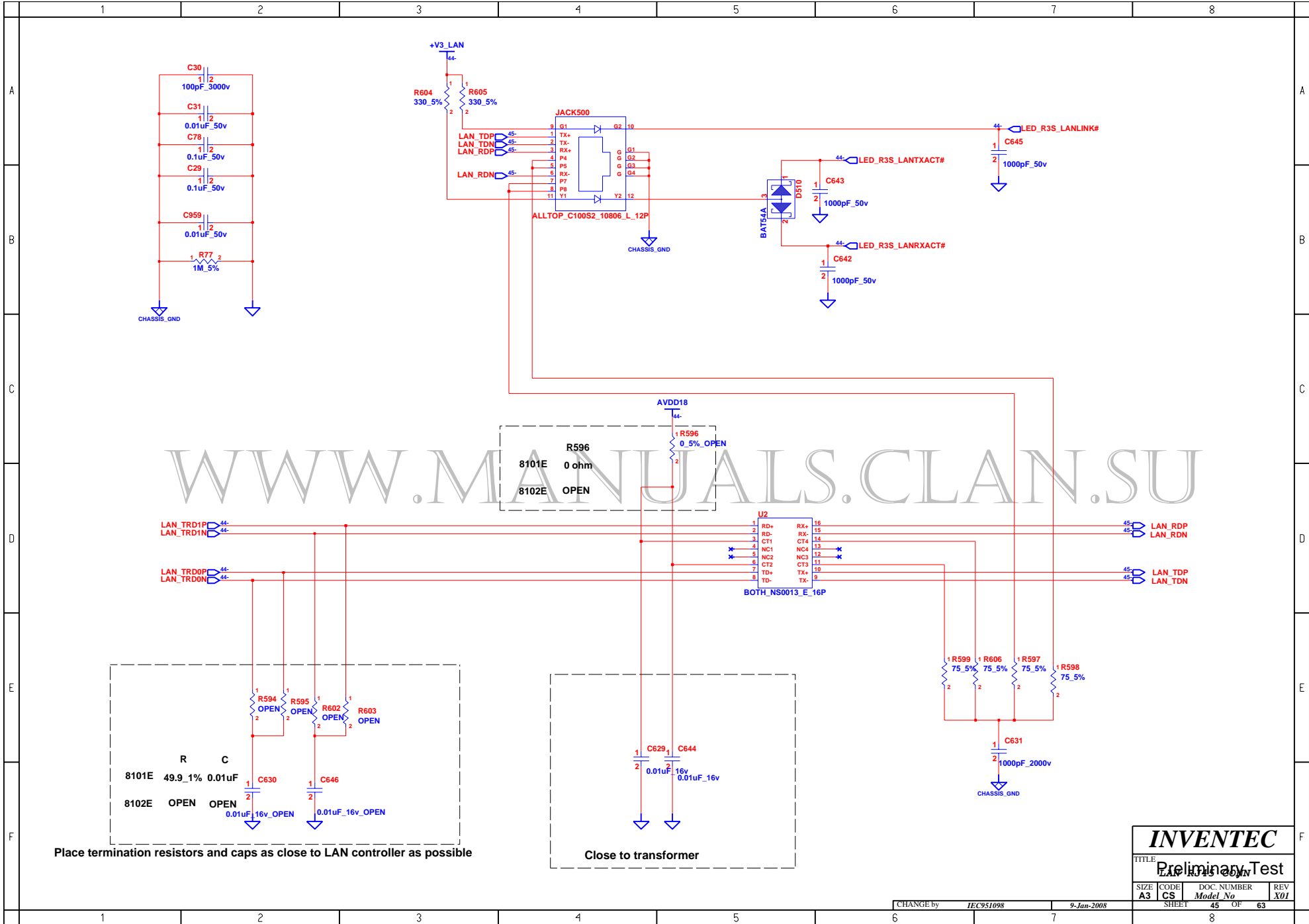
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| INVENTEC | | | |
| TITLE Preliminary Test <i>Express Card</i> | | | |
| SIZE A3 | CODE CS | DOC. NUMBER <i>Model No</i> | REV X01 |
| CHANGE by IEC951098 | | 30-Nov-2007 | |
| SHEET 43 OF 63 | | 8 | |

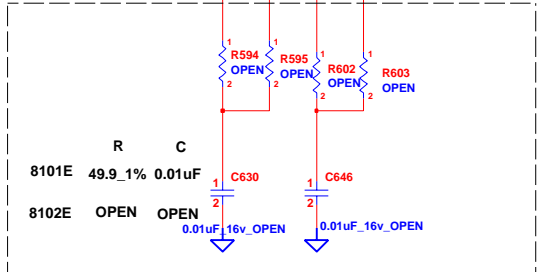


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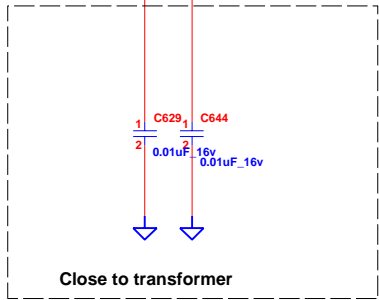
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|------------------------|------------|------------------------|------------|
| INVENTEC | | | |
| TITLE LAN INTERFACE | | | |
| SIZE A3 | CODE CS | DOC NUMBER Model No | REV X01 |
| CHANGE by IEC951098 | | 27-Dec-2007 | |
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Place termination resistors and caps as close to LAN controller as possible



Close to transformer

INVENTEC

TITLE Preliminary Test

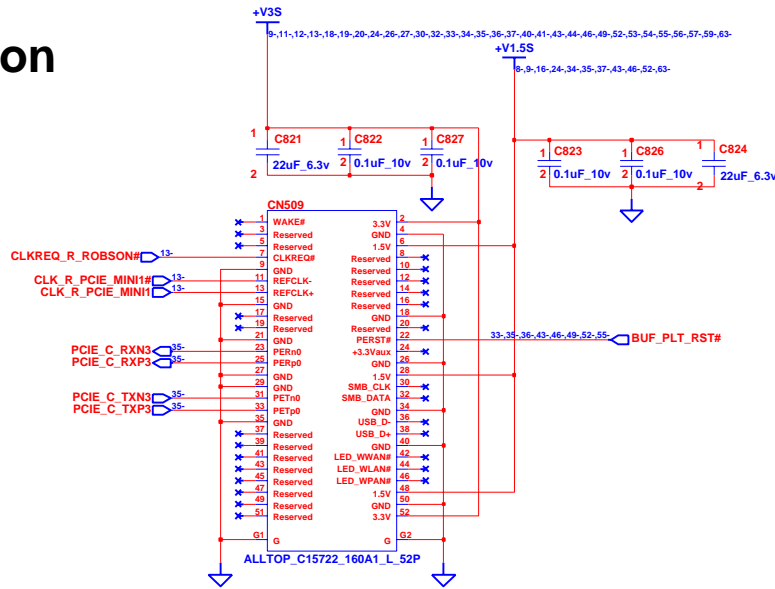
SIZE CODE DOC. NUMBER REV
A3 CS Model No X01

CHANGE by IEC951098

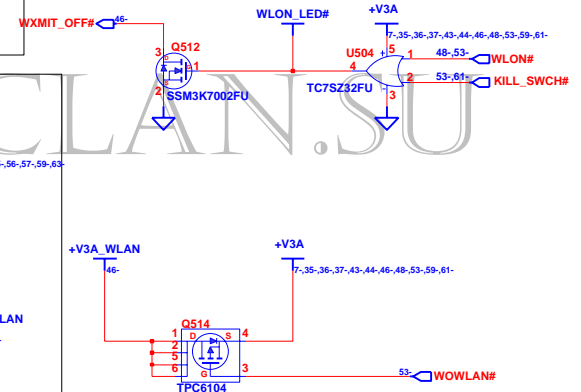
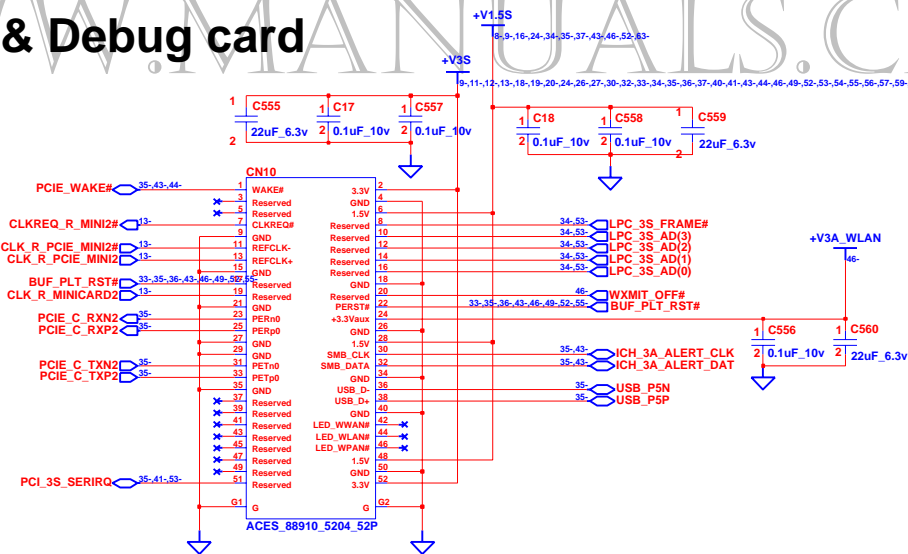
9-Jan-2008

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Robson

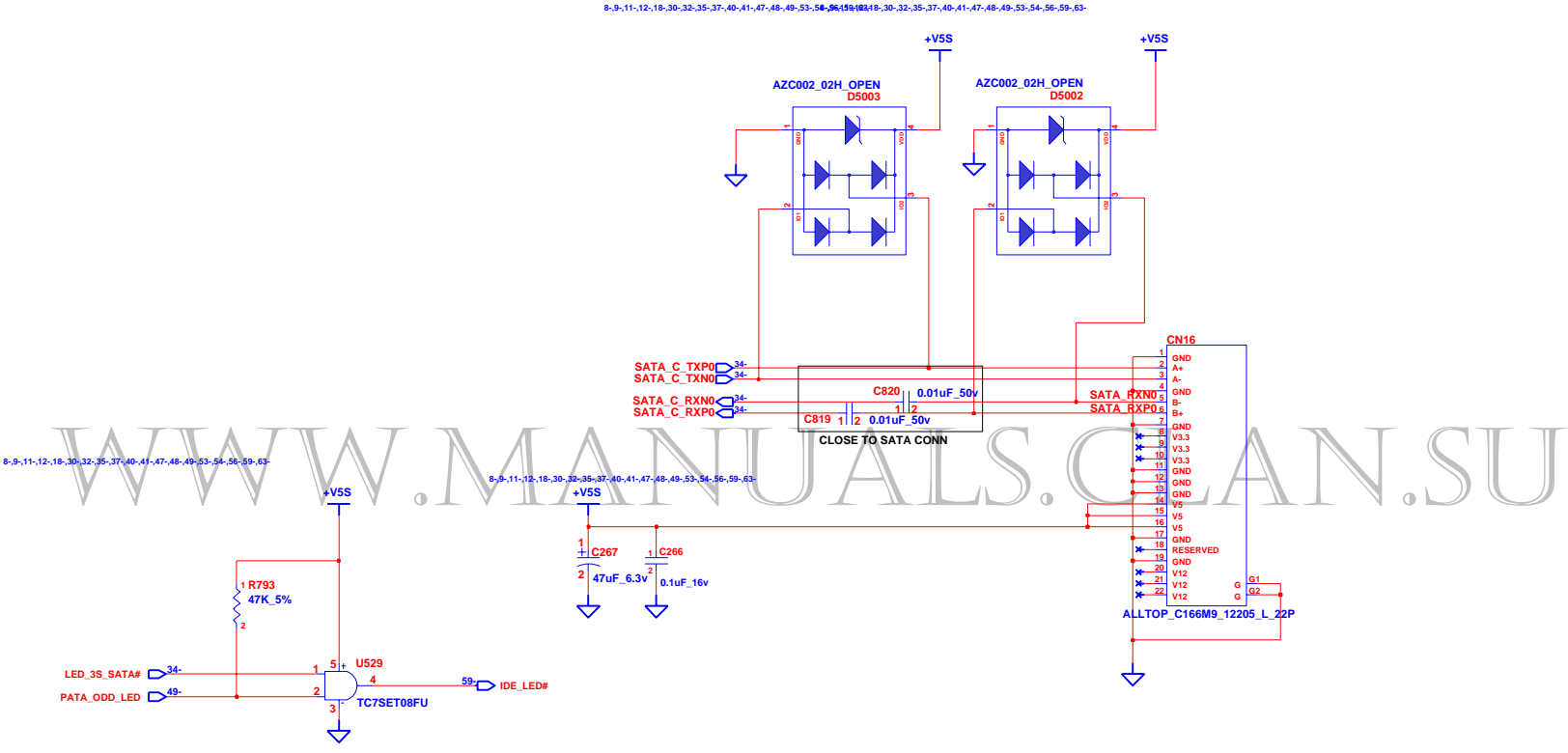


Wireless & Debug card



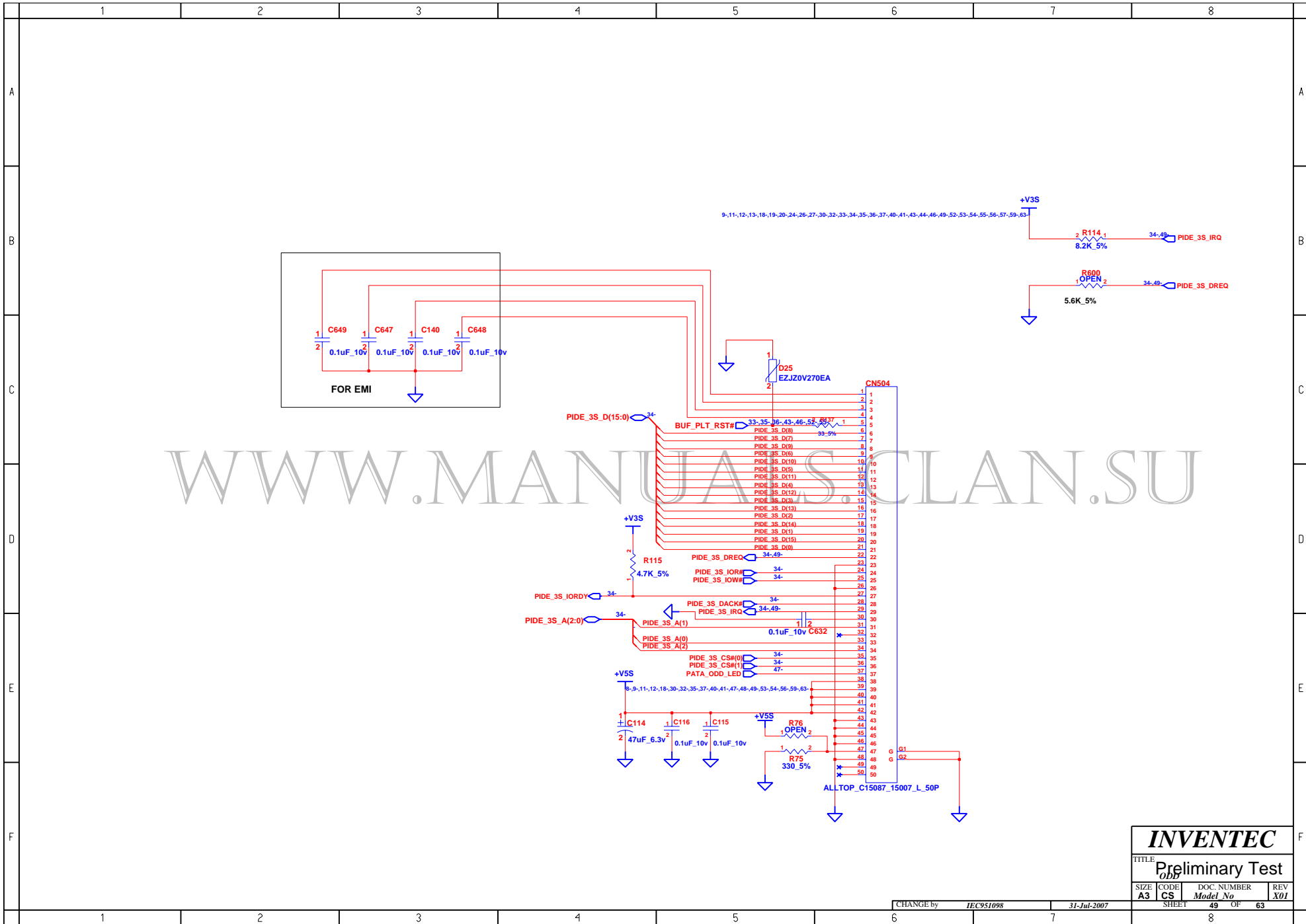
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| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| <i>Robson & WLAN</i> | | | |
| SIZE A3 | CODE CS | DOC. NUMBER <i>Model No</i> | REV X01 |
| CHANGE by <i>IEC951098</i> | | DATE <i>4-Dec-2007</i> | |
| SHEET 46 | | OF 63 | |

SATA HDD01



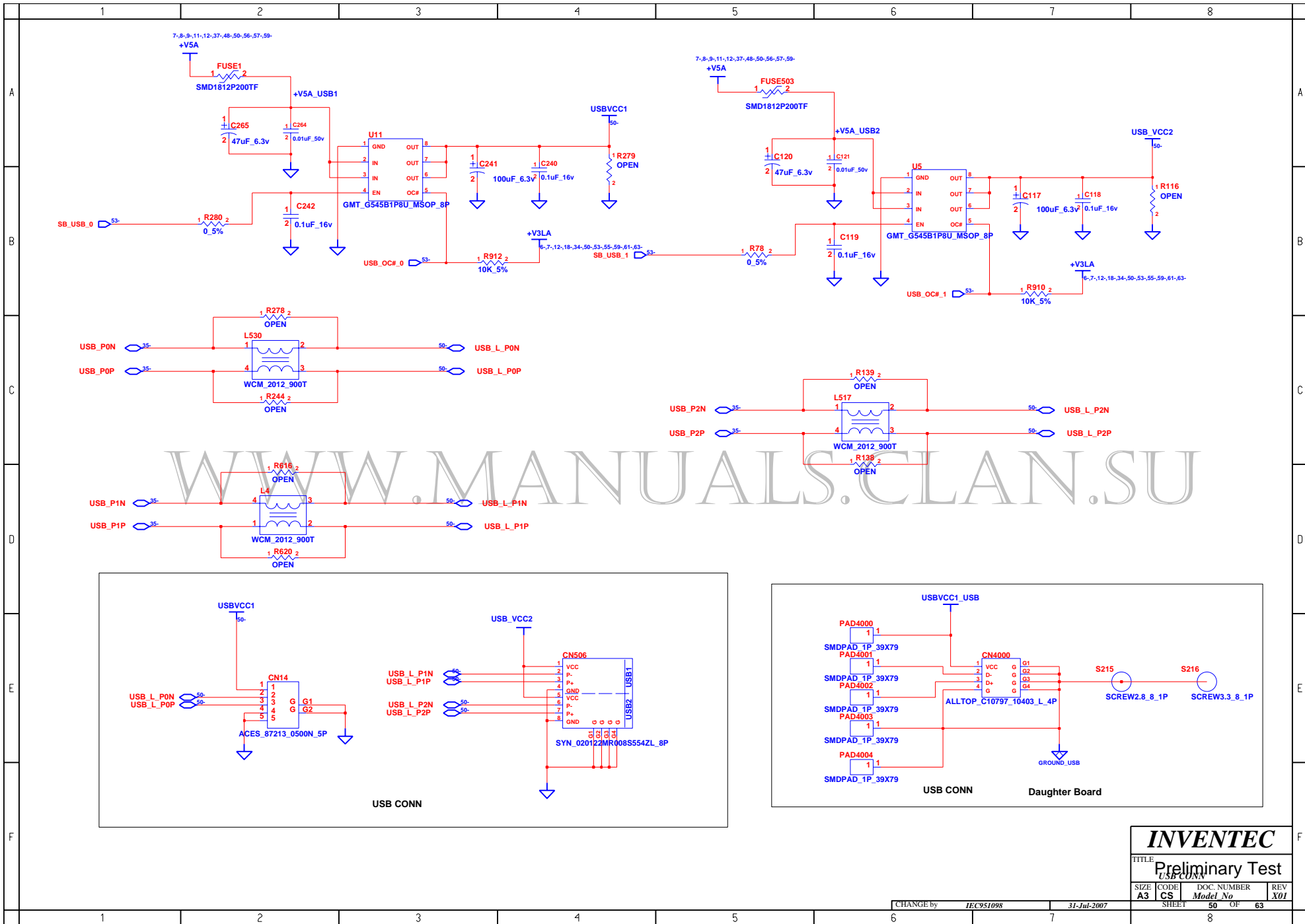
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| INVENTEC | | | |
| TITLE SATA HDD CONN | | | |
| Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |

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| CHANGE by | IEC951098 | 30-Nov-2007 | SHEET | 47 | OF | 63 |
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| INVENTEC | | | |
| TITLE <i>Obp</i> Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER <i>Model No</i> | REV X01 |
| CHANGE by IEC951098 | | 31-Jul-2007 | |
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INVENTEC

TITLE
Preliminary Test
USB CONN

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| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |

CHANGE by IEC951098

31-Jul-2007

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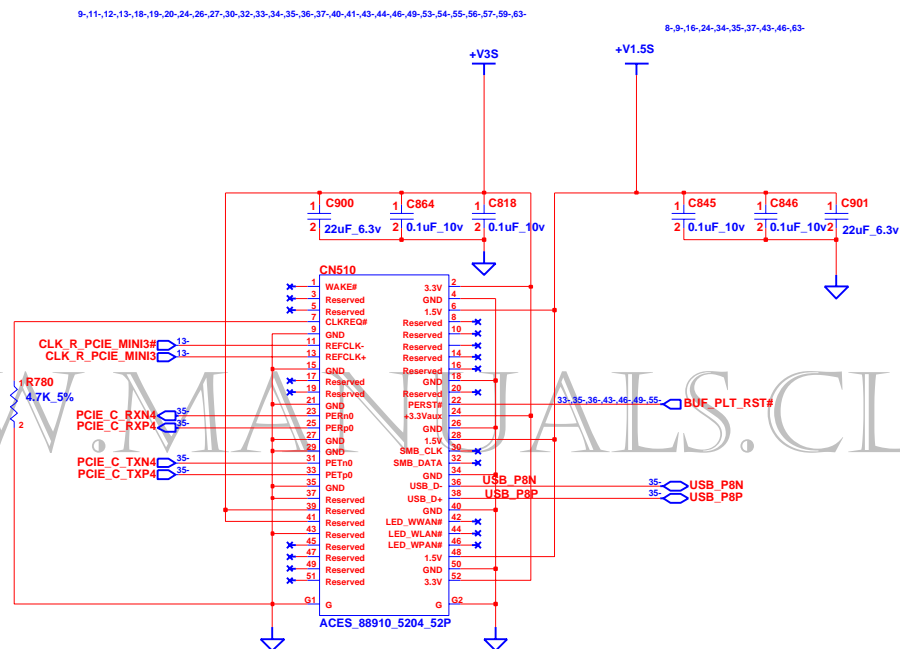
BLANK

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| INVENTEC | | | |
| TITLE | | | |
| Preliminary Test | | | |
| BLUETOOTH | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |
| SHEET | | 51 | OF 63 |

CHANGE by IEC951098 30-Nov-2007

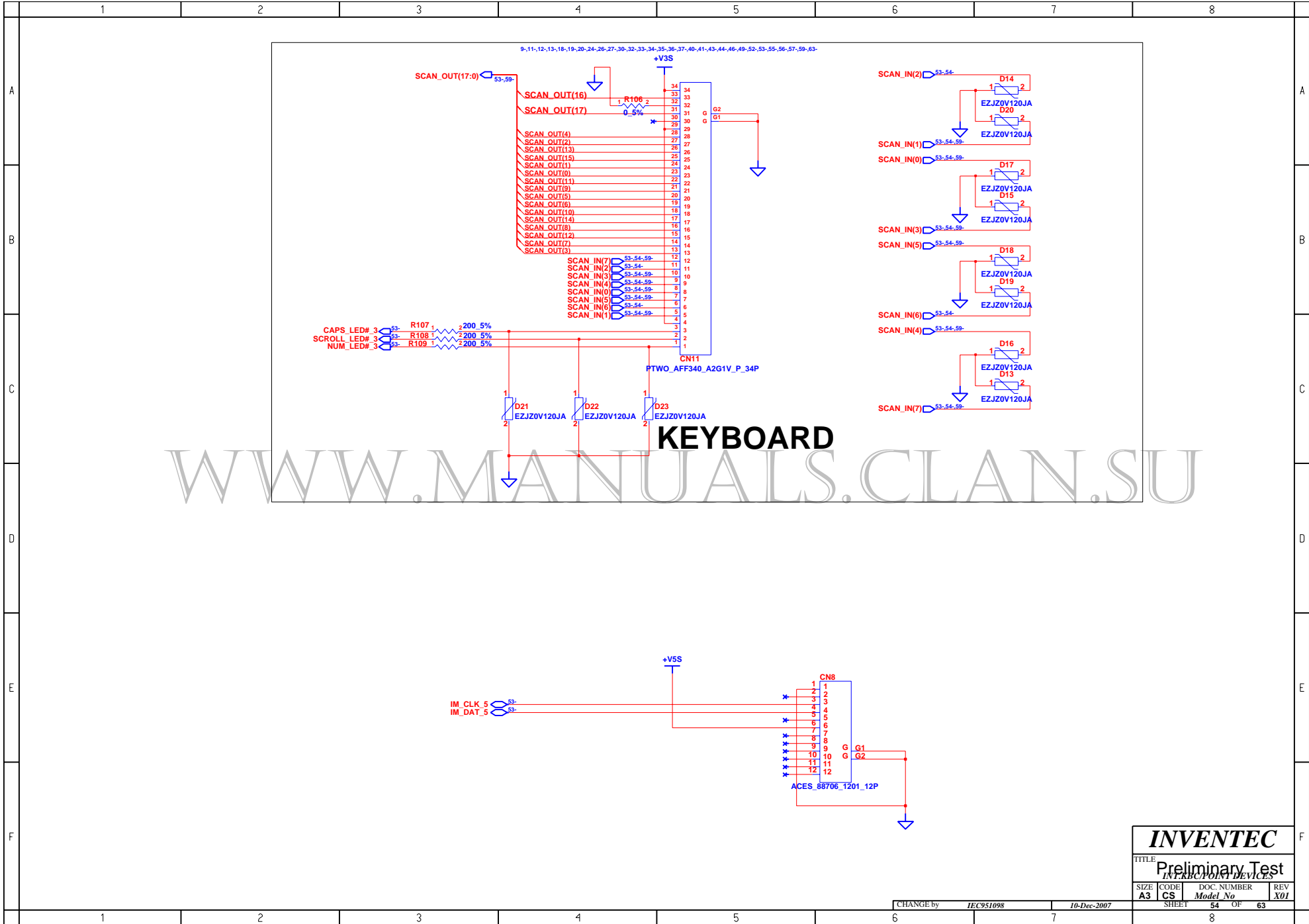
SHEET 51 OF 63

HD_DVD



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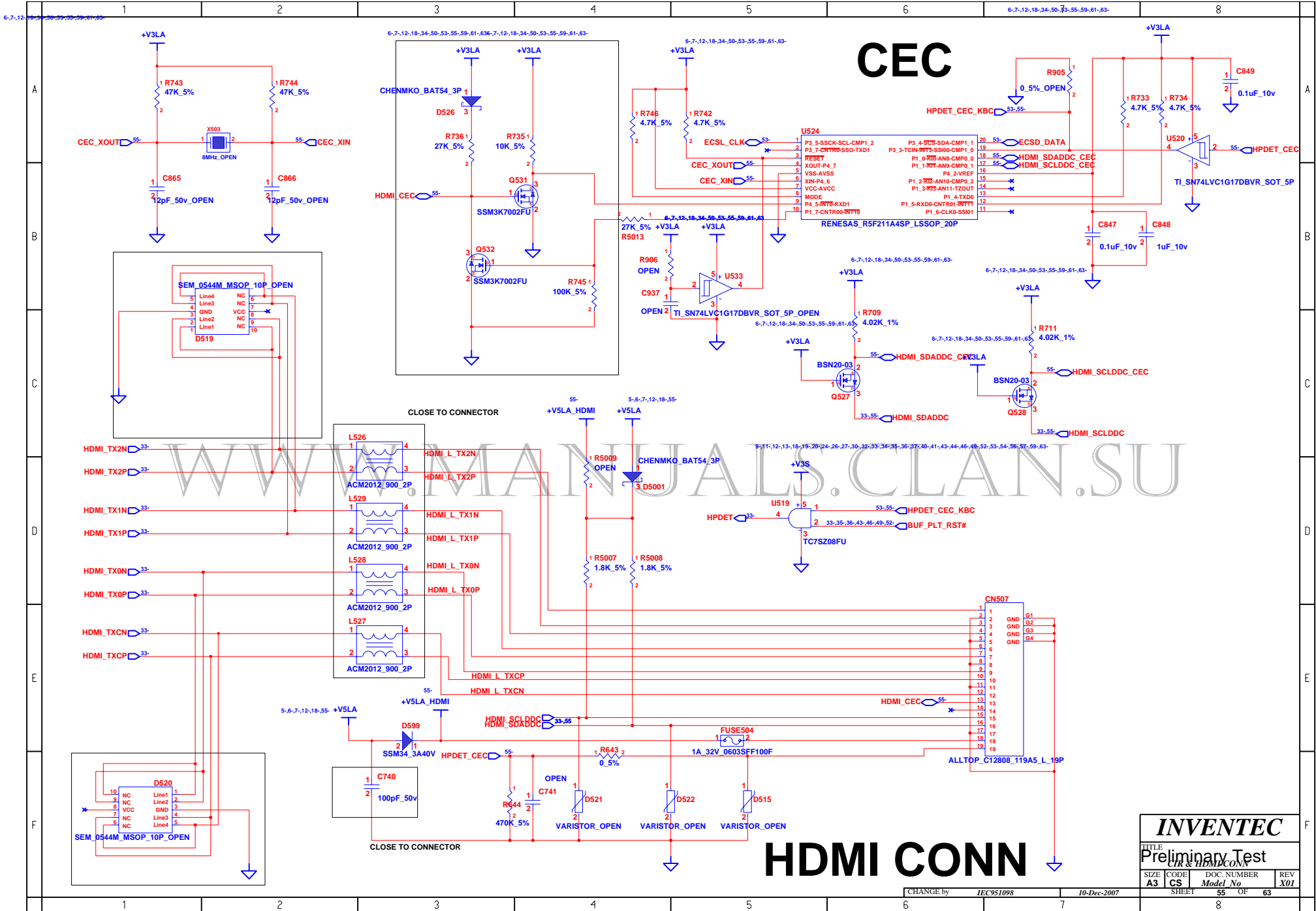
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| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| 3G_S1M | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
| CHANGE by IEC951098 | | 27-Dec-2007 | SHEET 52 OF 63 |



INVENTEC
 TITLE Preliminary Test
 INT.RBC POINT DEVICES

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|-------------------|-------------------|-------------------------|-------------------|
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
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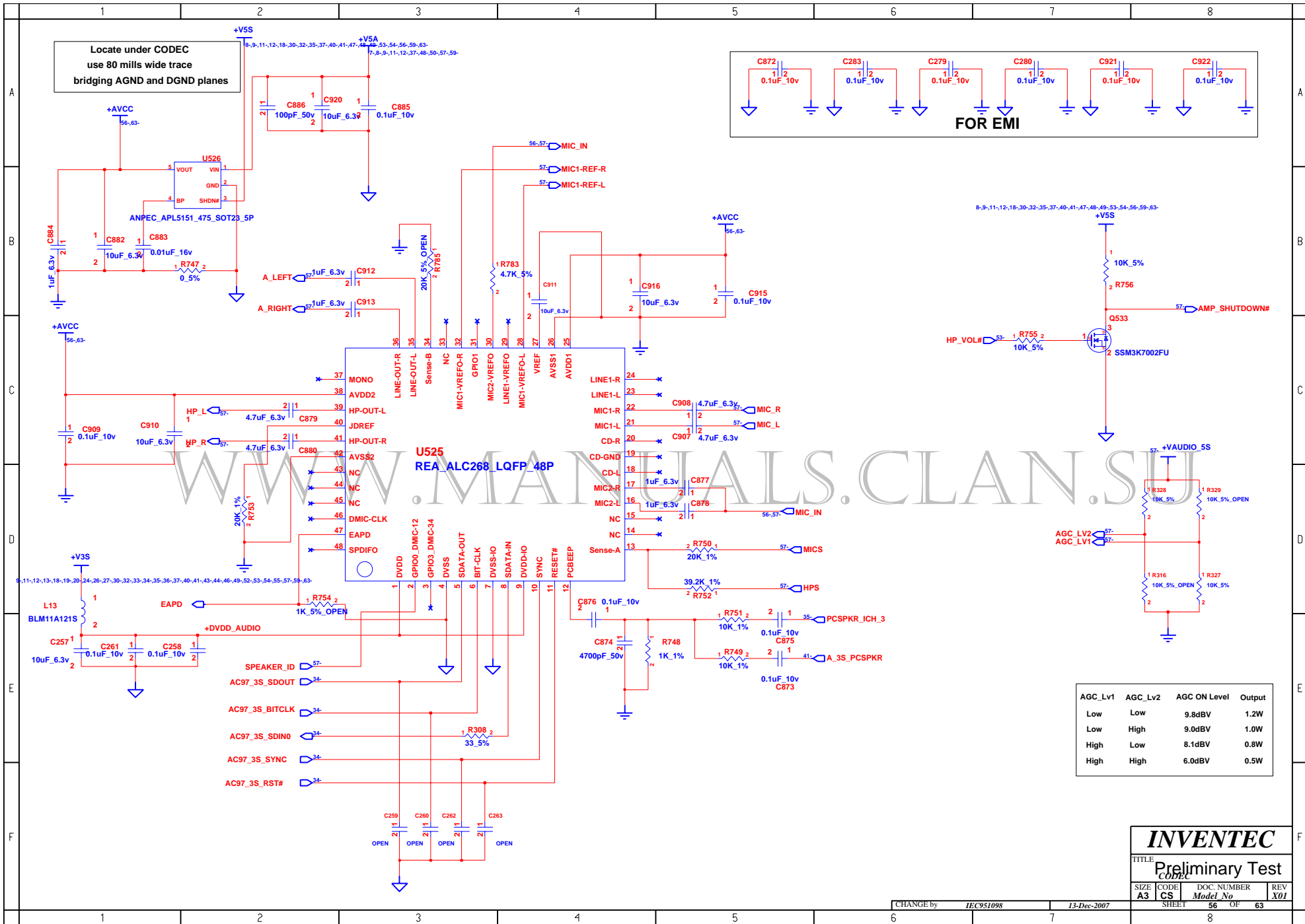
CHANGE by IEC951098 10-Dec-2007 SHEET 54 OF 63



CEC

HDMI CONN

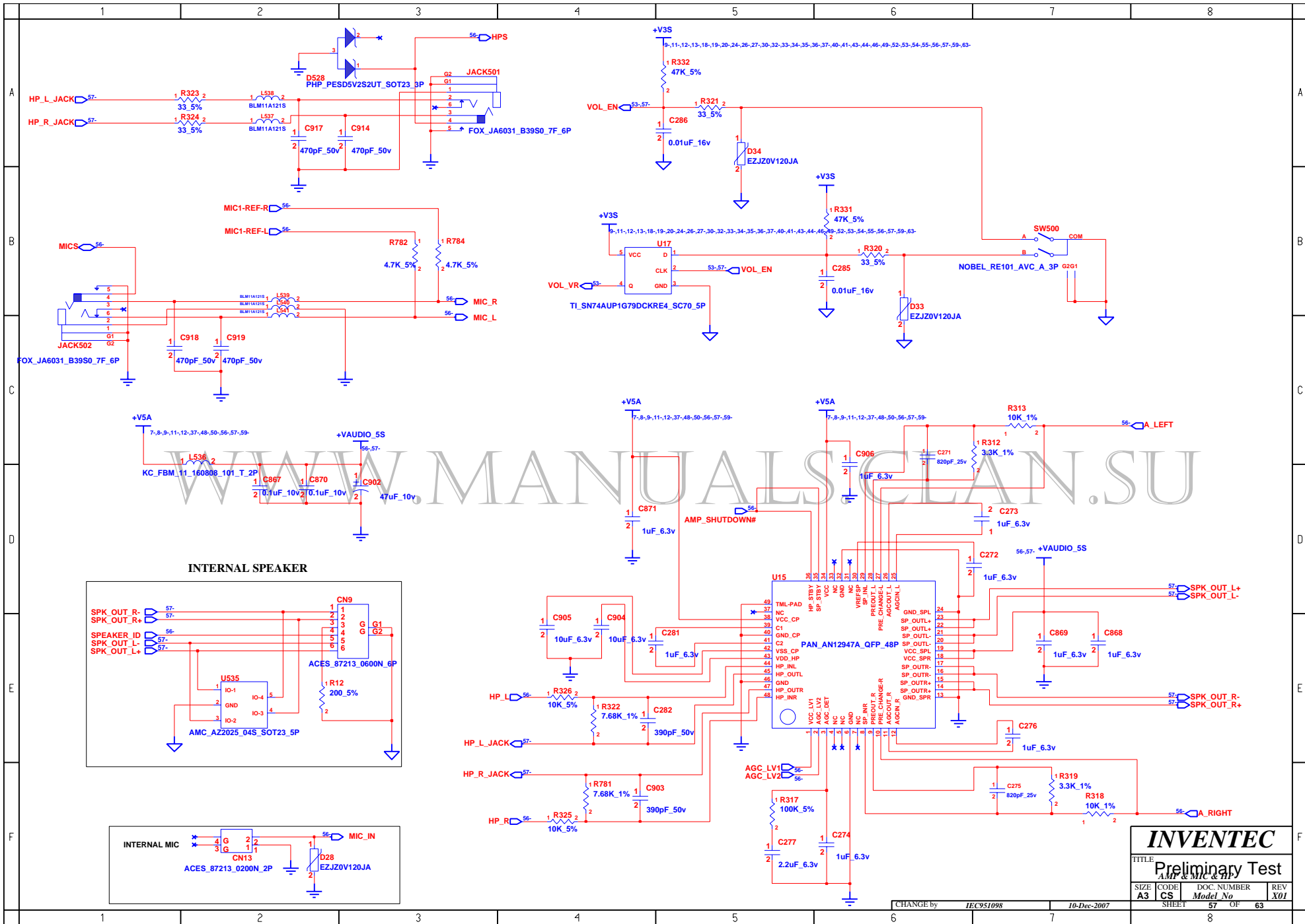
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| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| CR & HBM CONN | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |
| CHANGE by | | IEC951098 | 10-Dec-2007 |
| SHEET | | 55 | OF 63 |



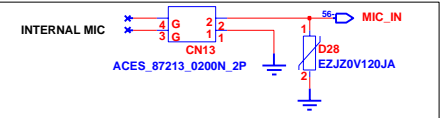
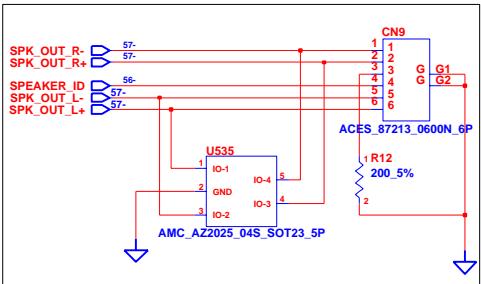
INVENTEC

TITLE
Preliminary Test

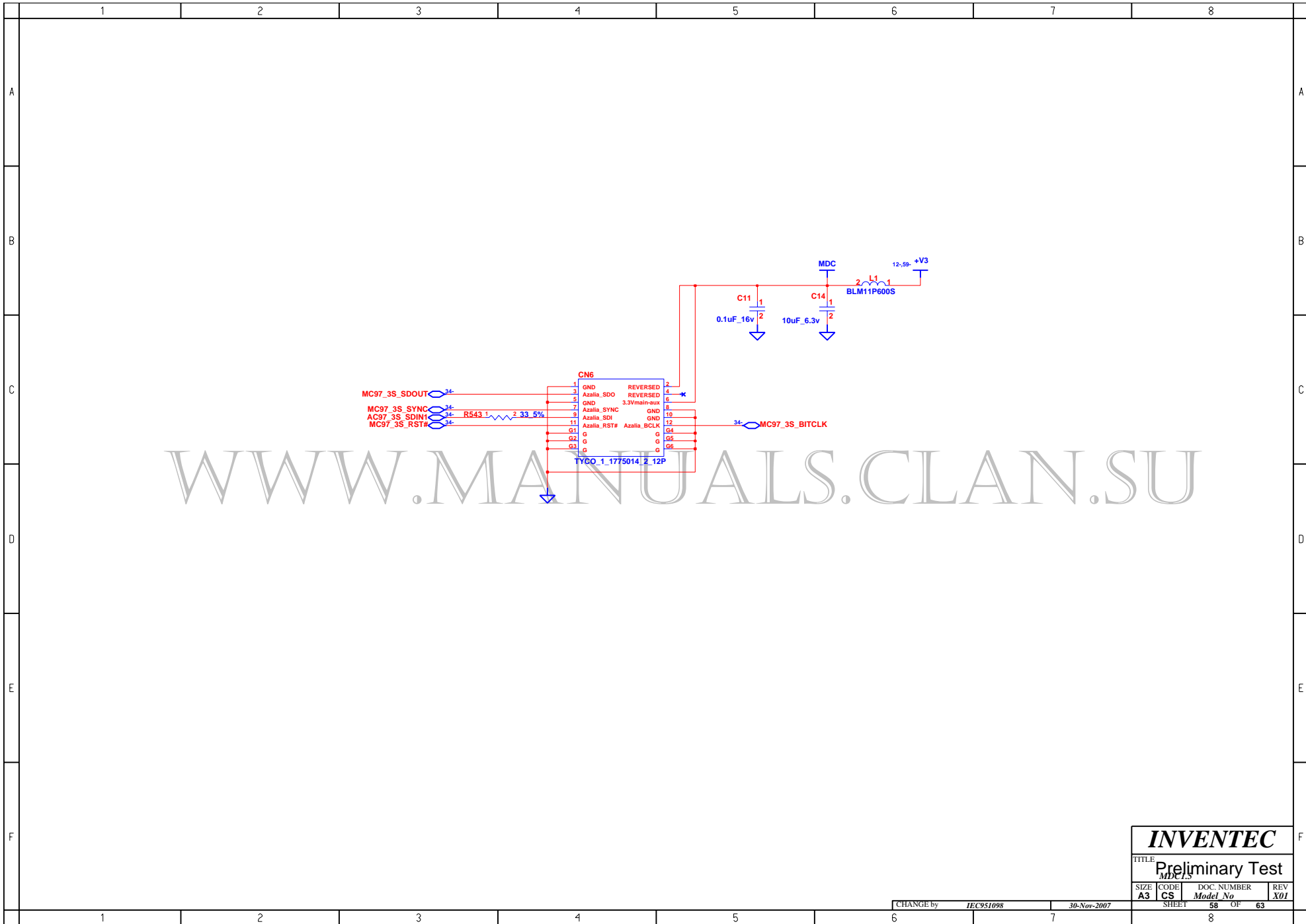
SIZE CODE DOC. NUMBER REV
A3 CS Model No X01



INTERNAL SPEAKER

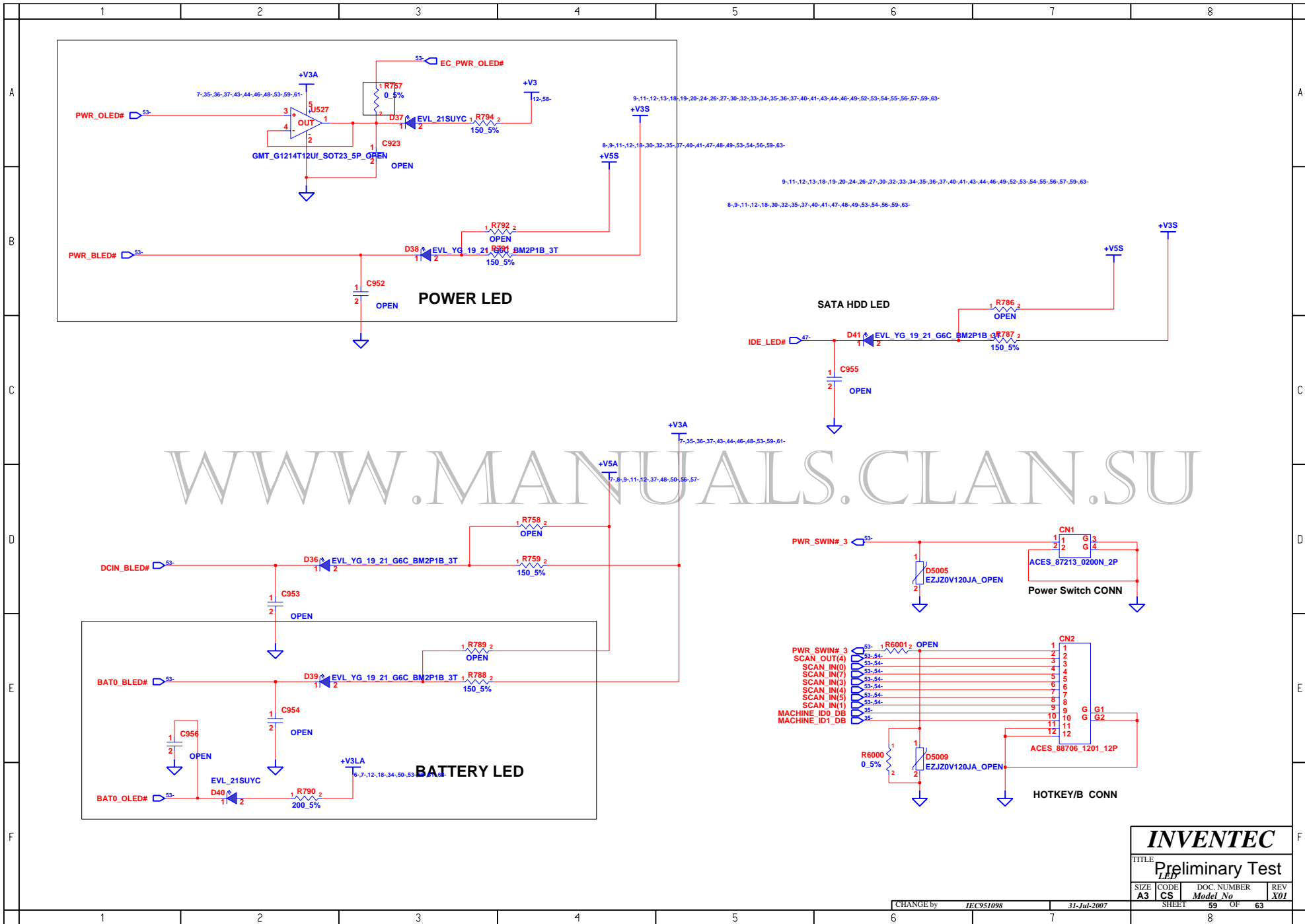


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| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
| CHANGE by IEC951098 | | 10-Dec-2007 | |
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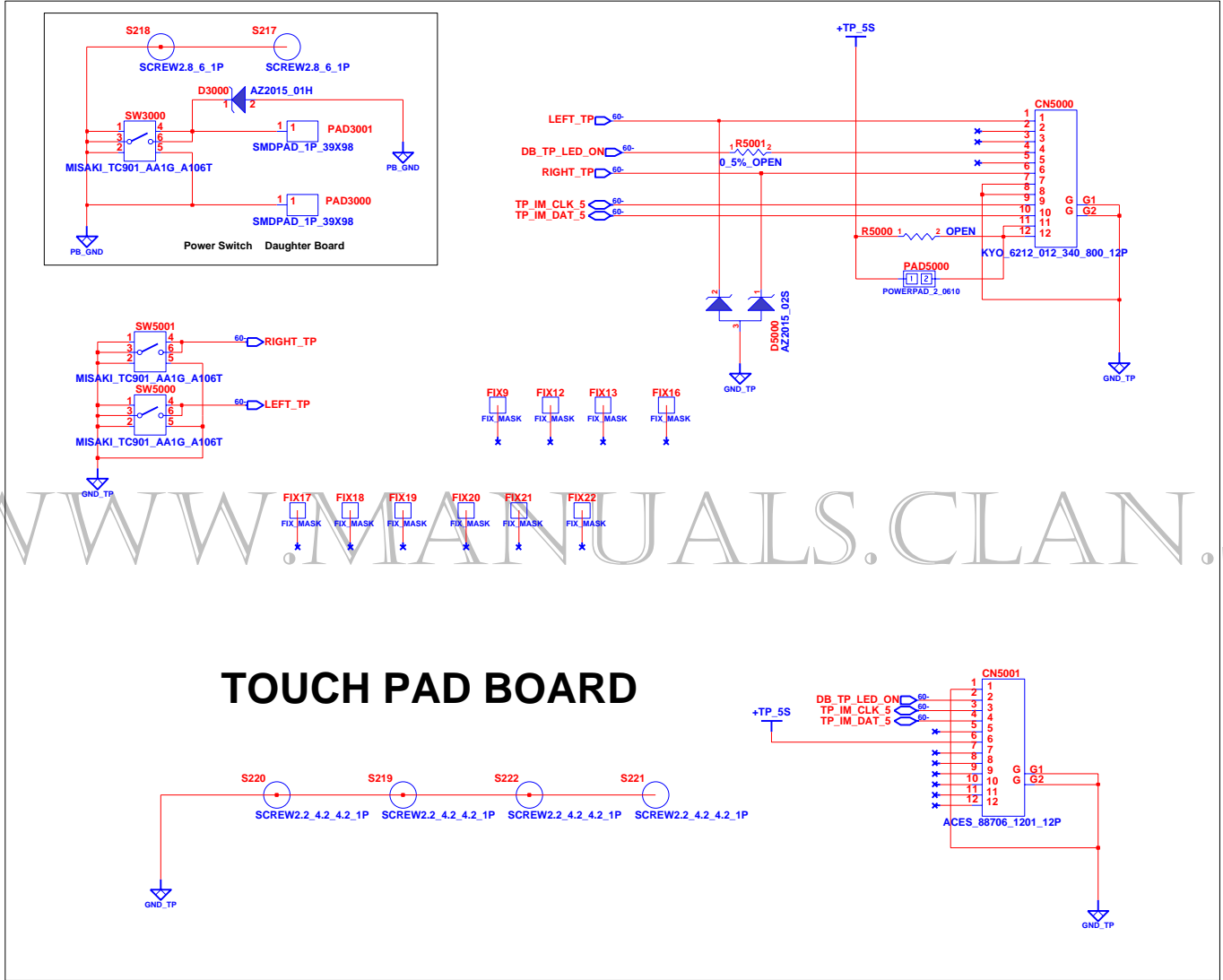


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| INVENTEC | | | |
| TITLE | | | |
| Preliminary Test | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |
| CHANGE by | | SHEET | |
| IEC951098 | | 58 OF 63 | |
| 30-Nov-2007 | | 8 | |



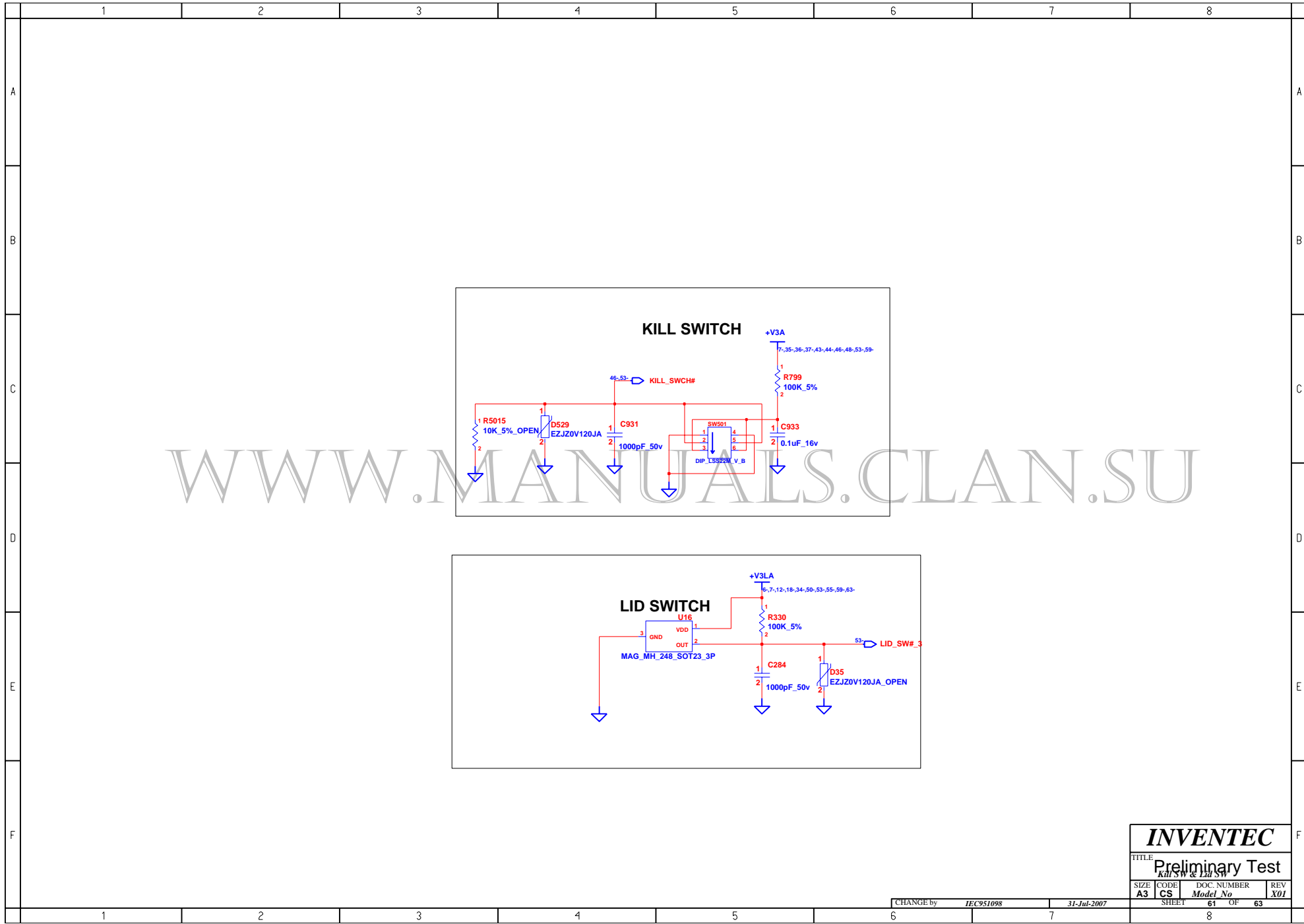
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| INVENTEC | | | |
| TITLE Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER <i>Model No</i> | REV X01 |
| CHANGE by <i>IEC951098</i> | | 31-Jul-2007 | |
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TOUCH PAD BOARD

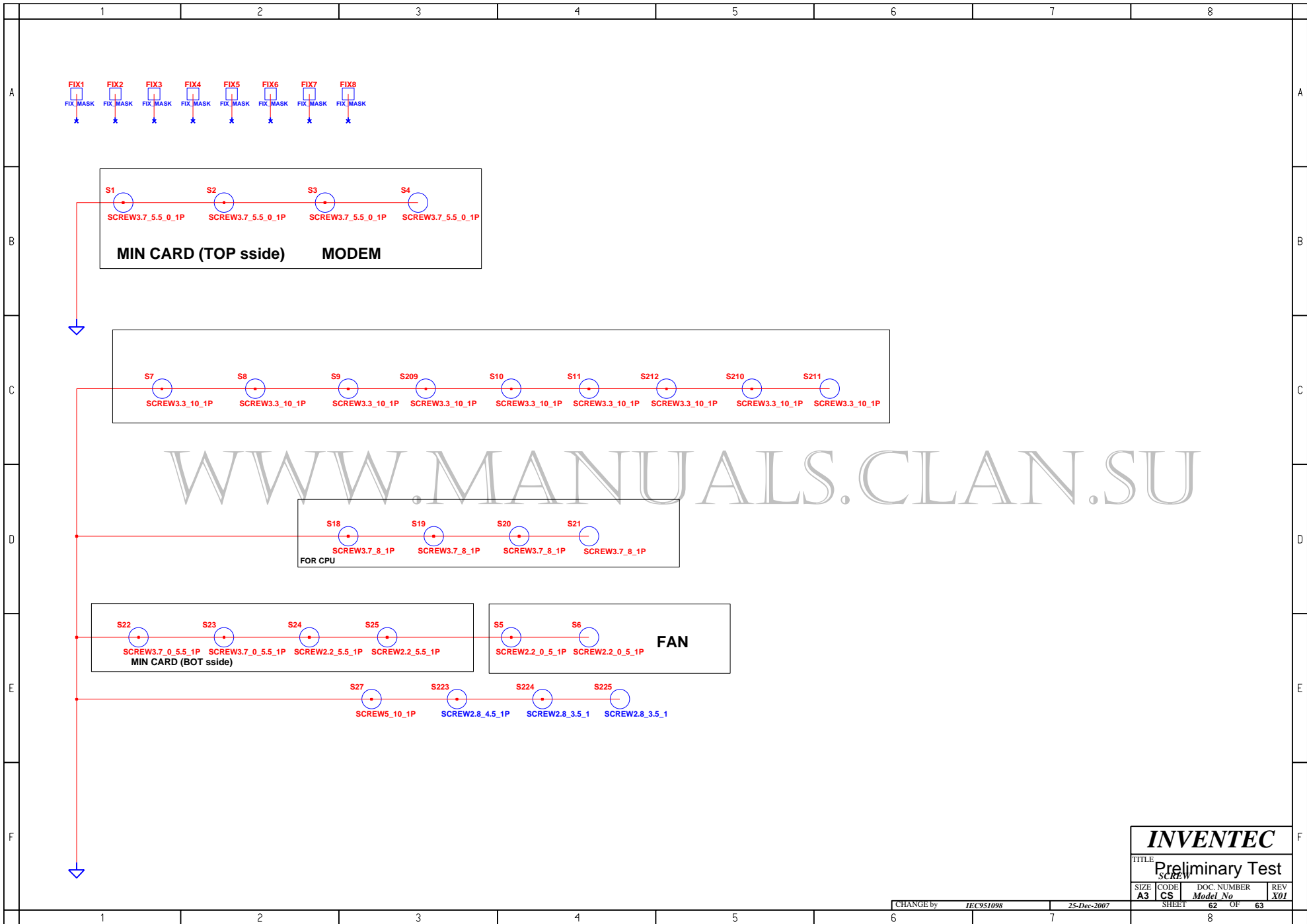
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| INVENTEC | | | |
| TITLE Touch Pad Board | | | |
| Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
| CHANGE by <i>IEC951098</i> | | SHEET 60 OF 63 | |



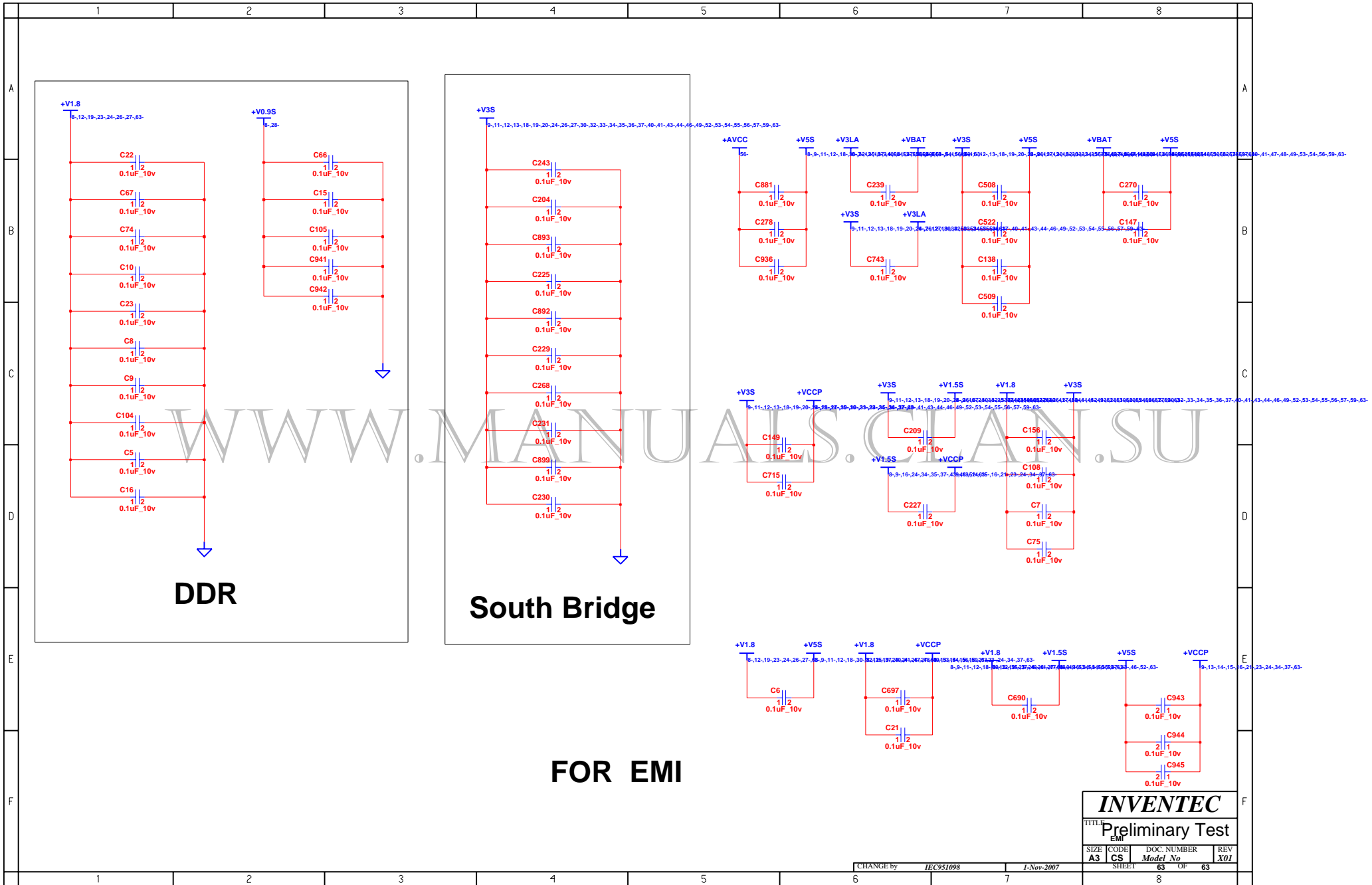
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|---------------------------|------------|-------------------------|------------|
| INVENTEC | | | |
| TITLE Kill Sw & Lid Sw | | | |
| Preliminary Test | | | |
| SIZE A3 | CODE CS | DOC. NUMBER Model No | REV X01 |
| CHANGE by IEC951098 | | 31-Jul-2007 | |
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| INVENTEC | | | |
| TITLE | | | |
| Preliminary Test | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |
| CHANGE by | | SHEET | |
| IEC951098 | | 62 OF 63 | |
| 25-Dec-2007 | | 8 | |



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|-------------------------|------|-------------|-----|
| INVENTEC | | | |
| TITLE: Preliminary Test | | | |
| SIZE | CODE | DOC. NUMBER | REV |
| A3 | CS | Model No | X01 |
| CHANGE by IECYS1098 | | L-Nov-2007 | |
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